

# NCV8535

## Ultra High Accuracy, Low Iq, 500 mA Low Dropout Regulator with Enable

The NCV8535 is a high performance, low dropout regulator. With accuracy of  $\pm 0.9\%$  over line and load and ultra-low quiescent current and noise it encompasses all of the necessary features required by today's consumer electronics. This unique device is guaranteed to be stable without a minimum load current requirement and stable with any type of capacitor as small as 1.0  $\mu\text{F}$ . The NCV8535 also comes equipped with sense and noise reduction pins to increase the overall utility of the device. The NCV8535 offers reverse bias protection.

### Features

- High Accuracy Over Line and Load ( $\pm 0.9\%$  at  $25^\circ\text{C}$ )
- Ultra-Low Dropout Voltage at Full Load (260 mV typ.)
- No Minimum Output Current Required for Stability
- Low Noise (31  $\mu\text{Vrms}$  w/10 nF  $C_{\text{nr}}$  and 51  $\mu\text{Vrms}$  w/out  $C_{\text{nr}}$ )
- Low Shutdown Current (0.07  $\mu\text{A}$ )
- Reverse Bias Protected
- 2.6 V to 12 V Supply Range
- Thermal Shutdown Protection
- Current Limitation
- Requires Only 1.0  $\mu\text{F}$  Output Capacitance for Stability
- Stable with Any Type of Capacitor (including MLCC)
- Available in 1.5 V, 1.8 V, 2.5 V, 2.8 V, 2.85 V, 3.0 V, 3.3 V, 5.0 V and Adjustable Output Voltages
- These are Pb-Free Devices

### Applications

- PCMCIA Card
- Cellular Phones
- Camcoders and Cameras
- Networking Systems, DSL/Cable Modems
- Cable Set-Top Box
- MP3/CD Players
- DSP Supply
- Displays and Monitors



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DFN10, 3x3  
MN SUFFIX  
CASE 485C

### MARKING DIAGRAM

Fixed Version	Pin 1, 2. $V_{\text{out}}$		Adj Version	Pin 1, 2. $V_{\text{out}}$	
	3. Sense			3. Adj	
Pin 1, 2. $V_{\text{out}}$	4. GND		Pin 1, 2. $V_{\text{out}}$	4. GND	
	5, 6. NC			5, 6. NC	
	7. NR			7. NR	
	8. $\overline{\text{SD}}$			8. $\overline{\text{SD}}$	
9, 10. $V_{\text{in}}$			9, 10. $V_{\text{in}}$		

V8535 = Specific Device Code

xxx = ADJ, 150, 180, 250, 280, 285, 300, 330, 350, 500

A = Assembly Location

L = Wafer Lot

Y = Year

W = Work Week

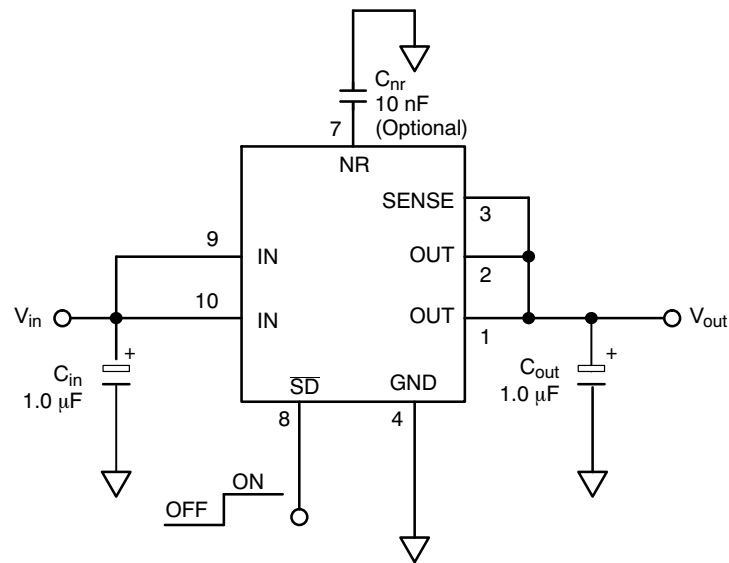
■ = Pb-Free Package

(Note: Microdot may be in either location)

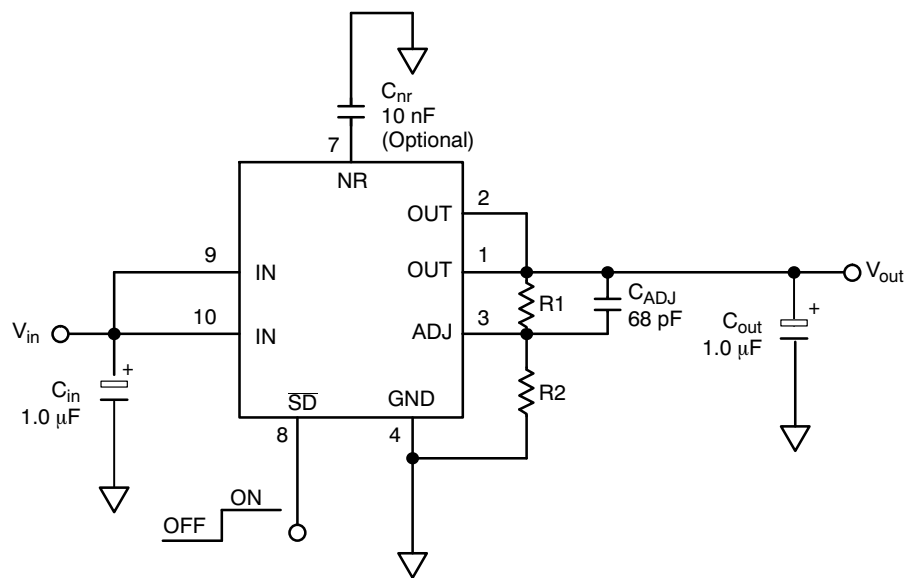
### ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 19 of this data sheet.

# NCV8535



**Figure 1. Typical Fixed Version Application Schematic**



**Figure 2. Typical Adjustable Version Application Schematic**

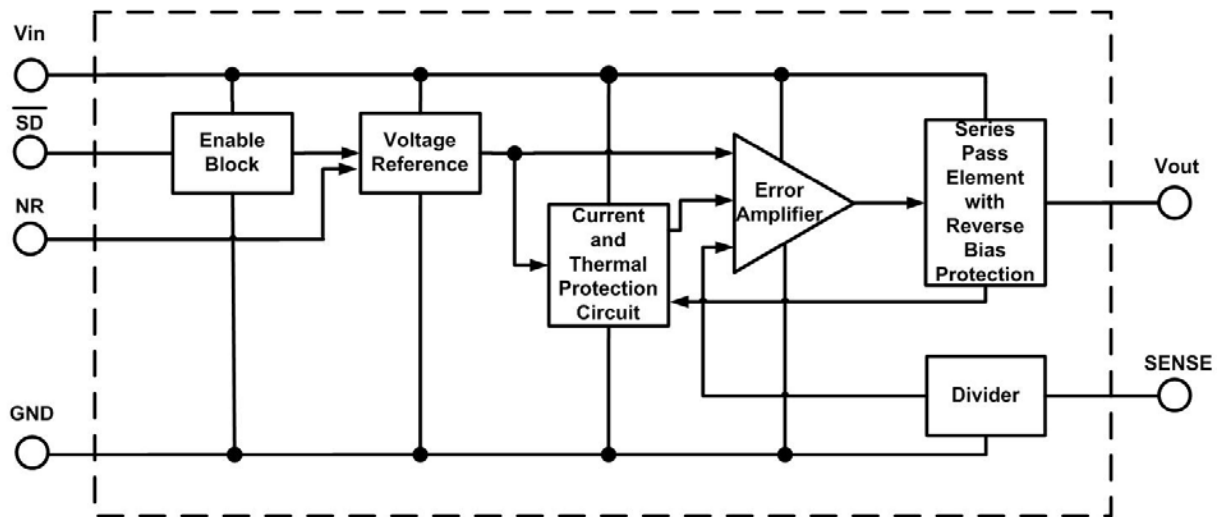


Figure 3. Block Diagram, Fixed Output Version

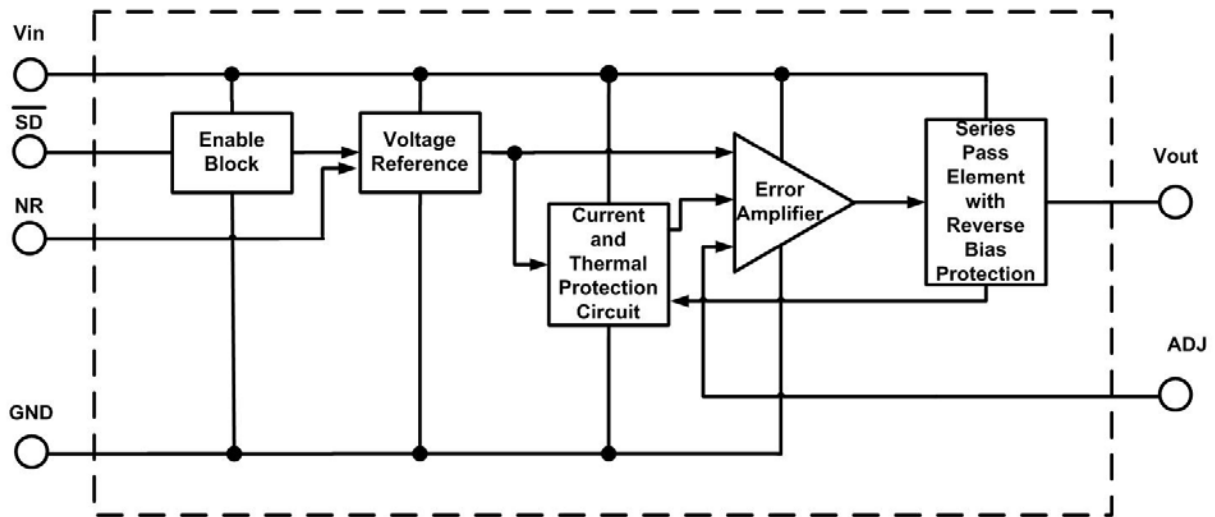


Figure 4. Block Diagram, Adjustable Output Version

## PIN FUNCTION DESCRIPTION

### Fixed Version

Pin No.	Pin Name	Description
1, 2	V <sub>out</sub>	Regulated output voltage. Bypass to ground with C <sub>out</sub> ≥ 1.0 μF.
3	SENSE	For output voltage sensing, connect to Pins 1 and 2.
4	GND	Power Supply Ground
7	NR	Noise Reduction Pin. This is an optional pin used to further reduce noise.
8	SD	Shutdown pin. When not in use, this pin should be connected to the input pin.
9, 10	V <sub>in</sub>	Power Supply Input Voltage
5, 6	NC	Not Connected
EPAD	EPAD	Exposed thermal pad should be connected to ground.

### Adjustable Version

1, 2	V <sub>out</sub>	Regulated output voltage. Bypass to ground with C <sub>out</sub> ≥ 1.0 μF.
3	Adj	Adjustable pin; reference voltage = 1.25 V.
4	GND	Power Supply Ground
7	NR	Noise Reduction Pin. This is an optional pin used to further reduce noise.
8	SD	Shutdown pin. When not in use, this pin should be connected to the input pin.
9, 10	V <sub>in</sub>	Power Supply Input Voltage
5, 6	NC	Not Connected
EPAD	EPAD	Exposed thermal pad should be connected to ground.

## MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage	V <sub>in</sub>	-0.3 to +16	V
Output Voltage	V <sub>out</sub>	-0.3 to V <sub>in</sub> + 0.3 or 10 V*	V
Shutdown Pin Voltage	V <sub>sh</sub>	-0.3 to +16	V
Junction Temperature Range	T <sub>J</sub>	-40 to +150	°C
Storage Temperature Range	T <sub>stg</sub>	-50 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

NOTE: This device series contains ESD protection and exceeds the following tests:

Human Body Model (HBM) tested per AEC-Q100-002 (EIA/JESD22-A114)

Machine Model (MM) tested per AEC-Q100-003 (EIA/JESD22-A115)

Charged Device Model (CDM) tested per EIA/JESD22-C101

\*Which ever is less. Reverse bias protection feature valid only if V<sub>out</sub> - V<sub>in</sub> ≤ 7 V.

## THERMAL CHARACTERISTICS

Characteristic	Test Conditions (Typical Value)		Unit
	Min Pad Board (Note 1)	1" Pad Board (Note 1)	
Junction-to-Air, θJA	215	66	°C/W
Junction-to-Pin, ψJL2	55	17	°C/W

1. As mounted on a 35 x 35 x 1.5 mm FR4 Substrate, with a single layer of a specified copper area of 2 oz (0.07 mm thick) copper traces and heat spreading area. JEDEC 51 specifications for a low and high conductivity test board recommend a 2 oz copper thickness. Test conditions are under natural convection or zero air flow.

**ELECTRICAL CHARACTERISTICS – 5.0 V** ( $V_{out} = 5.0$  V typical,  $V_{in} = 5.4$  V,  $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ , unless otherwise noted, Note 2.)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 5.4$ V to $9.0$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = 25^{\circ}\text{C}$	$V_{out}$	-0.9% 4.955	5.0	+0.9% 5.045	V
Output Voltage (Accuracy) $V_{in} = 5.4$ V to $9.0$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	$V_{out}$	-1.4% 4.930	5.0	+1.4% 5.070	V
Output Voltage (Accuracy) $V_{in} = 5.4$ V to $9.0$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	$V_{out}$	-1.5% 4.925	5.0	+1.5% 5.075	V
Line Regulation $V_{in} = 5.4$ V to $12$ V, $I_{load} = 0.1$ mA	$Line_{Reg}$		0.04		mV/V
Load Regulation $V_{in} = 5.4$ V, $I_{load} = 0.1$ mA to $500$ mA	$Load_{Reg}$		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500$ mA $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA	$V_{DO}$			340 230 110 10	mV
Peak Output Current (See Figure 16)	$I_{pk}$	500	700	830	mA
Short Output Current (See Figure 16)	$I_{sc}$			930	mA
Thermal Shutdown	$T_J$		160		$^{\circ}\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 3) $I_{load} = 300$ mA (Note 3) $I_{load} = 50$ mA $I_{load} = 0.1$ mA  In Dropout $V_{in} = 4.9$ V, $I_{load} = 0.1$ mA  In Shutdown $S_D = 0$ V	$I_{GND}$        $I_{GNDsh}$		9.0 4.6 0.8 –  –  0.07	14 7.5 2.5 190  500  1.0	mA        $\mu\text{A}$
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to $100$ kHz, $C_{out} = 10$ $\mu\text{F}$ $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to $100$ kHz, $C_{out} = 10$ $\mu\text{F}$	$V_{noise}$		93 58		$\mu\text{V}_{rms}$ $\mu\text{V}_{rms}$
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
$S_D$ Input Current, $V_{SD} = 0$ V to $0.4$ V or $V_{SD} = 2.0$ V to $V_{in}$	$I_{SD}$		0.07	1.0	$\mu\text{A}$
Output Current In Shutdown Mode, $V_{out} = 0$ V	$I_{OSD}$		0.07	1.0	$\mu\text{A}$
Reverse Bias Protection, Current Flowing from the Output Pin to GND ( $V_{in} = 0$ V, $V_{out\_forced} = 5.0$ V)	$I_{OUTR}$		10		$\mu\text{A}$

- Performance guaranteed over the operating temperature range by design and/or characterization, production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.
- $T_A$  must be greater than  $0^{\circ}\text{C}$ .

**ELECTRICAL CHARACTERISTICS – 3.3 V** ( $V_{out} = 3.3\text{ V}$  typical,  $V_{in} = 3.7\text{ V}$ ,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ , unless otherwise noted, Note 4.)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 3.7\text{ V}$ to $7.3\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = 25^\circ\text{C}$	$V_{out}$	-0.9% 3.270	3.3	+0.9% 3.330	V
Output Voltage (Accuracy) $V_{in} = 3.7\text{ V}$ to $7.3\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = 0^\circ\text{C}$ to $+85^\circ\text{C}$	$V_{out}$	-1.4% 3.254	3.3	+1.4% 3.346	V
Output Voltage (Accuracy) $V_{in} = 3.7\text{ V}$ to $7.3\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	$V_{out}$	-1.5% 3.250	3.3	+1.5% 3.350	V
Line Regulation $V_{in} = 3.7\text{ V}$ to $12\text{ V}$ , $I_{load} = 0.1\text{ mA}$	$Line_{Reg}$		0.04		mV/V
Load Regulation $V_{in} = 3.7\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$	$Load_{Reg}$		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500\text{ mA}$ $I_{load} = 300\text{ mA}$ $I_{load} = 50\text{ mA}$ $I_{load} = 0.1\text{ mA}$	$V_{DO}$			340 230 110 10	mV
Peak Output Current (See Figure 16)	$I_{pk}$	500	700	800	mA
Short Output Current (See Figure 16)	$I_{sc}$			900	mA
Thermal Shutdown	$T_J$		160		$^\circ\text{C}$
Ground Current In Regulation $I_{load} = 500\text{ mA}$ (Note 5) $I_{load} = 300\text{ mA}$ $I_{load} = 50\text{ mA}$ $I_{load} = 0.1\text{ mA}$  In Dropout $V_{in} = 3.2\text{ V}$ , $I_{load} = 0.1\text{ mA}$  In Shutdown $S_D = 0\text{ V}$	$I_{GND}$        $I_{GNDsh}$		9.0 4.6 0.8 –  –  0.07	14 7.5 2.5 190  500  1.0	mA        $\mu\text{A}$
Output Noise $C_{nr} = 0\text{ nF}$ , $I_{load} = 500\text{ mA}$ , $f = 10\text{ Hz}$ to $100\text{ kHz}$ , $C_{out} = 10\text{ }\mu\text{F}$ $C_{nr} = 10\text{ nF}$ , $I_{load} = 500\text{ mA}$ , $f = 10\text{ Hz}$ to $100\text{ kHz}$ , $C_{out} = 10\text{ }\mu\text{F}$	$V_{noise}$		69 46		$\mu\text{Vrms}$ $\mu\text{Vrms}$
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
$S_D$ Input Current, $V_{SD} = 0\text{ V}$ to $0.4\text{ V}$ or $V_{SD} = 2.0\text{ V}$ to $V_{in}$	$I_{SD}$		0.07	1.0	$\mu\text{A}$
Output Current In Shutdown Mode, $V_{out} = 0\text{ V}$	$I_{OSD}$		0.07	1.0	$\mu\text{A}$
Reverse Bias Protection, Current Flowing from the Output Pin to GND ( $V_{in} = 0\text{ V}$ , $V_{out\_forced} = 3.3\text{ V}$ )	$I_{OUTR}$		10		$\mu\text{A}$

- Performance guaranteed over the operating temperature range by design and/or characterization, production tested at  $T_J = T_A = 25^\circ\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.
- $T_A$  must be greater than  $0^\circ\text{C}$ .

**ELECTRICAL CHARACTERISTICS – 3.0 V** ( $V_{out} = 3.0$  V typical,  $V_{in} = 3.4$  V,  $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ , unless otherwise noted, Note 6.)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 3.4$ V to $7.0$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = 25^{\circ}\text{C}$	$V_{out}$	-0.9% 2.973	3.0	+0.9% 3.027	V
Output Voltage (Accuracy) $V_{in} = 3.4$ V to $7.0$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	$V_{out}$	-1.4% 2.958	3.0	+1.4% 3.042	V
Output Voltage (Accuracy) $V_{in} = 3.4$ V to $7.0$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	$V_{out}$	-1.5% 2.955	3.0	+1.5% 3.045	V
Line Regulation $V_{in} = 3.4$ V to $12$ V, $I_{load} = 0.1$ mA	$Line_{Reg}$		0.04		mV/V
Load Regulation $V_{in} = 3.4$ V, $I_{load} = 0.1$ mA to $500$ mA	$Load_{Reg}$		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500$ mA $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA	$V_{DO}$			340 230 110 10	mV
Peak Output Current (See Figure 16)	$I_{pk}$	500	700	800	mA
Short Output Current (See Figure 16)	$I_{sc}$			900	mA
Thermal Shutdown	$T_J$		160		$^{\circ}\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 7) $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA  In Dropout $V_{in} = 2.9$ V, $I_{load} = 0.1$ mA  In Shutdown $S_D = 0$ V	$I_{GND}$        $I_{GNDsh}$		9.0 4.6 0.8 –  –  0.07	14 7.5 2.5 190  500  1.0	mA        $\mu\text{A}$
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to $100$ kHz, $C_{out} = 10$ $\mu\text{F}$ $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to $100$ kHz, $C_{out} = 10$ $\mu\text{F}$	$V_{noise}$		56 37		$\mu\text{V}_{rms}$ $\mu\text{V}_{rms}$
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
$S_D$ Input Current, $V_{SD} = 0$ V to $0.4$ V or $V_{SD} = 2.0$ V to $V_{in}$	$I_{SD}$		0.07	1.0	$\mu\text{A}$
Output Current In Shutdown Mode, $V_{out} = 0$ V	$I_{OSD}$		0.07	1.0	$\mu\text{A}$
Reverse Bias Protection, Current Flowing from the Output Pin to GND ( $V_{in} = 0$ V, $V_{out\_forced} = 3.0$ V)	$I_{OUTR}$		10		$\mu\text{A}$

6. Performance guaranteed over the operating temperature range by design and/or characterization, production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.
7.  $T_A$  must be greater than  $0^{\circ}\text{C}$ .

**ELECTRICAL CHARACTERISTICS – 2.85 V** ( $V_{out} = 2.85$  V typical,  $V_{in} = 3.25$  V,  $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ , unless otherwise noted, Note 8)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 3.25$ V to $6.85$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = 25^{\circ}\text{C}$	$V_{out}$	-0.9% 2.824	2.85	+0.9% 2.876	V
Output Voltage (Accuracy) $V_{in} = 3.25$ V to $6.85$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	$V_{out}$	-1.4% 2.810	2.85	+1.4% 2.890	V
Output Voltage (Accuracy) (Note 9) $V_{in} = 3.25$ V to $6.85$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	$V_{out}$	-1.5% 2.807	2.85	+1.5% 2.893	V
Line Regulation $V_{in} = 3.25$ V to $12$ V, $I_{load} = 0.1$ mA	$Line_{Reg}$		0.04		mV/V
Load Regulation $V_{in} = 3.25$ V, $I_{load} = 0.1$ mA to $500$ mA	$Load_{Reg}$		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500$ mA $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA	$V_{DO}$			340 230 110 10	mV
Peak Output Current (See Figure 16)	$I_{pk}$	500	700	800	mA
Short Output Current (See Figure 16)	$I_{sc}$			900	mA
Thermal Shutdown	$T_J$		160		$^{\circ}\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 10) $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA  In Dropout $V_{in} = 2.75$ V, $I_{load} = 0.1$ mA  In Shutdown $S_D = 0$ V	$I_{GND}$        $I_{GNDsh}$		9.0 4.6 0.8 –  –  0.07	14 7.5 2.5 190  500  1.0	mA        $\mu\text{A}$
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to $100$ kHz, $C_{out} = 10$ $\mu\text{F}$ $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to $100$ kHz, $C_{out} = 10$ $\mu\text{F}$	$V_{noise}$		61 40		$\mu\text{Vrms}$ $\mu\text{Vrms}$
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
$S_D$ Input Current, $V_{SD} = 0$ V to $0.4$ V or $V_{SD} = 2.0$ V to $V_{in}$	$I_{SD}$		0.07	1.0	$\mu\text{A}$
Output Current In Shutdown Mode, $V_{out} = 0$ V	$I_{OSD}$		0.07	1.0	$\mu\text{A}$
Reverse Bias Protection, Current Flowing from the Output Pin to GND ( $V_{in} = 0$ V, $V_{out\_forced} = 2.85$ V)	$I_{OUTR}$		10		$\mu\text{A}$

8. Performance guaranteed over the operating temperature range by design and/or characterization, production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.
9. For output current capability for  $T_A < 0^{\circ}\text{C}$ , please refer to Figure 18.
10.  $T_A$  must be greater than  $0^{\circ}\text{C}$ .



**ELECTRICAL CHARACTERISTICS – 2.8 V** ( $V_{out} = 2.8\text{ V}$  typical,  $V_{in} = 3.2\text{ V}$ ,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ , unless otherwise noted, Note 11.)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 3.2\text{ V}$ to $6.8\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = 25^\circ\text{C}$	$V_{out}$	-0.9% 2.774	2.8	+0.9% 2.826	V
Output Voltage (Accuracy) $V_{in} = 3.2\text{ V}$ to $6.8\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = 0^\circ\text{C}$ to $+85^\circ\text{C}$	$V_{out}$	-1.4% 2.760	2.8	+1.4% 2.840	V
Output Voltage (Accuracy) (Note 12) $V_{in} = 3.2\text{ V}$ to $6.8\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	$V_{out}$	-1.5% 2.758	2.8	+1.5% 2.842	V
Line Regulation $V_{in} = 3.2\text{ V}$ to $12\text{ V}$ , $I_{load} = 0.1\text{ mA}$	$Line_{Reg}$		0.04		mV/V
Load Regulation $V_{in} = 3.2\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$	$Load_{Reg}$		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500\text{ mA}$ $I_{load} = 300\text{ mA}$ $I_{load} = 50\text{ mA}$ $I_{load} = 0.1\text{ mA}$	$V_{DO}$			340 230 110 10	mV
Peak Output Current (See Figure 16)	$I_{pk}$	500	700	800	mA
Short Output Current (See Figure 16)	$I_{sc}$			900	mA
Thermal Shutdown	$T_J$		160		$^\circ\text{C}$
Ground Current In Regulation $I_{load} = 500\text{ mA}$ (Note 13) $I_{load} = 300\text{ mA}$ (Note 13) $I_{load} = 50\text{ mA}$ $I_{load} = 0.1\text{ mA}$  In Dropout $V_{in} = 2.7\text{ V}$ , $I_{load} = 0.1\text{ mA}$  In Shutdown $S_D = 0\text{ V}$	$I_{GND}$        $I_{GNDsh}$		9.0 4.6 0.8 –  –  0.07	14 7.5 2.5 190  500  1.0	mA        $\mu\text{A}$
Output Noise $C_{nr} = 0\text{ nF}$ , $I_{load} = 500\text{ mA}$ , $f = 10\text{ Hz}$ to $100\text{ kHz}$ , $C_{out} = 10\text{ }\mu\text{F}$ $C_{nr} = 10\text{ nF}$ , $I_{load} = 500\text{ mA}$ , $f = 10\text{ Hz}$ to $100\text{ kHz}$ , $C_{out} = 10\text{ }\mu\text{F}$	$V_{noise}$		52 36		$\mu\text{Vrms}$ $\mu\text{Vrms}$
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
$S_D$ Input Current, $V_{SD} = 0\text{ V}$ to $0.4\text{ V}$ or $V_{SD} = 2.0\text{ V}$ to $V_{in}$	$I_{SD}$		0.07	1.0	$\mu\text{A}$
Output Current In Shutdown Mode, $V_{out} = 0\text{ V}$	$I_{OSD}$		0.07	1.0	$\mu\text{A}$
Reverse Bias Protection, Current Flowing from the Output Pin to GND ( $V_{in} = 0\text{ V}$ , $V_{out\_forced} = 2.8\text{ V}$ )	$I_{OUTR}$		10		$\mu\text{A}$

11. Performance guaranteed over the operating temperature range by design and/or characterization, production tested at  $T_J = T_A = 25^\circ\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

12. For output current capability for  $T_A < 0^\circ\text{C}$ , please refer to Figure 19.

13.  $T_A$  must be greater than  $0^\circ\text{C}$ .

**ELECTRICAL CHARACTERISTICS – 2.5 V** ( $V_{out} = 2.5$  V typical,  $V_{in} = 2.9$  V,  $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ , unless otherwise noted, Note 14.)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 2.9$ V to $6.5$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = 25^{\circ}\text{C}$	$V_{out}$	-0.9% 2.477	2.5	+0.9% 2.523	V
Output Voltage (Accuracy) $V_{in} = 2.9$ V to $6.5$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	$V_{out}$	-1.4% 2.465	2.5	+1.4% 2.535	V
Output Voltage (Accuracy), (Note 15) $V_{in} = 2.9$ V to $6.5$ V, $I_{load} = 0.1$ mA to $500$ mA, $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	$V_{out}$	-1.5% 2.462	2.5	+1.5% 2.538	V
Line Regulation $V_{in} = 2.9$ V to $12$ V, $I_{load} = 0.1$ mA	$Line_{Reg}$		0.04		mV/V
Load Regulation $V_{in} = 2.9$ V, $I_{load} = 0.1$ mA to $500$ mA	$Load_{Reg}$		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500$ mA (Note 16) $I_{load} = 300$ mA (Note 16) $I_{load} = 50$ mA $I_{load} = 0.1$ mA	$V_{DO}$			340 230 110 10	mV
Peak Output Current (See Figure 16)	$I_{pk}$	500	700	800	mA
Short Output Current (See Figure 16)	$I_{sc}$			900	mA
Thermal Shutdown	$T_J$		160		$^{\circ}\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 16) $I_{load} = 300$ mA (Note 16) $I_{load} = 50$ mA $I_{load} = 0.1$ mA  In Dropout $V_{in} = 2.4$ V, $I_{load} = 0.1$ mA  In Shutdown $S_D = 0$ V	$I_{GND}$        $I_{GNDsh}$		9.0 4.6 0.8 –   0.07	14 7.5 2.5 190  500 1.0	mA        $\mu\text{A}$
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to $100$ kHz, $C_{out} = 10$ $\mu\text{F}$ $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to $100$ kHz, $C_{out} = 10$ $\mu\text{F}$	$V_{noise}$		56 35		$\mu\text{Vrms}$ $\mu\text{Vrms}$
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
$S_D$ Input Current, $V_{SD} = 0$ V to $0.4$ V or $V_{SD} = 2.0$ V to $V_{in}$	$I_{SD}$		0.07	1.0	$\mu\text{A}$
Output Current In Shutdown Mode, $V_{out} = 0$ V	$I_{OSD}$		0.07	1.0	$\mu\text{A}$
Reverse Bias Protection, Current Flowing from the Output Pin to GND ( $V_{in} = 0$ V, $V_{out\_forced} = 2.5$ V)	$I_{OUTR}$		10		$\mu\text{A}$

14. Performance guaranteed over the operating temperature range by design and/or characterization, production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

15. For output current capability for  $T_A < 0^{\circ}\text{C}$ , please refer to Figure 20.

16.  $T_A$  must be greater than  $0^{\circ}\text{C}$ .

**ELECTRICAL CHARACTERISTICS – 1.8 V** ( $V_{out} = 1.8\text{ V}$  typical,  $V_{in} = 2.9\text{ V}$ ,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ , unless otherwise noted, Note 17.)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 2.9\text{ V}$ to $5.8\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = 25^\circ\text{C}$	$V_{out}$	-0.9% 1.783	1.8	+0.9% 1.817	V
Output Voltage (Accuracy) $V_{in} = 2.9\text{ V}$ to $5.8\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = 0^\circ\text{C}$ to $+85^\circ\text{C}$	$V_{out}$	-1.4% 1.774	1.8	+1.4% 1.826	V
Output Voltage (Accuracy), (Note 18) $V_{in} = 2.9\text{ V}$ to $5.8\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	$V_{out}$	-1.5% 1.773	1.8	+1.5% 1.827	V
Line Regulation $V_{in} = 2.9\text{ V}$ to $12\text{ V}$ , $I_{load} = 0.1\text{ mA}$	$Line_{Reg}$		0.04		mV/V
Load Regulation $V_{in} = 2.9\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$	$Load_{Reg}$		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500\text{ mA}$ (Notes 19, 20) $I_{load} = 300\text{ mA}$ (Notes 19, 20) $I_{load} = 50\text{ mA}$ (Notes 19, 20)	$V_{DO}$		620 230 95	1130 1130 1130	mV
Peak Output Current (See Figure 16)	$I_{pk}$	500	700	830	mA
Short Output Current (See Figure 16)	$I_{sc}$			900	mA
Thermal Shutdown	$T_J$		160		$^\circ\text{C}$
Ground Current In Regulation $I_{load} = 500\text{ mA}$ (Note 19) $I_{load} = 300\text{ mA}$ (Note 19) $I_{load} = 50\text{ mA}$ $I_{load} = 0.1\text{ mA}$  In Dropout $V_{in} = 2.2\text{ V}$ , $I_{load} = 0.1\text{ mA}$  In Shutdown $S_D = 0\text{ V}$	$I_{GND}$		9.0 4.6 0.8 –	14 7.5 2.5 190	mA   $\mu\text{A}$
	$I_{GNDsh}$		0.07	500 1.0	$\mu\text{A}$ $\mu\text{A}$
Output Noise $C_{nr} = 0\text{ nF}$ , $I_{load} = 500\text{ mA}$ , $f = 10\text{ Hz}$ to $100\text{ kHz}$ , $C_{out} = 10\text{ }\mu\text{F}$ $C_{nr} = 10\text{ nF}$ , $I_{load} = 500\text{ mA}$ , $f = 10\text{ Hz}$ to $100\text{ kHz}$ , $C_{out} = 10\text{ }\mu\text{F}$	$V_{noise}$		52 33		$\mu\text{Vrms}$ $\mu\text{Vrms}$
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
$S_D$ Input Current, $V_{SD} = 0\text{ V}$ to $0.4\text{ V}$ or $V_{SD} = 2.0\text{ V}$ to $V_{in}$	$I_{SD}$		0.07	1.0	$\mu\text{A}$
Output Current In Shutdown Mode, $V_{out} = 0\text{ V}$	$I_{OSD}$		0.07	1.0	$\mu\text{A}$
Reverse Bias Protection, Current Flowing from the Output Pin to GND ( $V_{in} = 0\text{ V}$ , $V_{out\_forced} = 1.8\text{ V}$ )	$I_{OUTR}$		10		$\mu\text{A}$

17. Performance guaranteed over the operating temperature range by design and/or characterization, production tested at  $T_J = T_A = 25^\circ\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

18. For output current capability for  $T_A < 0^\circ\text{C}$ , please refer to Figure 21.

19.  $T_A$  must be greater than  $0^\circ\text{C}$ .

20. Maximum dropout voltage is limited by minimum input voltage  $V_{in} = 2.9\text{ V}$  recommended for guaranteed operation.

**ELECTRICAL CHARACTERISTICS – 1.5 V** ( $V_{out} = 1.5\text{ V}$  typical,  $V_{in} = 2.9\text{ V}$ ,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ , unless otherwise noted, Note 21.)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 2.9\text{ V}$ to $5.5\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = 25^\circ\text{C}$	$V_{out}$	-0.9% 1.486	1.5	+0.9% 1.514	V
Output Voltage (Accuracy) $V_{in} = 2.9\text{ V}$ to $5.5\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = 0^\circ\text{C}$ to $+85^\circ\text{C}$	$V_{out}$	-1.4% 1.479	1.5	+1.4% 1.521	V
Output Voltage (Accuracy), (Note 22) $V_{in} = 2.9\text{ V}$ to $5.5\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$ , $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$	$V_{out}$	-1.5% 1.477	1.5	+1.5% 1.523	V
Line Regulation $V_{in} = 2.9\text{ V}$ to $12\text{ V}$ , $I_{load} = 0.1\text{ mA}$	$Line_{Reg}$		0.04		mV/V
Load Regulation $V_{in} = 2.9\text{ V}$ , $I_{load} = 0.1\text{ mA}$ to $500\text{ mA}$	$Load_{Reg}$		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500\text{ mA}$ (Notes 23, 24) $I_{load} = 300\text{ mA}$ (Notes 23, 24) $I_{load} = 50\text{ mA}$ (Notes 23, 24)	$V_{DO}$		940 500 350	1430 1430 1430	mV
Peak Output Current (See Figure 16)	$I_{pk}$	500	700	860	mA
Short Output Current (See Figure 16)	$I_{sc}$			900	mA
Thermal Shutdown	$T_J$		160		$^\circ\text{C}$
Ground Current In Regulation $I_{load} = 500\text{ mA}$ (Note 23) $I_{load} = 300\text{ mA}$ (Note 23) $I_{load} = 50\text{ mA}$ $I_{load} = 0.1\text{ mA}$  In Dropout $V_{in} = 2.2\text{ V}$ , $I_{load} = 0.1\text{ mA}$  In Shutdown $S_D = 0\text{ V}$	$I_{GND}$		9.0 4.6 0.8 –	14 7.5 2.5 190	mA   $\mu\text{A}$
	$I_{GNDsh}$		0.07	1.0	$\mu\text{A}$
Output Noise $C_{nr} = 0\text{ nF}$ , $I_{load} = 500\text{ mA}$ , $f = 10\text{ Hz}$ to $100\text{ kHz}$ , $C_{out} = 10\text{ }\mu\text{F}$ $C_{nr} = 10\text{ nF}$ , $I_{load} = 500\text{ mA}$ , $f = 10\text{ Hz}$ to $100\text{ kHz}$ , $C_{out} = 10\text{ }\mu\text{F}$	$V_{noise}$		51 31		$\mu\text{Vrms}$ $\mu\text{Vrms}$
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
$S_D$ Input Current, $V_{SD} = 0\text{ V}$ to $0.4\text{ V}$ or $V_{SD} = 2.0\text{ V}$ to $V_{in}$	$I_{SD}$		0.07	1.0	$\mu\text{A}$
Output Current In Shutdown Mode, $V_{out} = 0\text{ V}$	$I_{OSD}$		0.07	1.0	$\mu\text{A}$
Reverse Bias Protection, Current Flowing from the Output Pin to GND ( $V_{in} = 0\text{ V}$ , $V_{out\_forced} = 1.5\text{ V}$ )	$I_{OUTR}$		10		$\mu\text{A}$

21. Performance guaranteed over the operating temperature range by design and/or characterization, production tested at  $T_J = T_A = 25^\circ\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

22. For output current capability for  $T_A < 0^\circ\text{C}$ , please refer to Figure 22.

23.  $T_A$  must be greater than  $0^\circ\text{C}$ .

24. Maximum dropout voltage is limited by minimum input voltage  $V_{in} = 2.9\text{ V}$  recommended for guaranteed operation.

**ELECTRICAL CHARACTERISTICS – Adjustable** ( $V_{out} = 1.25$  V typical,  $V_{in} = 2.9$  V,  $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ , unless otherwise noted, Note 25)

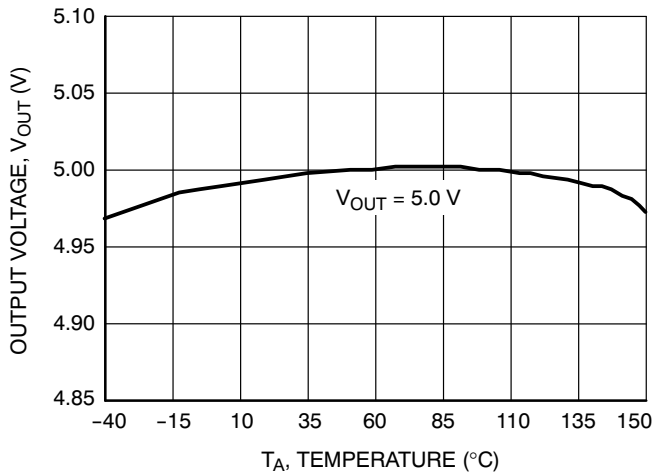
Characteristic	Symbol	Min	Typ	Max	Unit
Reference Voltage (Accuracy) $V_{in} = 2.9$ V to $V_{out} + 4.0$ V, $I_{load} = 0.1$ mA to 500 mA, $T_A = 25^{\circ}\text{C}$	$V_{ref}$	-0.9% 1.239	1.25	+0.9% 1.261	V
Reference Voltage (Accuracy) $V_{in} = 2.9$ V to $V_{out} + 4.0$ V, $I_{load} = 0.1$ mA to 500 mA, $T_A = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	$V_{ref}$	-1.4% 1.233	1.25	+1.4% 1.268	V
Reference Voltage (Accuracy) (Note 26) $V_{in} = 2.9$ V to $V_{out} + 4.0$ V, $I_{load} = 0.1$ mA to 500 mA, $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	$V_{ref}$	-1.5% 1.231	1.25	+1.5% 1.269	V
Line Regulation $V_{in} = 2.9$ V to 12 V, $I_{load} = 0.1$ mA	$Line_{Reg}$		0.04		mV/V
Load Regulation $V_{in} = 2.9$ V, $I_{load} = 0.1$ mA to 500 mA	$Load_{Reg}$		0.04		mV/mA
Dropout Voltage (See App Note), $V_{out} = 2.5$ V to 10 V $I_{load} = 500$ mA (Note 27) $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA	$V_{DO}$			340 230 110 10	mV
Peak Output Current (Note 27) (See Figure 16)	$I_{pk}$	500	700	860	mA
Short Output Current (See Figure 16) $V_{out} \leq 3.3$ V $V_{out} > 3.3$ V	$I_{sc}$			900 990	mA
Thermal Shutdown	$T_J$		160		$^{\circ}\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 27) $I_{load} = 300$ mA (Note 27) $I_{load} = 50$ mA $I_{load} = 0.1$ mA  In Dropout $V_{in} = V_{out} - 0.1$ V or 2.2 V (whichever is higher), $I_{load} = 0.1$ mA  In Shutdown $S_D = 0$ V	$I_{GND}$        $I_{GNDsh}$		9.0 4.6 0.8 -  -  0.07	14 7.5 2.5 190  500  1.0	mA        $\mu\text{A}$
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ $\mu\text{F}$ $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ $\mu\text{F}$	$V_{noise}$		38 26		$\mu\text{V}_{rms}$ $\mu\text{V}_{rms}$
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
$S_D$ Input Current, $V_{SD} = 0$ V to 0.4 V or $V_{SD} = 2.0$ V to $V_{in}$ $V_{in} \leq 5.4$ V $V_{in} > 5.4$ V	$I_{SD}$		0.07	1.0 5.0	$\mu\text{A}$
Output Current In Shutdown Mode, $V_{out} = 0$ V	$I_{OSD}$		0.07	1.0	$\mu\text{A}$
Reverse Bias Protection, Current Flowing from the Output Pin to GND ( $V_{in} = 0$ V, $V_{out\_forced} = V_{out}(\text{nom}) \leq 7$ V) (Note 28)	$I_{OUTR}$		1.0		$\mu\text{A}$

25. Performance guaranteed over the operating temperature range by design and/or characterization, production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

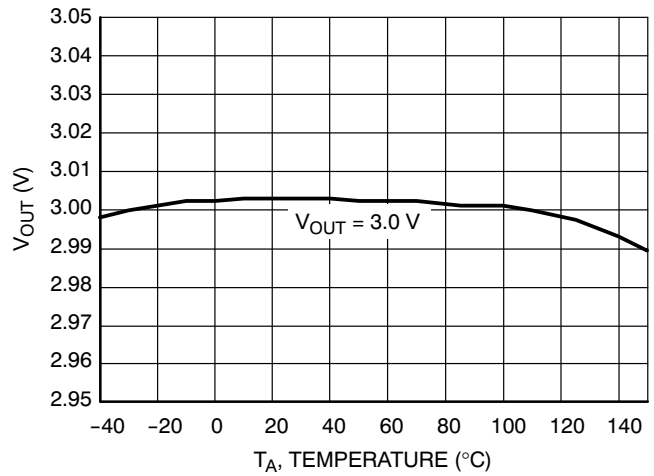
26. For output current capability for  $T_A < 0^{\circ}\text{C}$ , please refer to Figures 18 to 22.

27.  $T_A$  must be greater than  $0^{\circ}\text{C}$ .

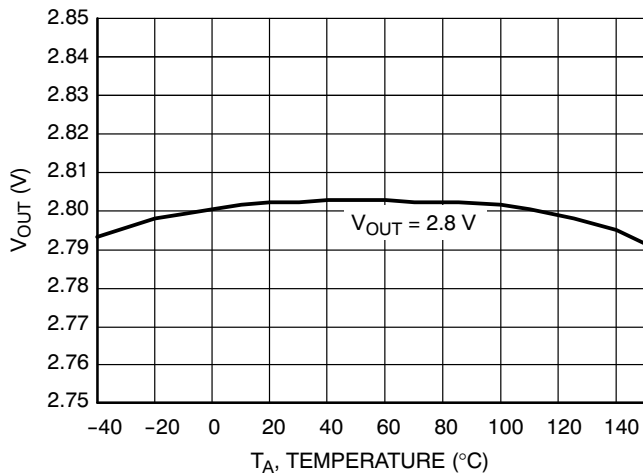
28. Reverse bias protection feature valid only if  $V_{out} - V_{in} \leq 7$  V.



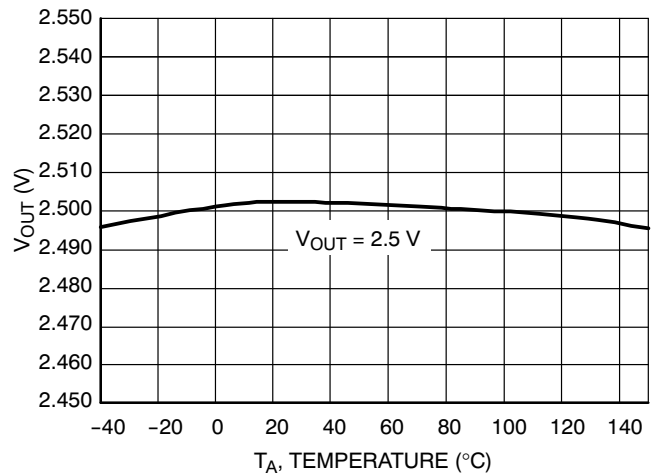
**Figure 5. Output Voltage vs. Temperature  
5.0 V Version**



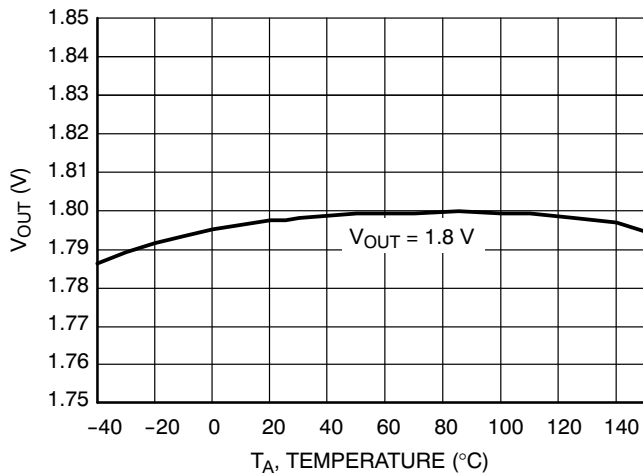
**Figure 6. Output Voltage vs. Temperature  
3.0 V Version**



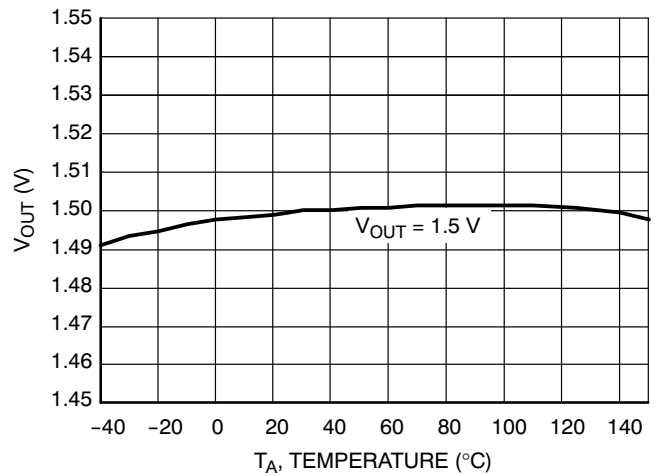
**Figure 7. Output Voltage vs. Temperature  
2.8 V Version**



**Figure 8. Output Voltage vs. Temperature  
2.5 V Version**



**Figure 9. Output Voltage vs. Temperature  
1.8 V Version**



**Figure 10. Output Voltage vs. Temperature  
1.5 V Version**

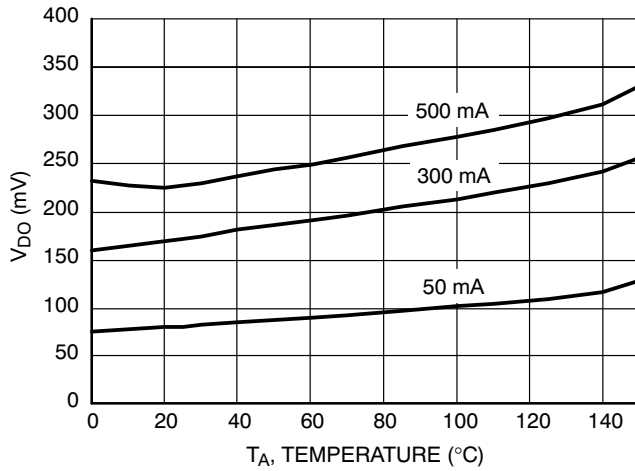


Figure 11. Dropout Voltage vs. Temperature  
2.8 V Version

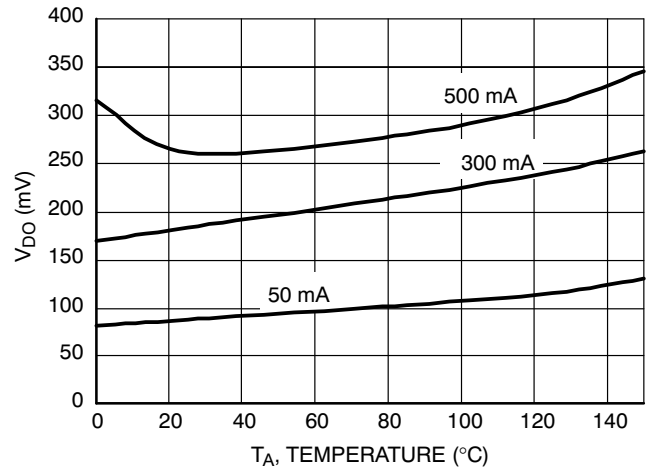


Figure 12. Dropout Voltage vs. Temperature  
2.5 V Version

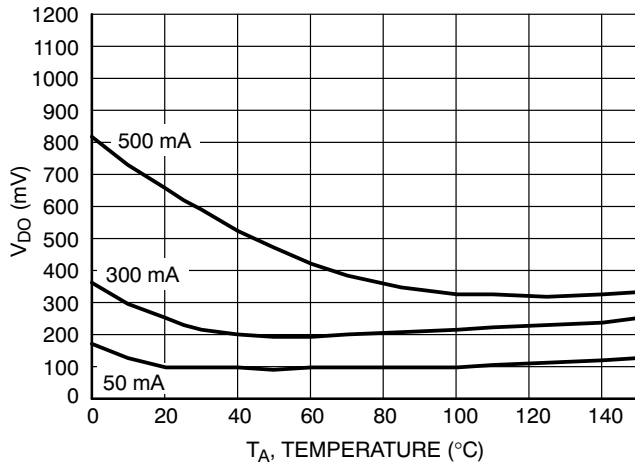


Figure 13. Dropout Voltage vs. Temperature  
1.8 V Version

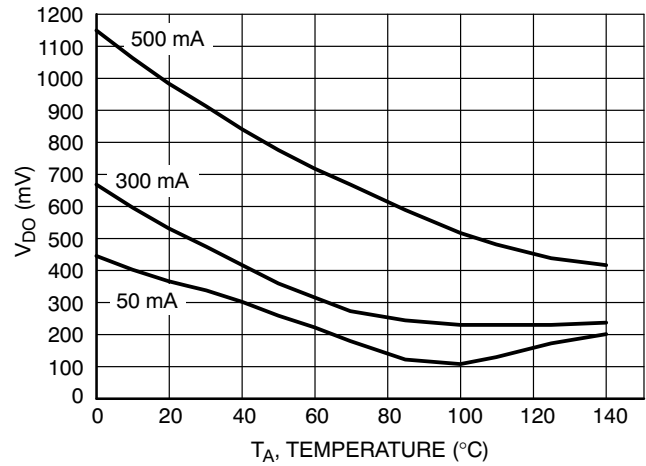


Figure 14. Dropout Voltage vs. Temperature  
1.5 V Version

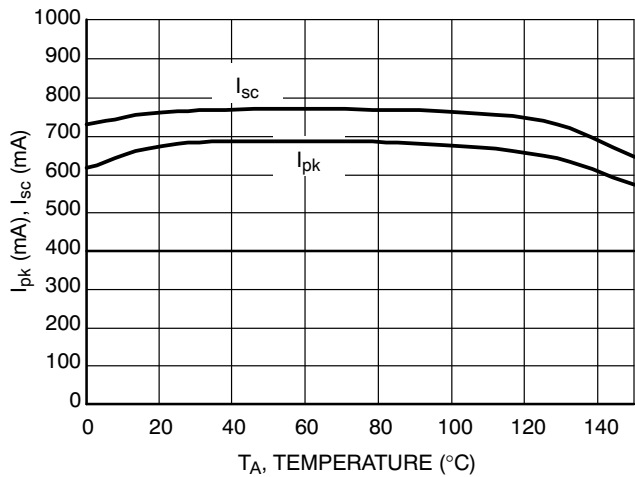


Figure 15. Peak and Short Current  
vs. Temperature

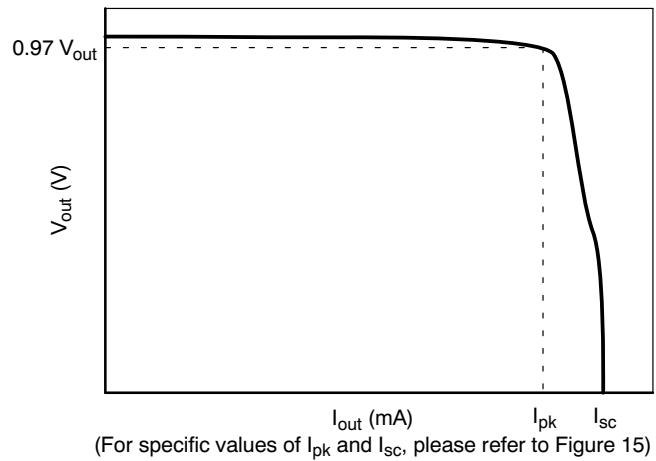


Figure 16. Output Voltage vs. Output Current

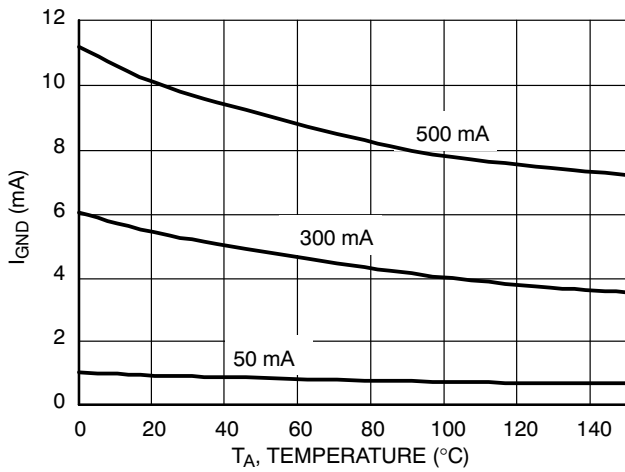


Figure 17. Ground Current vs. Temperature

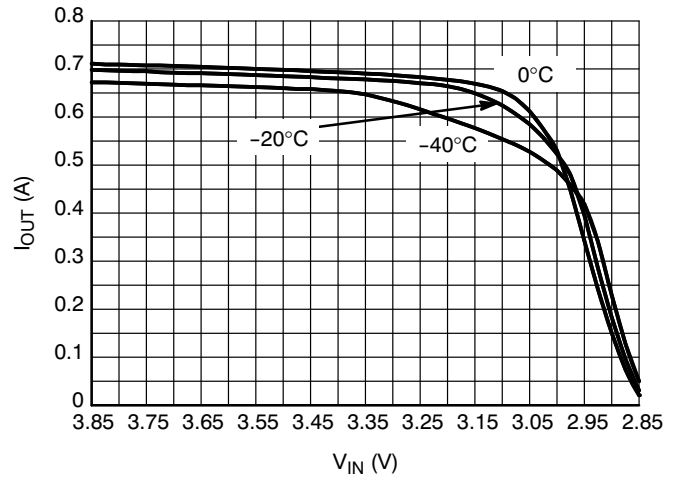


Figure 18. Output Current Capability for the 2.85 V Version

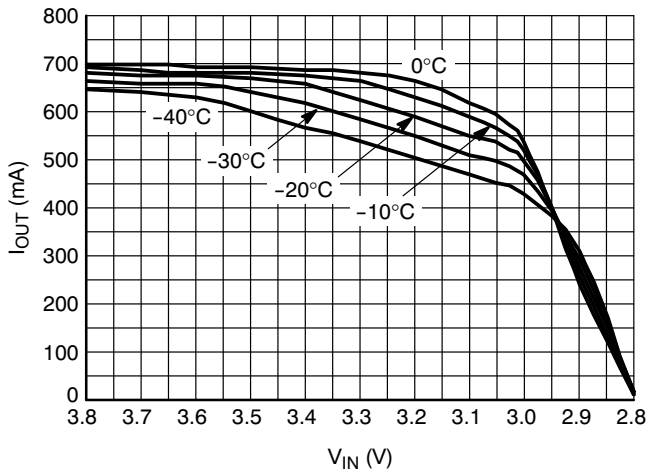


Figure 19. Output Current Capability for the 2.8 V Version

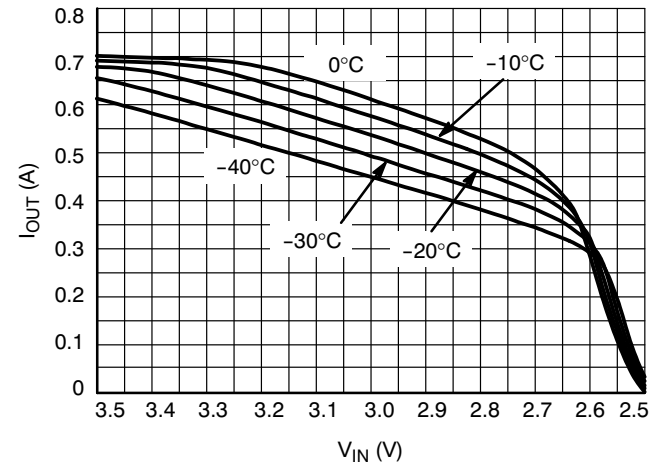


Figure 20. Output Current Capability for the 2.5 V Version

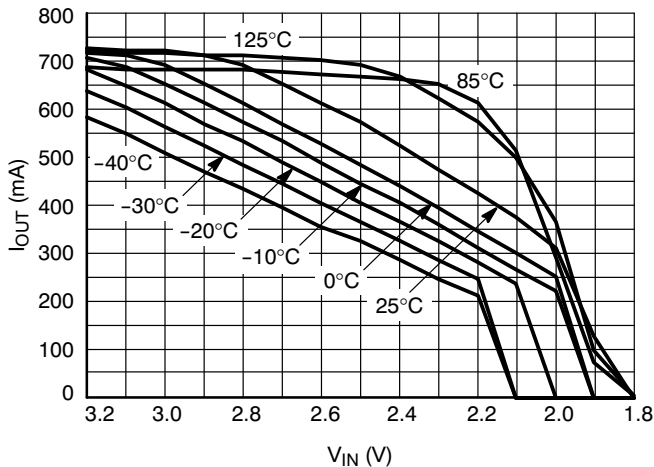


Figure 21. Output Current Capability for the 1.8 V Version

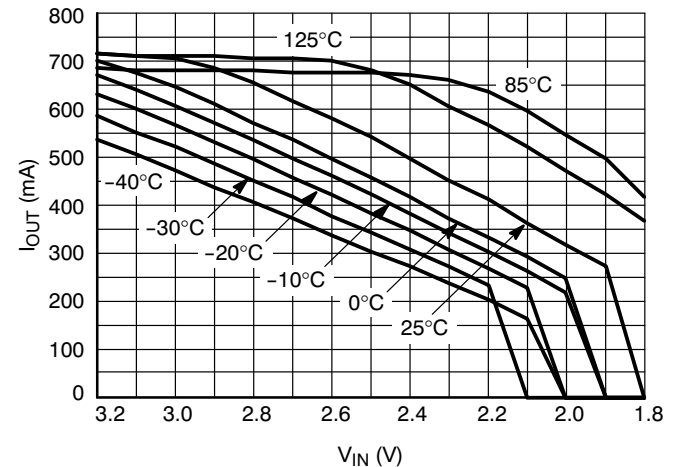


Figure 22. Output Current Capability for the 1.5 V Version



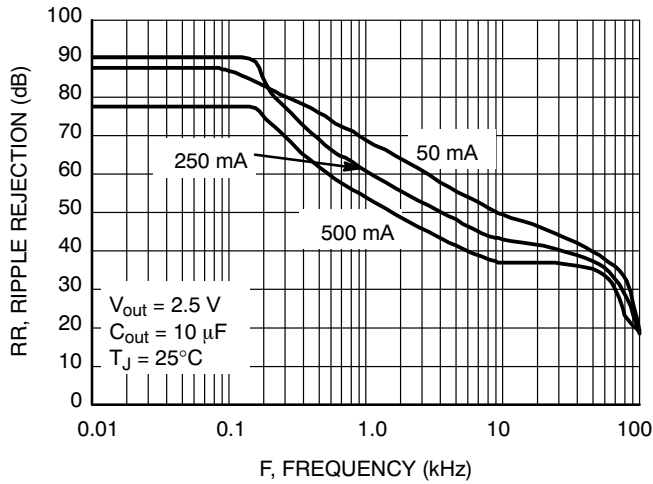


Figure 23. Ripple Rejection vs. Frequency

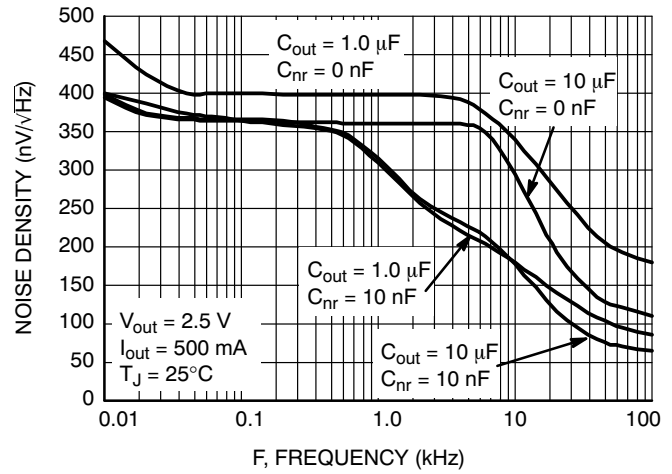


Figure 24. Output Noise Density

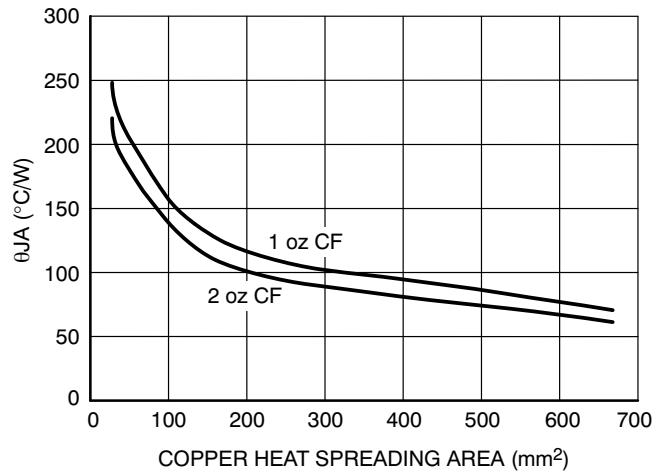


Figure 25. DFN 10 Self Heating Thermal Characteristic as a Function of Copper Area on the PCB

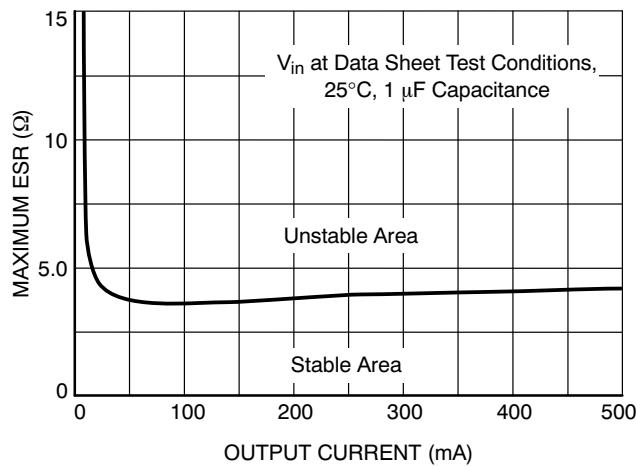


Figure 26. Stability with ESR vs.  $I_{out}$

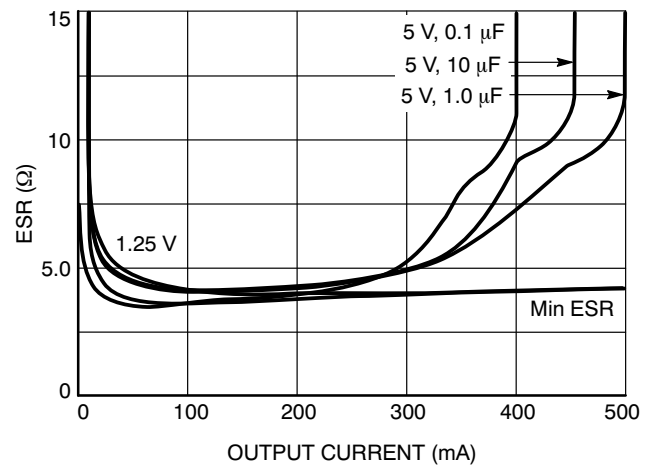


Figure 27. Output Current vs. ESR

NOTE: Typical characteristics were measured with the same conditions as electrical characteristics.

## APPLICATIONS INFORMATION

**Reverse Bias Protection**

Reverse bias is a condition caused when the input voltage goes to zero, but the output voltage is kept high either by a large output capacitor or another source in the application which feeds the output pin.

Normally in a bipolar LDO all the current will flow from the output pin to input pin through the PN junction with limited current capability and with the potential to destroy the IC.

Due to an improved architecture, the NCV8535 can withstand up to 7.0 V on the output pin with virtually no current flowing from output pin to input pin, and only negligible amount of current (tens of  $\mu\text{A}$ ) flowing from the output pin to ground for infinite duration.

**Input Capacitor**

An input capacitor of at least 1.0  $\mu\text{F}$ , any type, is recommended to improve the transient response of the regulator and/or if the regulator is located more than a few inches from the power source. It will also reduce the circuit's sensitivity to the input line impedance at high frequencies. The capacitor should be mounted with the shortest possible track length directly across the regular's input terminals.

**Output Capacitor**

The NCV8535 remains stable with any type of capacitor as long as it fulfills its 1.0  $\mu\text{F}$  requirement. There are no constraints on the minimum ESR and it will remain stable up to an ESR of 5.0  $\Omega$ . Larger capacitor values will improve the noise rejection and load transient response.

**Noise Reduction Pin**

Output noise can be greatly reduced by connecting a 10 nF capacitor ( $C_{nr}$ ) between the noise reduction pin and ground (see Figure 1). In applications where very low noise is not required, the noise reduction pin can be left unconnected.

For the adjustable version, in addition to the 10 nF  $C_{nr}$ , a 68 pF capacitor connected in parallel with R1 (see Figure 2) is recommended to further reduce output noise and improve stability.

**Adjustable Operation**

The output voltage can be set by using a resistor divider as shown in Figure 2 with a range of 1.25 to 10 V. The appropriate resistor divider can be found by solving the equation below. The recommended current through the resistor divider is from 10  $\mu\text{A}$  to 100  $\mu\text{A}$ . This can be accomplished by selecting resistors in the  $\text{k}\Omega$  range. As result, the  $I_{adj} \cdot R_2$  becomes negligible in the equation and can be ignored.

$$V_{out} = 1.25 \cdot \left(1 + \frac{R_1}{R_2}\right) + I_{adj} \cdot R_2 \quad (\text{eq. 1})$$

Example:

For  $V_{out} = 2.9 \text{ V}$ , can use  $R_1 = 36 \text{ k}\Omega$  and  $R_2 = 27 \text{ k}\Omega$ .

$$1.25 \cdot \left(1 + \frac{36 \text{ k}\Omega}{27 \text{ k}\Omega}\right) = 2.91 \text{ V} \quad (\text{eq. 2})$$

**Dropout Voltage**

The voltage dropout is measured at 97% of the nominal output voltage.

**No-Load Regulation Considerations**

If there is no load at output of the regulator and ambient temperature is higher than 85°C leakage current flowing from input to output through pass transistor may cause increase of output voltage out of specification range up to input voltage level. To avoid this situation minimum load current of 100  $\mu\text{A}$  or higher is recommended if ambient temperature exceeds 85°C.

**Thermal Considerations**

Internal thermal limiting circuitry is provided to protect the integrated circuit in the event that the maximum junction temperature is exceeded. This feature provides protection from a catastrophic device failure due to accidental overheating. This protection feature is not intended to be used as a substitute to heat sinking. The maximum power that can be dissipated, can be calculated with the equation below:

$$P_D = \frac{T_{J(max)} - T_A}{R_{\theta JA}} \quad (\text{eq. 3})$$

## NCV8535

### DEVICE ORDERING INFORMATION

Device	Voltage Version	Marking Code	Package	Shipping <sup>†</sup>
NCV8535MNADJR2G	Adj.	ADJ	DFN10 (Pb-Free)	3000 / Tape & Reel
NCV8535MN150R2G	1.5 V	150		
NCV8535MN180R2G	1.8 V	180		
NCV8535MN250R2G	2.5 V	250		
NCV8535MN280R2G	2.8 V	280		
NCV8535MN285R2G	2.85 V	285		
NCV8535MN300R2G	3.0 V	300		
NCV8535MN330R2G	3.3 V	330		
NCV8535MN500R2G	5.0 V	500		

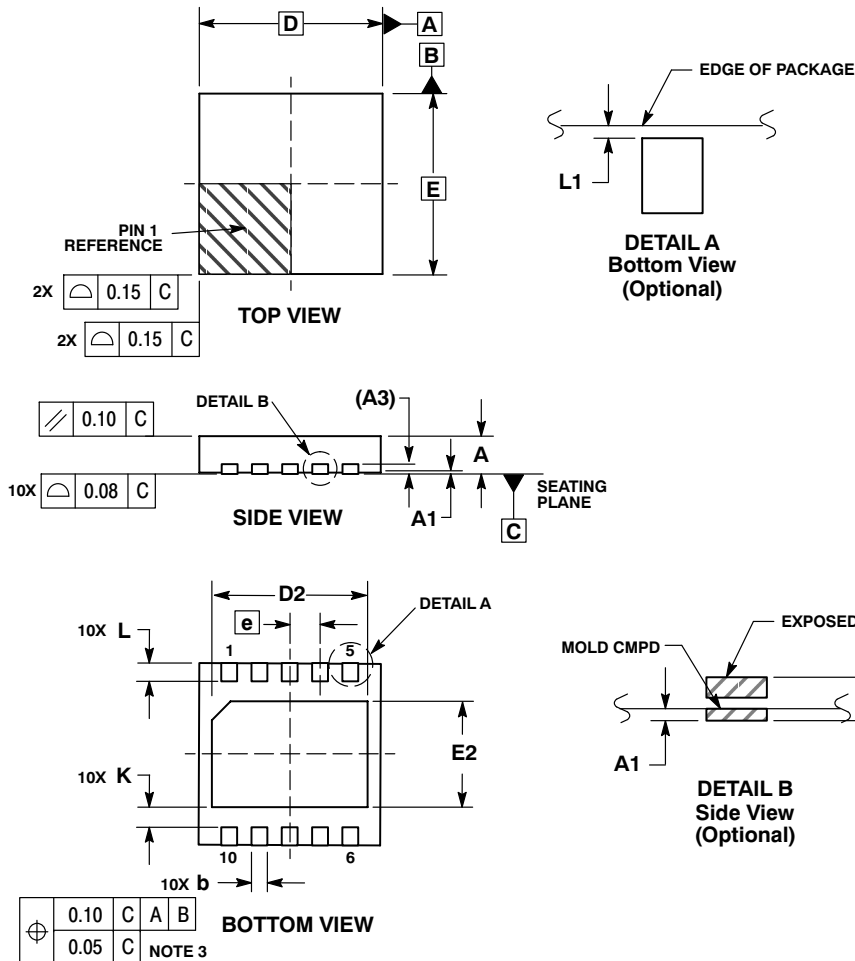
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*Please contact factory for other voltage options.

# NCV8535

## PACKAGE DIMENSIONS

DFN10, 3x3  
CASE 485C-01  
ISSUE A



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM TERMINAL.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
5. TERMINAL b MAY HAVE MOLD COMPOUND MATERIAL ALONG SIDE EDGE. MOLD FLASHING MAY NOT EXCEED 30 MICRONS ONTO BOTTOM SURFACE OF TERMINAL b.
6. DETAILS A AND B SHOW OPTIONAL VIEWS FOR END OF TERMINAL LEAD AT EDGE OF PACKAGE.

MILLIMETERS		
DIM	MIN	MAX
A	0.80	1.00
A1	0.00	0.05
A3	0.20	REF
b	0.18	0.30
D	3.00	BSC
D2	2.45	2.55
E	3.00	BSC
E2	1.75	1.85
e	0.50	BSC
K	0.19	TYP
L	0.35	0.45
L1	0.00	0.03

The products described herein NCV8535, may be covered by one or more of the following U.S. patents; 5,920,184, 5,966,004, and 5,834,926. There may be other patents pending.

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