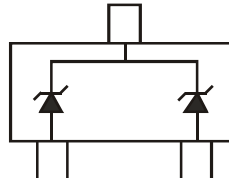


Features

- Dual TVS in Common Cathode Configuration for ESD Protection
- 40 Watt Peak Power Dissipation @1.0ms (Unidirectional)
- 225 mW Power Dissipation
- Ideally Suited for Automated Insertion
- Low Leakage
- **Lead, Halogen and Antimony Free, RoHS Compliant "Green" Device (Notes 4 and 5)**
- **Qualified to AEC-Q101 Standards for High Reliability**



Top View



Device Schematic

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic. UL Flammability Rating Classification 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Polarity: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.008 grams (approximate)

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Peak Power Dissipation (Note 2)	P _{PK}	40	W

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 1)	P _D	225	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R _{θJA}	556	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

V_F = 0.9V max @ I_F = 10mA (Note 3)

Type Number	Marking Code	V _{RWM}	I _R @ V _{RWM}	Breakdown Voltage				V _C @ I _{PP} (Note 2)		Typical Temperature Coefficient
				V _{BR} (Note 3) (V)			@ I _T	V _C	I _{PP}	
				Min	Nom	Max				
MMBZ15VDL	KVJ	12.8	100	14.3	15	15.8	1.0	21.2	1.9	+0.080

V_F = 1.1V max @ I_F = 200mA (Note 3)

Type Number	Marking Code	V _{RWM} Volts	I _R @ V _{RWM} nA	Breakdown Voltage			V _C @ I _{PP} (Note 2)		Typical Temperature Coefficient T _C (%/°C)	
				V _{BR} (Note 3) (V)			@ I _T	V _C		I _{PP}
				Min	Nom	Max	mA	V		A
MMBZ27VCL	KVP	22	50	25.65	27	28.35	1.0	38	1.0	+0.090

- Notes:
1. Device mounted on FR-5 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes Inc. suggested pad layout AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>. 200mW per element must not be exceeded.
 2. Non-repetitive current pulse per Figure 2 and derate above T_A = 25°C per Figure 1.
 3. Short duration pulse test used to minimize self-heating effect.
 4. No purposefully added lead. Halogen and Antimony Free.
 5. Product manufactured with Data Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.

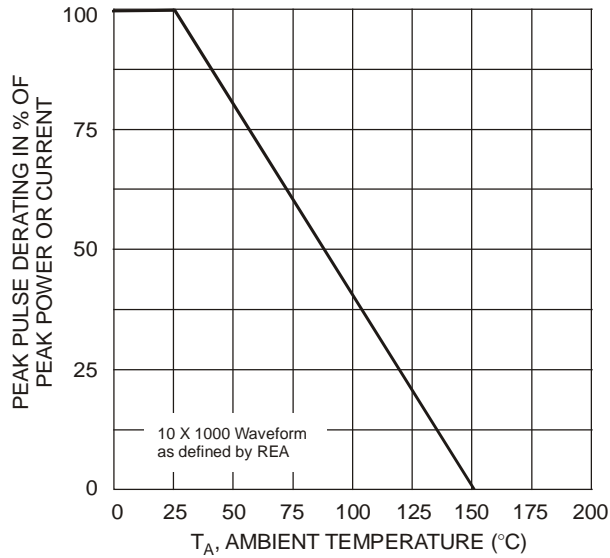


Fig. 1 Pulse Derating Curve

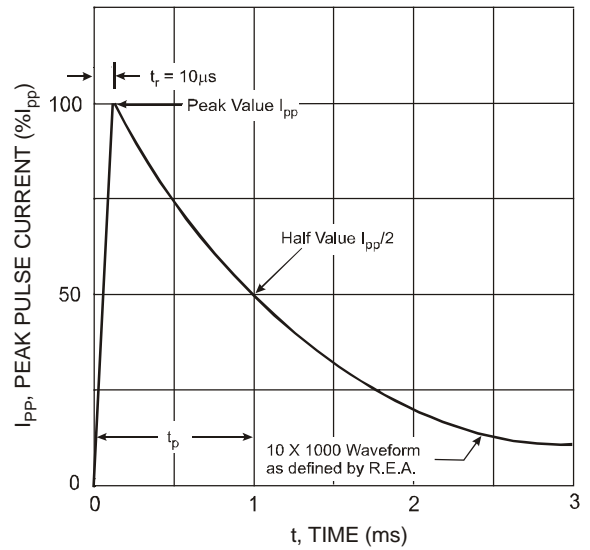


Fig. 2 Pulse Waveform

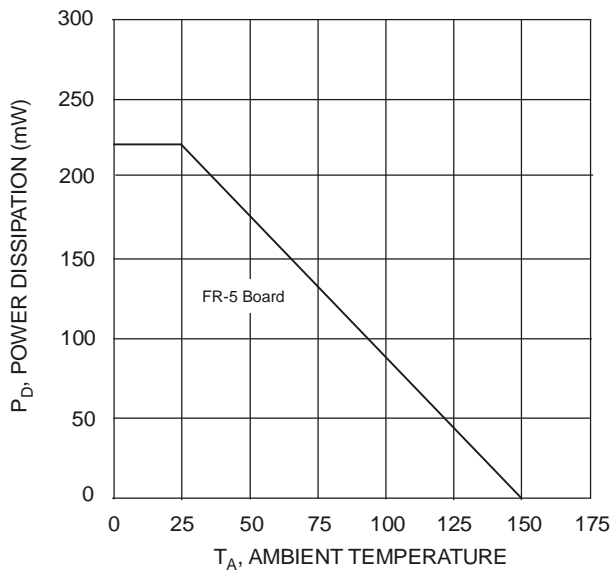


Fig. 3 Steady State Power Derating Curve

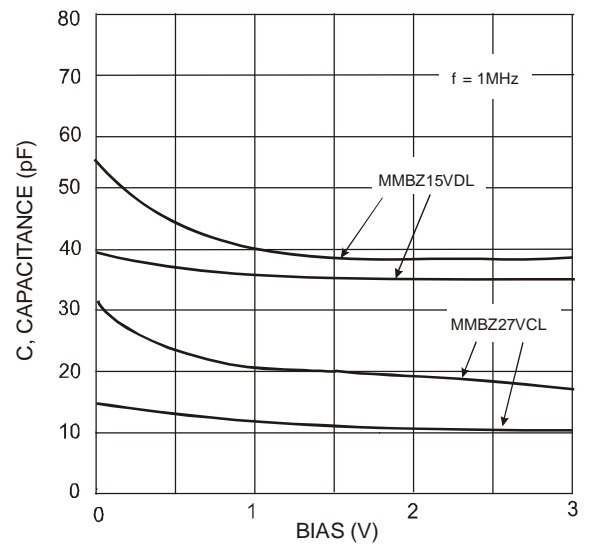


Fig. 4 Typical Capacitance vs. Bias Voltage
(Lower curve is Bidirectional mode,
Upper curve is Unidirectional mode)

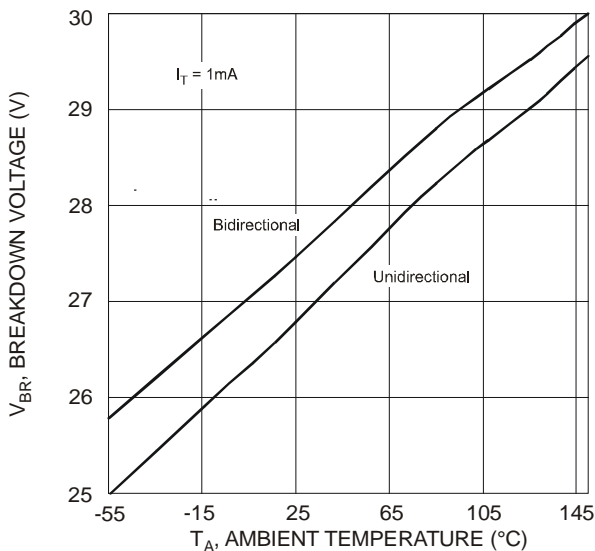


Fig. 5 Typical Breakdown Voltage vs. Temperature (MMBZ27VCL)

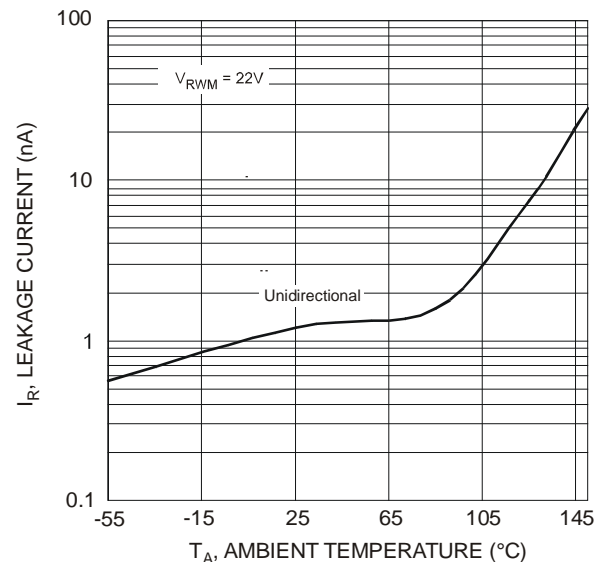
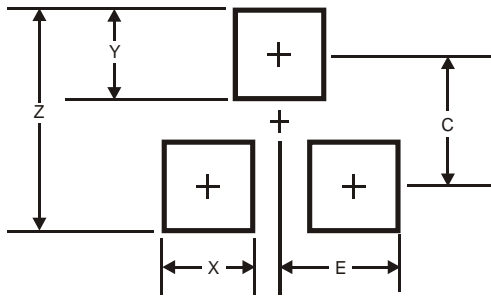


Fig. 6 Typical Leakage Current vs. Temperature (MMBZ27VCL)

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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