April 2007

## National Semiconductor

# LM333 3-Ampere Adjustable Negative Regulator

### **General Description**

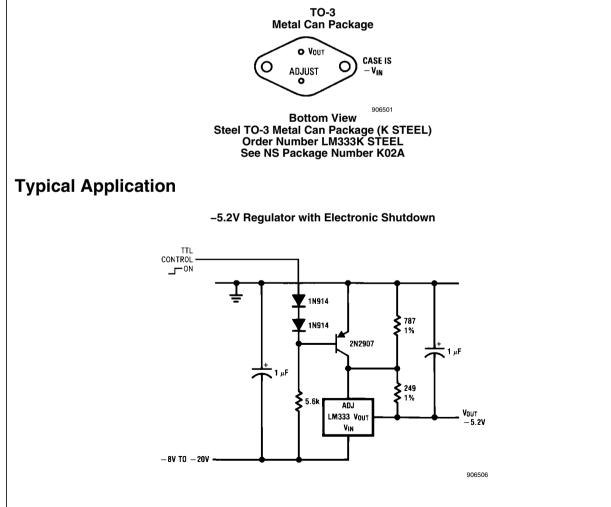
The LM333 is an adjustable 3-terminal negative voltage regulator capable of supplying in excess of -3.0A over an output voltage range of -1.2V to -32V. This regulator is exceptionally easy to apply, requiring only 2 external resistors to set the output voltage and 1 output capacitor for frequency compensation. The circuit design has been optimized for excellent regulation and low thermal transients. Further, the LM333 features internal current limiting, thermal shutdown and safearea compensation, making them substantially immune to failure from overloads.

The LM333 serves a wide variety of applications including local on-card regulation, programmable-output voltage regulation or precision current regulation. The LM333 is an ideal complement to the LM150/LM350 adjustable positive regulators.

#### **Features**

- Output voltage adjustable from -1.2V to -32V
- 3.0A output current guaranteed, -40°C to +125°C
- Line regulation typically 0.01%/V
- Load regulation typically 0.2%
- Excellent rejection of thermal transients
- 50 ppm/°C temperature coefficient
- Temperature-independent current limit
- Internal thermal overload protection
- Standard 3-lead transistor package
- Output is short circuit protected

#### **Connection Diagram**





**Power Dissipation** 

Input-Output Voltage Differential

#### Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Operating Junction Temperature Range	
LM333	-4
Storogo Tomporaturo	6

T<sub>MIN</sub> to T<sub>MAX</sub> -40°C to +125°C -65°C to +150°C

Storage Temperature Lead Temperature (Soldering, 10 sec.) TO-3 Package ESD Susceptibility

300°C TBD

#### **Electrical Characteristics LM333**

Specifications with standard typeface are for  $T_J = 25^{\circ}$ C, and those with **boldface type** apply over the full operating temperature range. (Note 3)

Internally Limited

35V

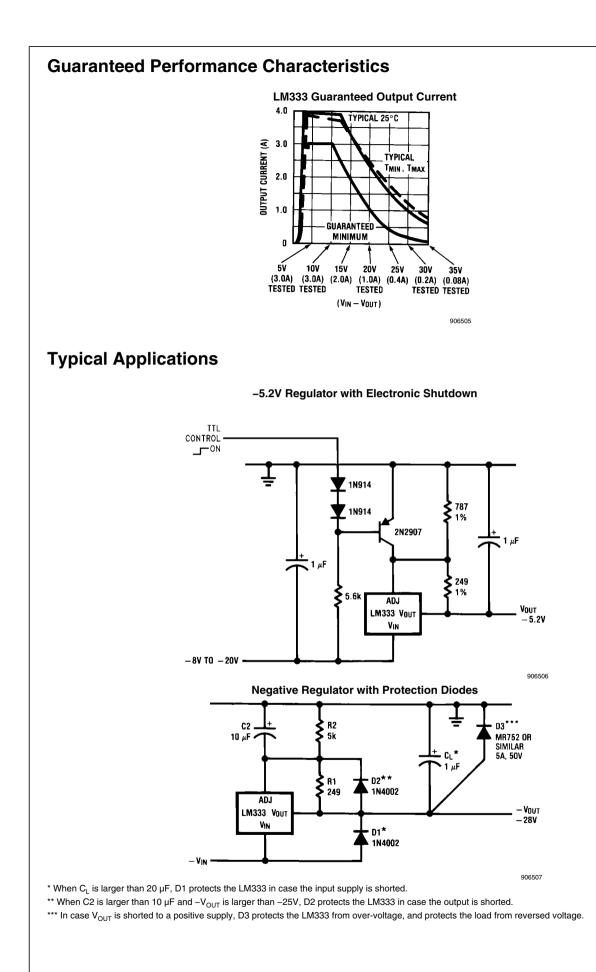
Conditions	Typical	Min	Max	Units	
		(Note 2)	(Note 2)		
I <sub>L</sub> = 10 mA	-1.250 -1.225 -1.2	-1.275			
/oltage $3V \le  V_{IN} - V_{OUT}  \le 35V$ 1.250	_1 250	1 212	_1 287	V	
10 mA $\leq$ I <sub>L</sub> $\leq$ 3A, P $\leq$ P <sub>MAX</sub>	-1.250	-1.250	-1.213	-1.207	
$3V \le  V_{IN} - V_{OUT}  \le 35V$	0.01 <b>0.02</b>		0.04	% /V	
I <sub>OUT</sub> = 50 mA (Note 4)			0.07		
10 mA $\leq$ I <sub>L</sub> $\leq$ 3A, P $\leq$ P <sub>MAX</sub>	0.2		1.0	%	
(Notes 4, 5)	0.4		1.5		
10 ms Pulse	0.002		0.02	% /W	
$T_{MIN} \le T_J \le T_{MAX}$	0.5			%	
$T_{J} = 125^{\circ}C$ , 1000 Hours	0.2			%	
	65		95	μA	
	70		100	μ., ,	
-	2.5		8	μA	
$3.0V \le  V_{\rm IN} - V_{\rm OUT}  \le 35V$		2.0			μ, ι
$ V_{IN} - V_{OUT}  \le 35V$	2.5   10     1.5   5.0	10	mA		
$ V_{IN} - V_{OUT}  \le 10V$			5.0		
$3V \le  V_{IN} - V_{OUT}  \le 10V$	<b>3.9</b> 3.0				
$ V_{IN} - V_{OUT}  = 20V$	2.4	1.0		А	
$ V_{IN} - V_{OUT}  = 30V$	0.4	0.20			
10 Hz to 10 kHz	0.003			% (rms)	
	0.000			78 (IIII3)	
V <sub>OUT</sub> = 10V, f = 120 Hz					
$C_{ADJ} = 0 \ \mu F$	60			dB	
C <sub>ADJ</sub> = 10 μF	77				
	163			°C	
<u> </u>	1.2		1.8	°C/W	
(K Steel)					
TO-3 Package	35	05		00/14/	
(K Steel)				°C/W	
	$\begin{split}  I_{L} = 10 \text{ mA} \\ 3V \leq  V_{ N} - V_{OUT}  \leq 35V \\ 10 \text{ mA} \leq  L \leq 3A, P \leq P_{MAX} \\ 3V \leq  V_{ N} - V_{OUT}  \leq 35V \\  _{OUT} = 50 \text{ mA} (Note 4) \\ 10 \text{ mA} \leq  L \leq 3A, P \leq P_{MAX} \\ (Notes 4, 5) \\ 10 \text{ ms Pulse} \\ \hline T_{MIN} \leq T_{J} \leq T_{MAX} \\ \hline T_{J} = 125^{\circ}C, 1000 \text{ Hours} \\ \hline \\ 10 \text{ mA} \leq  L \leq 3A \\ 3.0V \leq  V_{ N} - V_{OUT}  \leq 35V \\  V_{ N} - V_{OUT}  \leq 35V \\  V_{ N} - V_{OUT}  \leq 35V \\  V_{ N} - V_{OUT}  \leq 10V \\ 3V \leq  V_{ N} - V_{OUT}  \leq 10V \\  V_{ N} - V_{OUT}  = 30V \\ 10 \text{ Hz to 10 kHz} \\ \hline \\ V_{OUT} = 10V, f = 120 \text{ Hz} \\ \hline \\ $	I <sub>L</sub> = 10 mA -1.250 $3V \le  V_{ N} - V_{OUT}  \le 35V$ -1.250 $10 mA \le I_L \le 3A, P \le P_{MAX}$ 0.01 $J_{OUT} = 50 mA (Note 4)$ 0.02   10 mA \le I_L \le 3A, P \le P_{MAX} 0.2   (Notes 4, 5) 0.4   10 ms Pulse 0.002   T <sub>MIN</sub> $\le T_J \le T_{MAX}$ 0.5   T_J = 125°C, 1000 Hours 0.2   10 mA $\le I_L \le 3A$ 2.5   70 65   70 65   10 mA $\le I_L \le 3A$ 2.5 $ V_{IN} - V_{OUT}  \le 35V$ 2.5 $ V_{IN} - V_{OUT}  \le 35V$ 2.5 $ V_{IN} - V_{OUT}  \le 35V$ 3.9 $ V_{IN} - V_{OUT}  = 20V$ 3.9 $ V_{IN} - V_{OUT}  = 30V$ 0.4   10 Hz to 10 kHz 0.003 $V_{OUT} = 10V, f = 120 Hz$ 60 $C_{ADJ} = 10 \mu F$ 60 $C_{ADJ} = 10 \mu F$ 77   163 163   TO-3 Package 1.2   TO-3 Package 35	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Electrical specifications do not apply when operating the device outside of its stated operating conditions.

Note 2: All limits are guaranteed at either room temperature (standard type face) or at temperature extremes (bold typeface) by production testing or correlation techniques using standard Statistical Quality Control (SQC) methods.

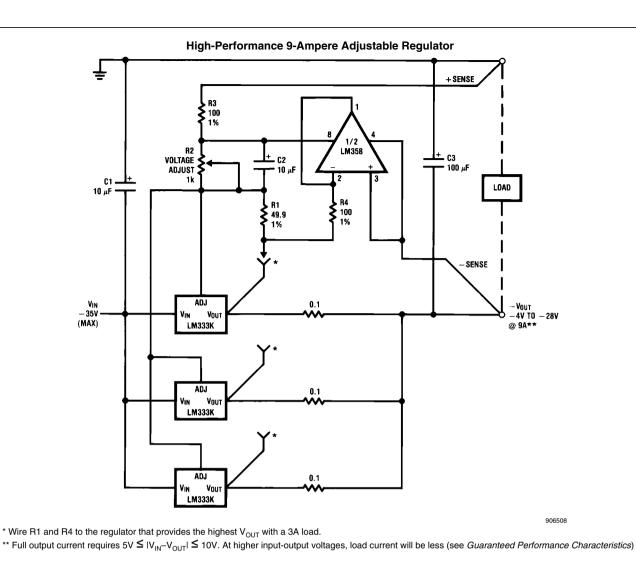
Note 3: Unless otherwise specified:  $|V_{IN} - V_{OUT}| = 5V$ ,  $I_{OUT} = 0.5A$ ,  $P_{DISS} \leq 30W$ .

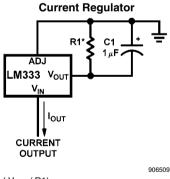
**Note 4:** Load and line regulation are measured at constant junction temperature, using low duty cycle pulse testing (output voltage changes due to heating effects are covered by the Thermal Regulation specification). For the TO-3 package, load regulation is measured on the output pin, below the base of the package. **Note 5:** The output current of the LM333 is guaranteed to be  $\geq$  3A in the range  $3V \leq |V_{IN} - V_{OUT}| \leq 10V$ . For the range  $10V \leq |V_{IN} - V_{OUT}| \leq 15V$ , the guaranteed minimum output current is equal to: 30/ ( $V_{IN} - V_{OUT}$ ). Refer to *Guaranteed Performance Characteristics* for guaranteed output currents at other voltages.



LM333

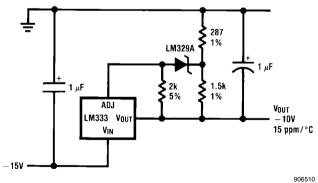


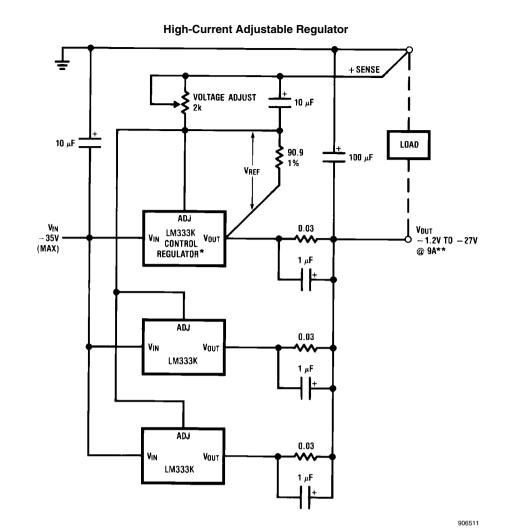


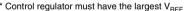


 $I_{OUT} = (V_{REF} / R1)$ \*  $0.4\Omega \leq R1 \leq 120\Omega$ 

High Stability 10V Regulator



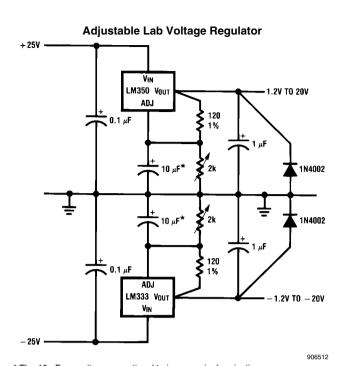


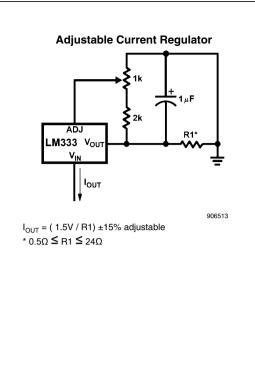


\* Control regulator must have the largest  $V_{REF}$ \*\* Full output current requires  $5V \le |V_{IN} - V_{OUT}| \le 10V$ . At higher input-output voltages, load current will be less (see *Guaranteed Performance Characteristics*)

LM333

LM333





 $^{\ast}$  The 10  $\mu F$  capacitors are optional to improve ripple rejection.

#### THERMAL REGULATION

When power is dissipated in an IC, a temperature gradient occurs across the IC chip affecting the individual IC circuit components. With an IC regulator, this gradient can be especially severe since the power dissipation is large. Thermal regulation is the effect of these temperature gradients on output voltage (in percentage output change) per watt of power change in a specified time. Thermal regulation error is independent of electrical regulation or temperature coefficient, and occurs within 5 ms to 50ms after a change in power dissipation. Thermal regulation depends on IC layout as well as electrical design. The thermal regulation of a voltage regulator is defined as the percentage change of  $V_{OUT}$ , per watt, within the first 10 ms after a step of power is applied. The LM333's specification is 0.01%/W, max.

In *Figure 1*, a typical LM333's output drifts only 2mV (or 0.02% of V<sub>OUT</sub> = -10V) when a 20W pulse is applied for 10 ms. This performance is thus well inside the specification limit of  $0.01\%/W\times20W = 0.2\%$  max. When the 20W pulse is ended, the thermal regulation again shows a 2 mV step as the LM333 chip cools off. Note that the load regulation error of about 1 mV (0.01%) is additional to the thermal regulation error. In *Figure 2*, when the 20W pulse is applied for 100 ms, the output

drifts only slightly beyond the drift in the first 10ms, and the thermal error stays well within 0.1% (10mV).

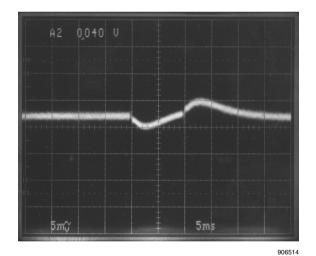
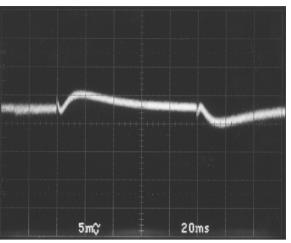
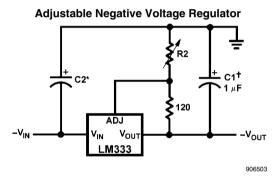


FIGURE 1.



906515





$$V_{OUT} = -1.25V \left(1 + \frac{R2}{120\Omega}\right) + \left(-I_{ADJ} \times R2\right)$$

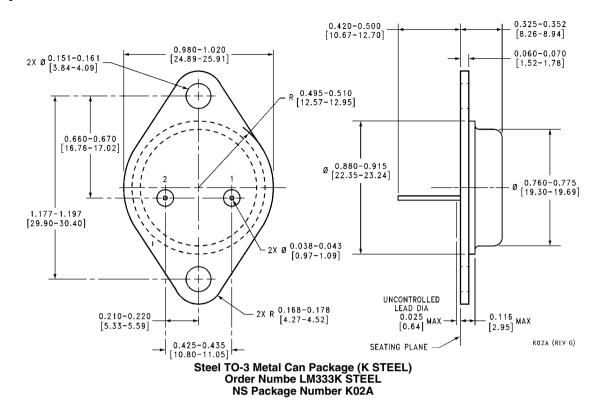
 $^{\dagger}$  C1 = 1  $\mu F$  solid tantalum or 10  $\mu F$  aluminum electrolytic required for stability.

\* C2 = 1 µF solid tantalum is required only if regulator is more than 4 from power supply filter capacitor.

Output capacitors in the range of 1  $\mu$ F to 1000  $\mu$ F of aluminum or tantalum electrolytic are commonly used to provide lower output impedance and improved transient response.

#### Physical Dimensions inches (millimeters) unless otherwise noted

LM333



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