



# LM2736 Evaluation Board

National Semiconductor  
Application Note 1418  
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October 2005

## Introduction

The LM2736 demo board is configured to convert 5V input to 1.5V output at 750 mA load current using the LM2736X 1.6 MHz or the LM2736Y 550 kHz step down DC-DC regulator. The tiny low profile thin SOT23 package allows the demo board to be manufactured using less than 1 square inch of a 4-layer printed circuit board.

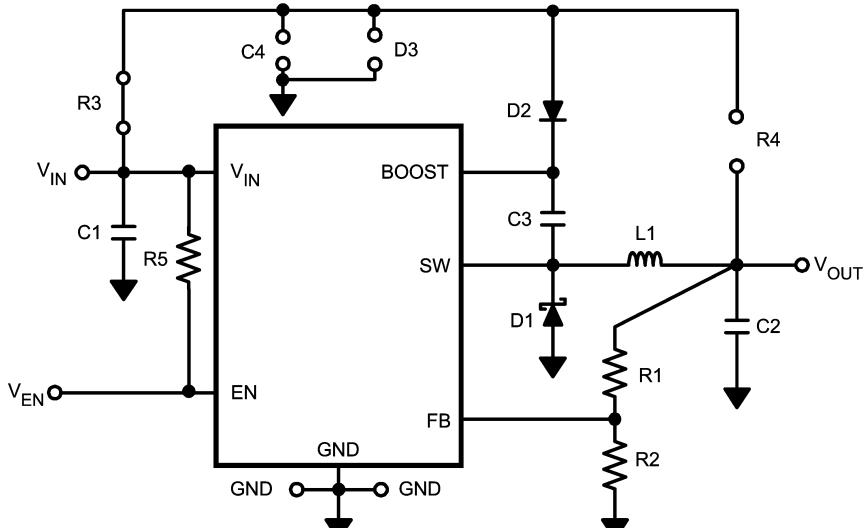
The circuit is configured with the boost diode connected to  $V_{IN}$ , and according to the datasheet,  $V_{IN}$  must not exceed the maximum operating limit of  $5.5V + V_{FID2}$  using this configuration. This will ensure that the voltage between the Boost and SW pins,  $V_{BOOST} - V_{SW}$ , does not exceed 5.5V for proper operation. Please see the LM2736 datasheet for more information regarding this requirement.

A bill of materials below describes the parts used on this demo board. A schematic and layout have also been in-

cluded below along with measured performance characteristics. The schematics at the end of this document show how to re-configure this demo board for various input and output conditions as discussed in the LM2736 datasheet. Short or leave open the indicated connection as indicated in the schematics. The above restrictions for the input voltage are valid only for the demo board as shipped with the demo board schematic below.

## Operating Conditions

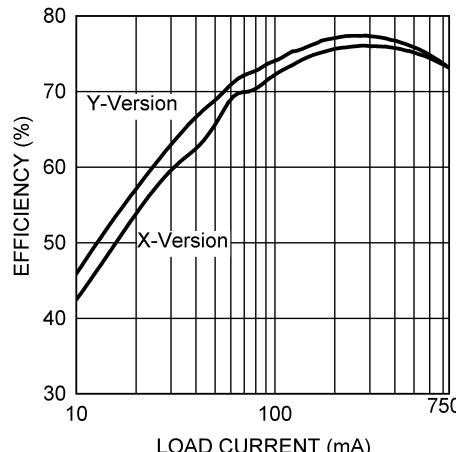
$V_{IN} = 5V$   
 $V_O = 1.5V$   
 $I_O = 750\text{ mA}$



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LM2736 Demo Board Schematic

## Operating Conditions (Continued)



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Efficiency vs Load Current

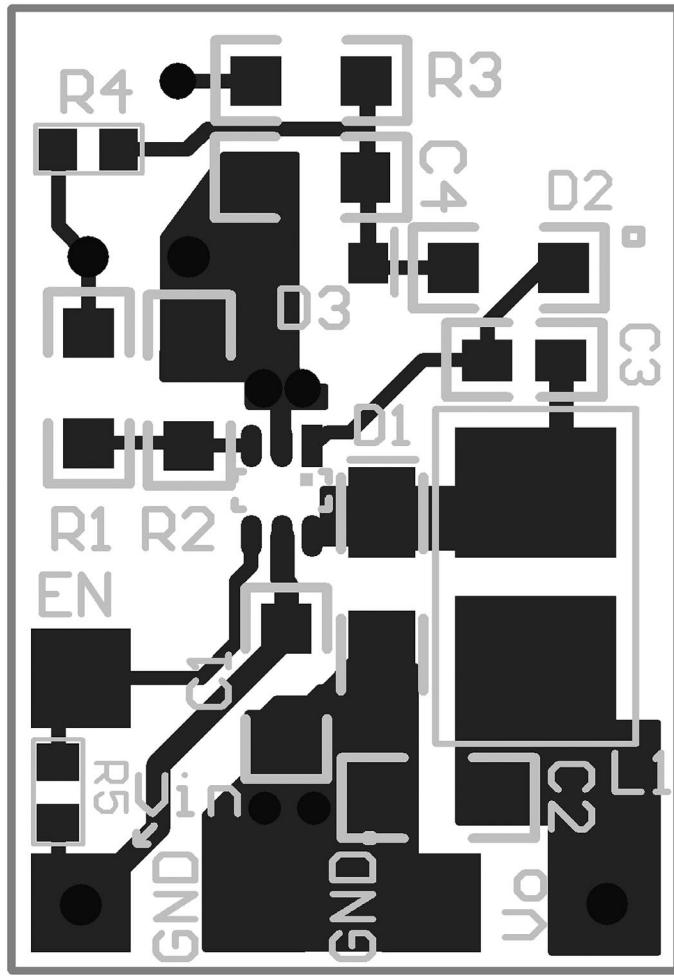
### Bill of Materials X-Version

Part ID	Part Value	Manufacturer	Part Number	Package Type
C1, Input Cap	4.7 $\mu$ F, 10V, X5R	Murata	GRM42-6X5R475K10	1206
C2, Output Cap	10 $\mu$ F, 6.3V, X5R	Murata	GRM42-6X5R106K6.3	1206
C3, Boost Cap	0.01 $\mu$ F	Vishay	VJ1206Y103KXXA	1206
D2, Boost Diode	1V <sub>f</sub> @ 50 mA Diode	Diodes, Inc.	1N4148W	SOD-123
R2	10 k $\Omega$ , 1%	Vishay	CRCW12061002F	1206
U1	750 mA Buck Regulator	National Semiconductor	LM2736X	Thin SOT23-6
D1, Catch Diode	0.34V <sub>f</sub> Schottky 1A, 20V <sub>R</sub>	International Rectifier	MBRA120	SMA
L1	4.7 $\mu$ H, 1.6A, 28 m $\Omega$	TDK	SLF6028T-4R7M1R6	6028
R1	2 k $\Omega$ , 1%	Vishay	CRCW12062001F	1206
R3	0 $\Omega$	Vishay	CRCW12060000F	1206
R5	50 k $\Omega$ , 1%	Vishay	CRCW08055002F	0805
D3, C4, R4	Open			

### Bill of Materials Y-Version

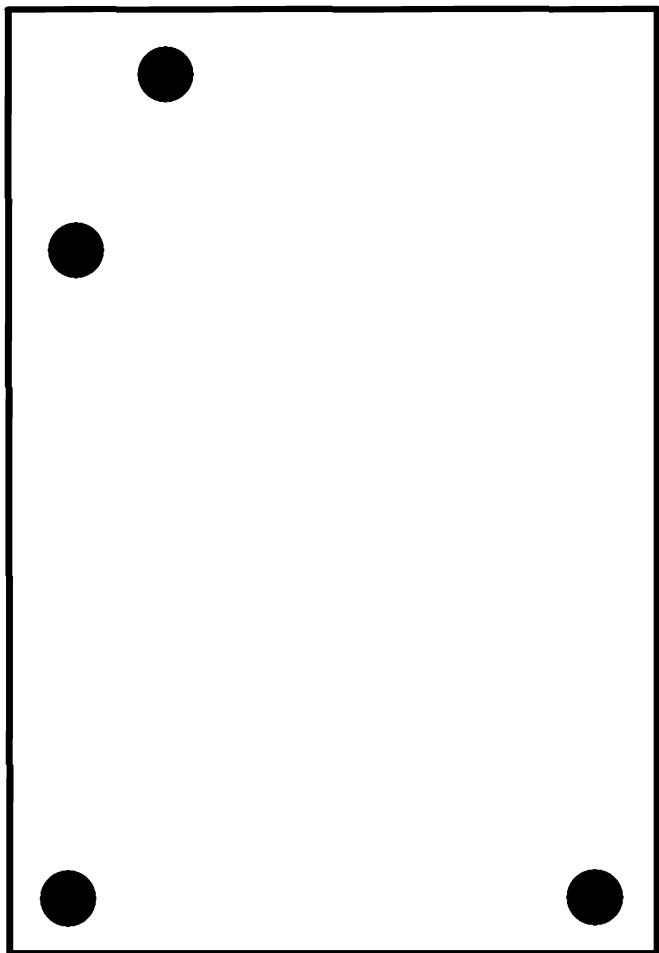
Part ID	Part Value	Manufacturer	Part Number	Package Type
C1, Input Cap	10 $\mu$ F, 10V, X5R	Murata	GRM42-6X5R106K10	1206
C2, Output Cap	10 $\mu$ F, 6.3V, X5R	Murata	GRM42-6X5R106K6.3	1206
C3, Boost Cap	0.01 $\mu$ F	Vishay	VJ1206Y103KXXA	1206
D2, Boost Diode	1V <sub>f</sub> @ 50 mA Diode	Diodes, Inc.	1N4148W	SOD-123
R2	10 k $\Omega$ , 1%	Vishay	CRCW12061002F	1206
U1	750 mA Buck Regulator	National Semiconductor	LM2736Y	Thin SOT23-6
D1, Catch Diode	0.34V <sub>f</sub> Schottky 1A, 20V <sub>R</sub>	International Rectifier	MBRA120	SMA
L1	10 $\mu$ H, 1.3A, 53 m $\Omega$	TDK	SLF6028T-100M1R3	6028
R1	2 k $\Omega$ , 1%	Vishay	CRCW12062001F	1206
R3	0 $\Omega$	Vishay	CRCW12060000F	1206
R5	50 k $\Omega$ , 1%	Vishay	CRCW08055002F	0805
D3, C4, R4	Open			

## PCB Layout



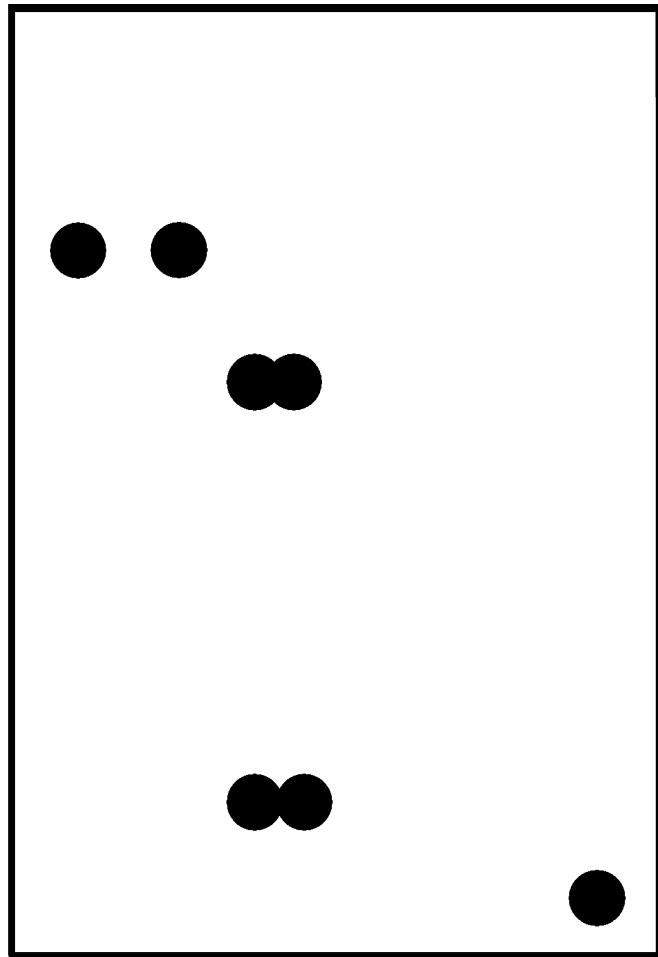
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Top Layer

**PCB Layout** (Continued)

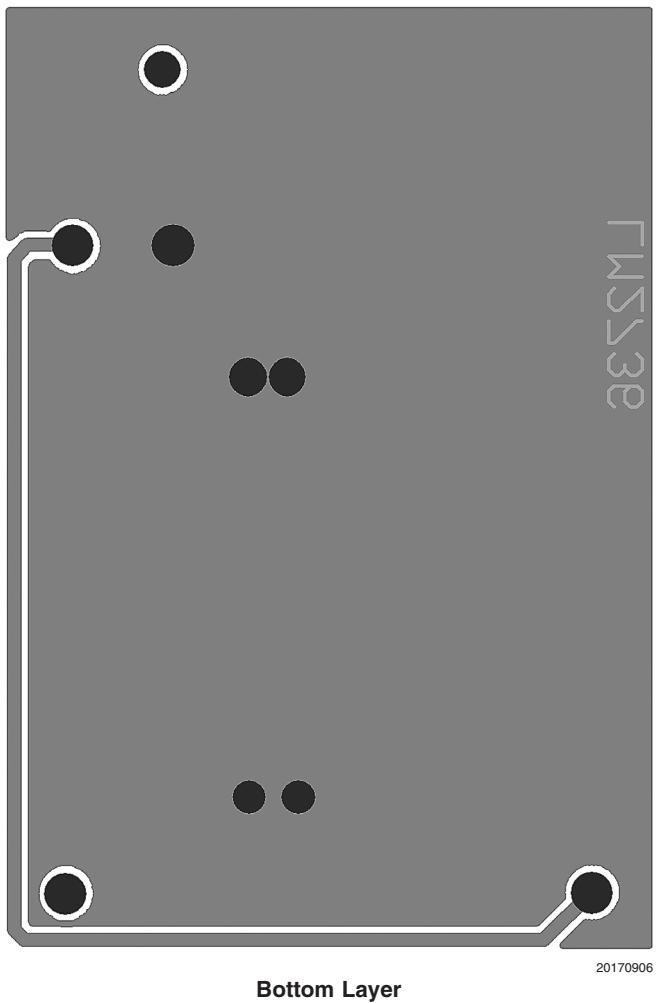
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**Internal Plane 1 (GND)**

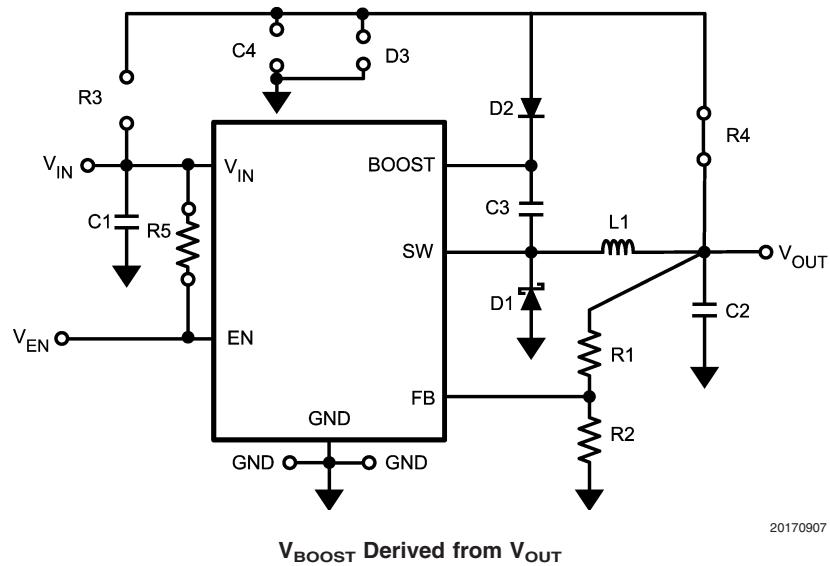


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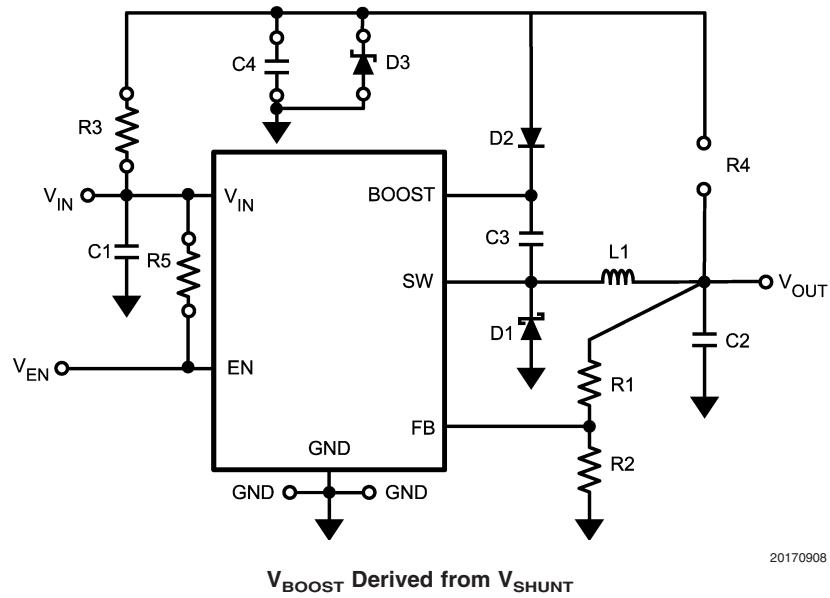
**Internal Plane 2 ( $V_{IN}$ )**

**PCB Layout** (Continued)

## Additional Circuit Configuration Schematics

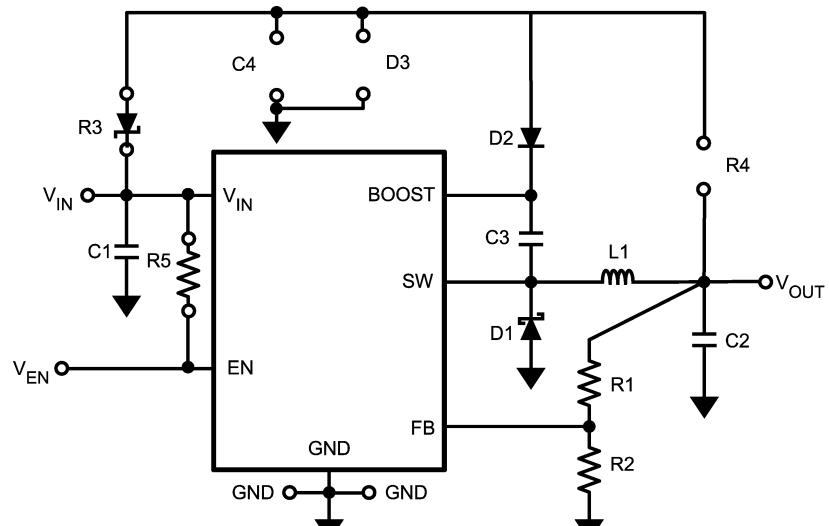


$V_{BOOST}$  Derived from  $V_{OUT}$

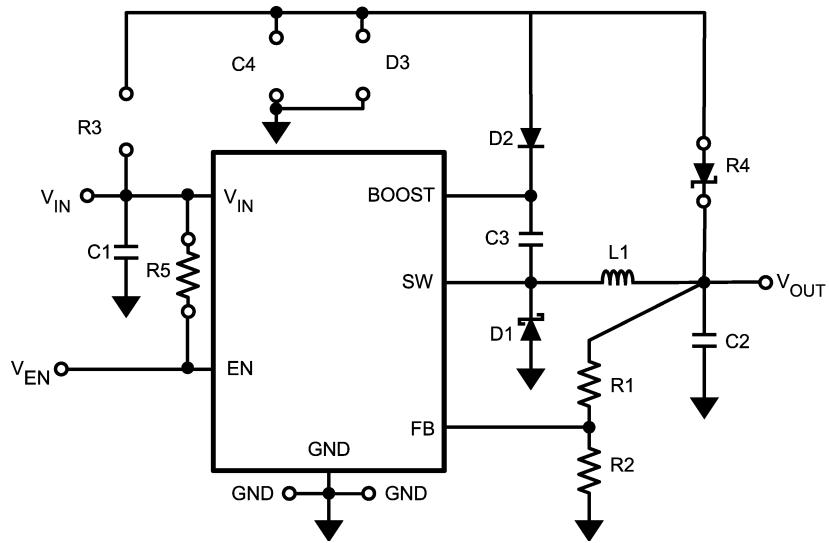


$V_{BOOST}$  Derived from  $V_{SHUNT}$

## Additional Circuit Configuration Schematics (Continued)



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 $V_{BOOST}$  Derived from Series Zener Diode ( $V_{IN}$ )

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 $V_{BOOST}$  Derived from Series Zener Diode ( $V_{OUT}$ )

## Notes

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