

Clock Oscillators Surface Mount Type KC3225A-C2 Series (K25-2C Series)



CMOS/ 2.5V/ 3.2×2.5mm



Ph Free

RoHS Compliant

Features

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output
- Supply voltage $V_{CC}=2.5V$
Lower voltage available
- $\pm 25 \times 10^{-6}$ available

Table 1

Freq. Tol.		Operating Temperature Range (°C)	Note
Code	$\times 10^{-6}$		
0	± 50	-10 to +70	Standard specifications
S	± 30		With only certain frequencies
U	± 25		
F	± 100		
G	± 50	-40 to +85	

How to Order

KC3225A 25.0000 C 2 0 E 00
① ② ③ ④ ⑤ ⑥ ⑦

- ① Type (3.2×2.5mm SMD)
- ② Output Frequency
- ③ Output Type (CMOS)
- ④ Supply Voltage (2.5V)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ Enable Function (45/ 55%, Stand-by)
- ⑦ Customer Special Model Suffix (STD Specification is "00")

Packaging (Tape & Reel 2000 pcs./ reel)

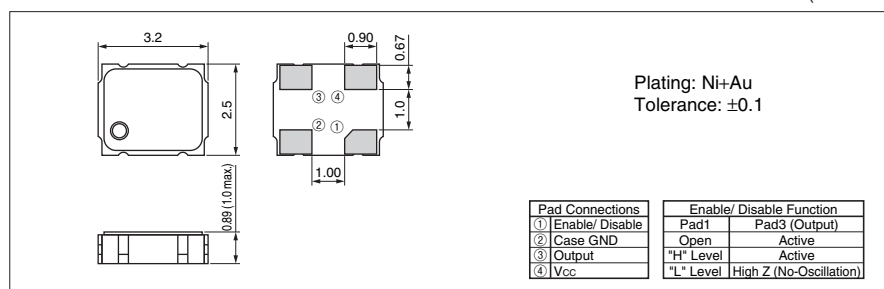
Specifications

Item	Symbol	Conditions		Min.	Max.	Units
Output Frequency Range	f _o			1.5	125	MHz
Frequency Tolerance	f _{tol}	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	Op. Temp.: -40 to +85°C	-100	+100	×10 ⁻⁶
			Op. Temp.: -10 to +70°C/ -40 to +85°C	-50	+50	
			Op. Temp.: -10 to +70°C	-30	+30	
			Op. Temp.: -10 to +70°C	-25	+25	
Storage Temperature Range	T _{stg}			-55	+125	°C
Operating Temperature Range	T _{use}	Standard Specifications		-10	+70	°C
		Extend (Option)		-40	+85	
Max. Supply Voltage	—			-0.5	+7	V
Supply Voltage	V _{CC}	Freq. Tol.Code: 0, S, F		2.38	2.62	V
		Freq. Tol.Code: U, G		2.43	2.57	
Current Consumption (Maximum Loaded)	I _{CC}	1.5≤f _o ≤26MHz		—	4	mA
		26<f _o ≤50MHz		—	6	
		50<f _o ≤67.5MHz		—	9	
		67.5<f _o ≤95MHz		—	14	
		95<f _o ≤125MHz		—	18	
Stand-by Current	I _{std}			—	10	μA
Symmetry	SYM	@ 50% V _{CC}		45	55	%
Rise/ Fall Time (10% V _{CC} to 90% V _{CC} Maximum Loaded)	tr/ tf	1.5≤f _o ≤67.5MHz		—	6	nS
		67.5<f _o ≤125MHz		—	4	
Low Level Output Voltage	V _{OL}	I _{OL} =4mA		—	10% V _{CC}	V
High Level Output Voltage	V _{OH}	I _{OH} =-4mA		90% V _{CC}	—	V
CMOS Load	L _{CMOS}	CMOS Output		—	15	pF
Input Voltage Range	V _{IN}			0	V _{CC}	V
Low Level Input Voltage	V _{IL}			—	30% V _{CC}	V
High Level Input Voltage	V _{IH}			70% V _{CC}	—	V
Disable Time	t _{dis}			—	150	nS
Enable Time	t _{ena}			—	5	mS
Start-up Time	t _{str}	@ Minimum operation voltage to be 0 sec.		—	10	mS
1 Sigma Jitter	J _{Sigma}	Measured with Wavecrest DTS-2079 VISI 6.3.1	1.5≤f _o ≤60MHz	—	8	pS
			60<f _o ≤125MHz	—	5	pS
Peak to Peak Jitter	J _{PK-PK}		1.5≤f _o ≤60MHz	—	80	pS
			60<f _o ≤125MHz	—	40	pS

Note: All electrical characteristics are defined at the maximum load and operating temperature range.
Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Dimensions

(Unit: mm)



Recommended Land Pattern

(Unit: mm)

