

EMD22 / UMD22N

- 1) Both the D1A143Z chip and DTC143Z chip in an EMT or UMT package.
- 2) Mounting possible with EMT3 or UMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

A PNP and NPN digital transistor
(each with a single built in resistor)

Abbreviated symbol : D22

*120mW per element must not be exceeded

$R_1=4.7k\Omega$
 $R_2=47k\Omega$

Transistors

●Electrical characteristics (Ta=25°C)

DTr 1

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	—	—	0.5	V	$V_{CC}=5V$, $I_O=100\mu A$
	$V_{I(on)}$	1.3	—	—		$V_O=0.3V$, $I_O=5mA$
Output voltage	$V_{O(on)}$	—	0.1	0.3	V	$I_O/I_I=5mA/0.25mA$
Input current	I_I	—	—	1.8	mA	$V_I=5V$
Output current	$I_{O(off)}$	—	—	0.5	μA	$V_{CC}=50V$, $V_I=0V$
DC current gain	G_I	80	—	—	—	$V_O=5V$, $I_O=10mA$
Input resistance	R_I	3.29	4.7	6.11	$k\Omega$	—
Resistance ratio	R_2/R_1	8	10	12	—	—
Transition frequency	f_T	—	250	—	MHz	$V_{CE}=10V$, $I_E=5mA$, $f=100MHz$ *

*Transition frequency of the device

DTr 2

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	—	—	−0.5	V	$V_{CC}=-5V$, $I_O=-100\mu A$
	$V_{I(on)}$	−1.3	—	—		$V_O=-0.3V$, $I_O=-5mA$
Output voltage	$V_{O(on)}$	—	−0.1	−0.3	V	$I_O/I_I=-5mA/-0.25mA$
Input current	I_I	—	—	−1.8	mA	$V_I=-5V$
Output current	$I_{O(off)}$	—	—	−0.5	μA	$V_{CC}=-50V$, $V_I=0V$
DC current gain	G_I	80	—	—	—	$V_O=-5V$, $I_O=-10mA$
Input resistance	R_I	3.29	4.7	6.11	$k\Omega$	—
Resistance ratio	R_2/R_1	8	10	12	—	—
Transition frequency	f_T	—	250	—	MHz	$V_{CE}=-10V$, $I_E=5mA$, $f=100MHz$ *

*Transition frequency of the device

●Packaging specifications

Type	Package	Taping	
	Code	T2R	TR
	Basic ordering unit (pieces)	8000	3000
EMD22		○	—
UMD22N		—	○

Transistors

●Electrical characteristic curves

DTr 1

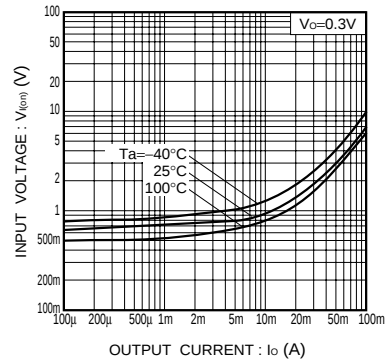


Fig.1 Input voltage vs. output current (ON characteristics)

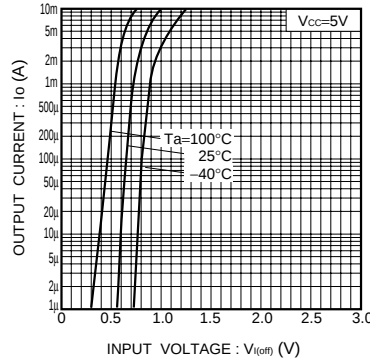


Fig.2 Output current vs. input voltage (OFF characteristics)

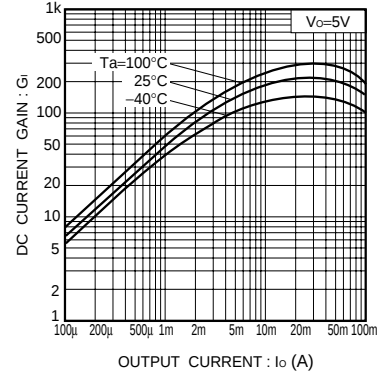


Fig.3 DC current gain vs. output current

DTr 2

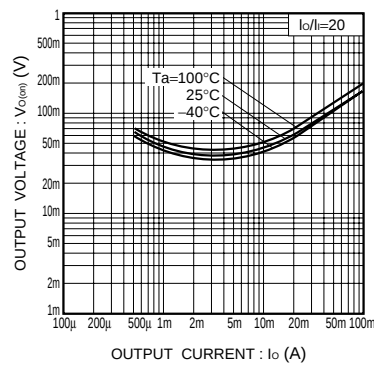


Fig.4 Output voltage vs. output current

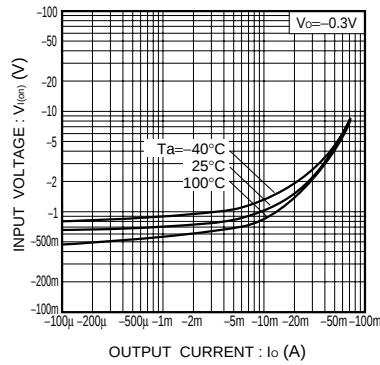


Fig.5 Input voltage vs. output current (ON characteristics)

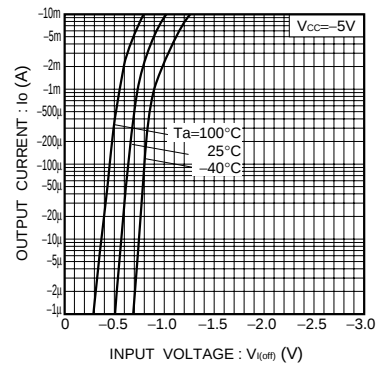


Fig.6 Output current vs. input voltage (OFF characteristics)

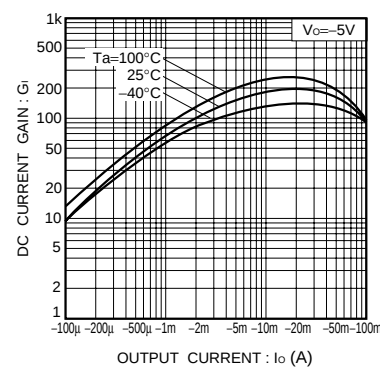


Fig.7 DC current gain vs. output current

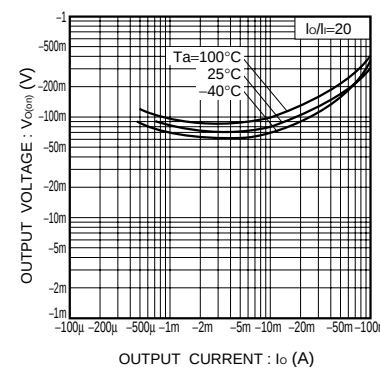


Fig.8 Output voltage vs. output current

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