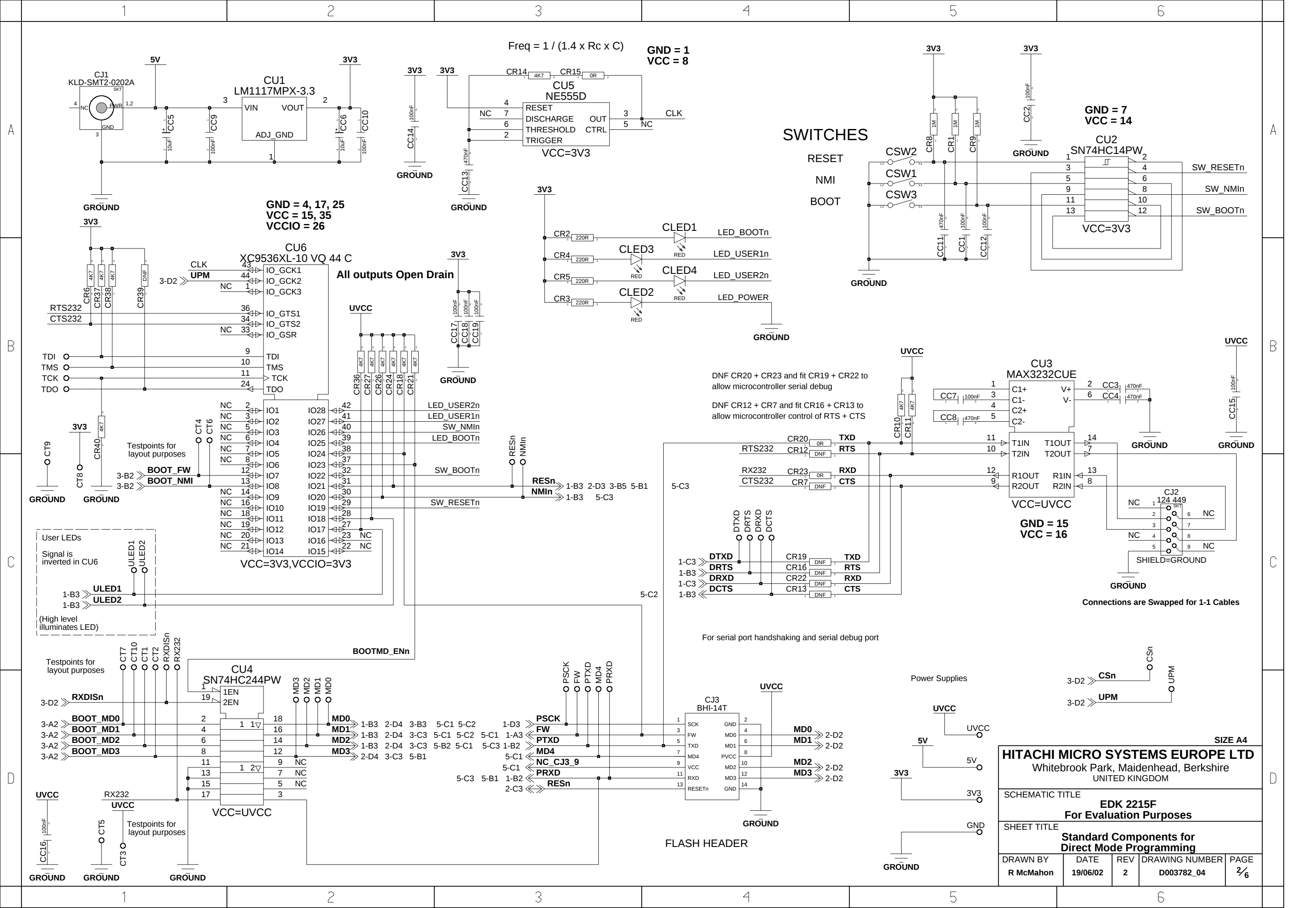




A

B

C

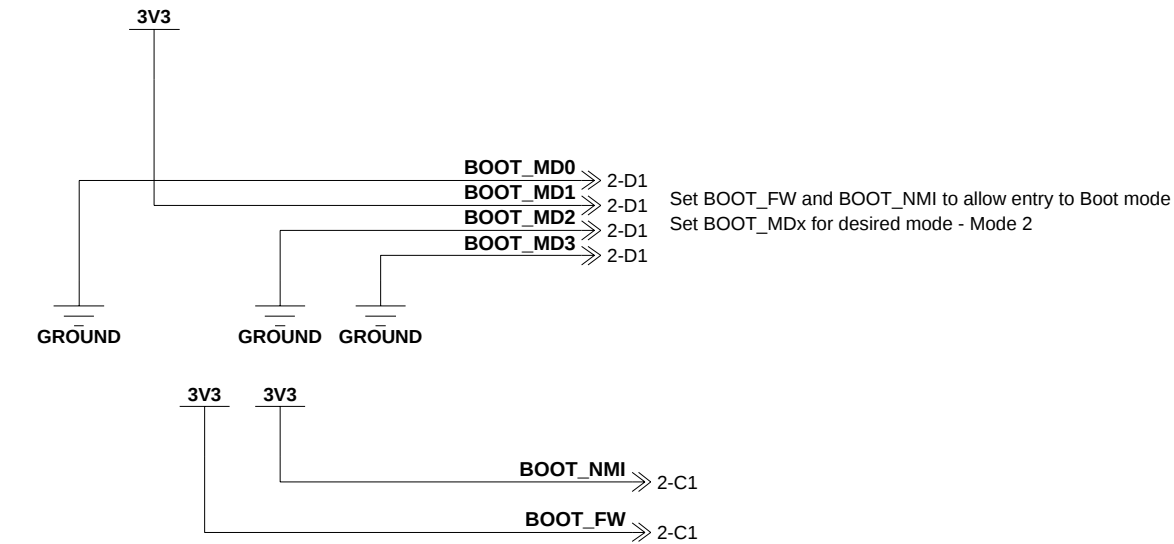
D

A

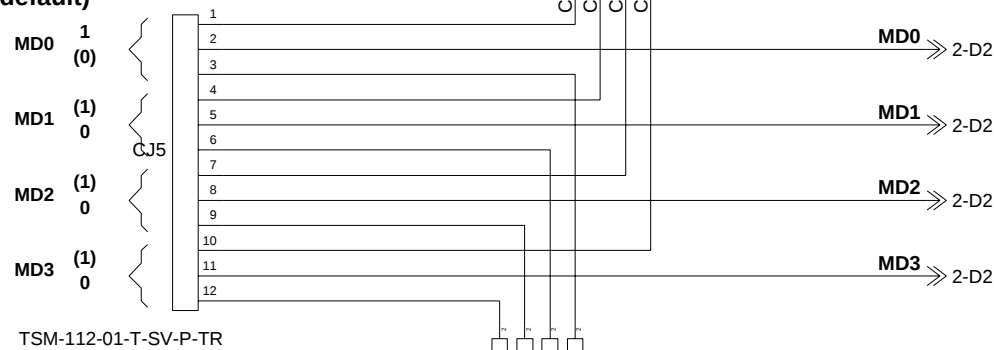
B

C

D



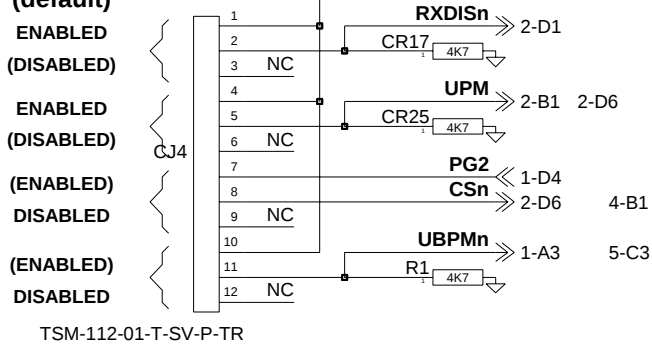
Jumper Block for User Mode (default)



Default Mode for EDK2215 is Mode 6

MD3 is not implemented on H8S/2215

Jumper Block for General Use (default)



RXDISn

Normally flash header is disabled
Fit jumper to pins 1-2 to enable flash header and disable RS232 comms

UPM

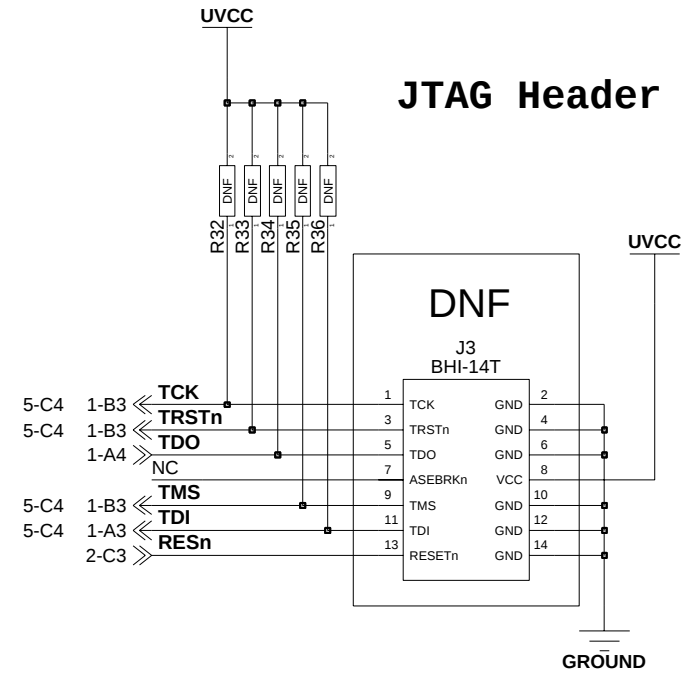
In case of FWE on device, input HIGH to tri-state the FW, causing User Program Mode transition

CSn

SRAM can be enabled or disabled via CS2n signal
By default the SRAM is enabled

UBPMn

Selects between self-powered mode and bus-powered mode.
Fit jumper to pins 10-11 for Self-powered mode (Default setting)



SIZE A4

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UNITED KINGDOM

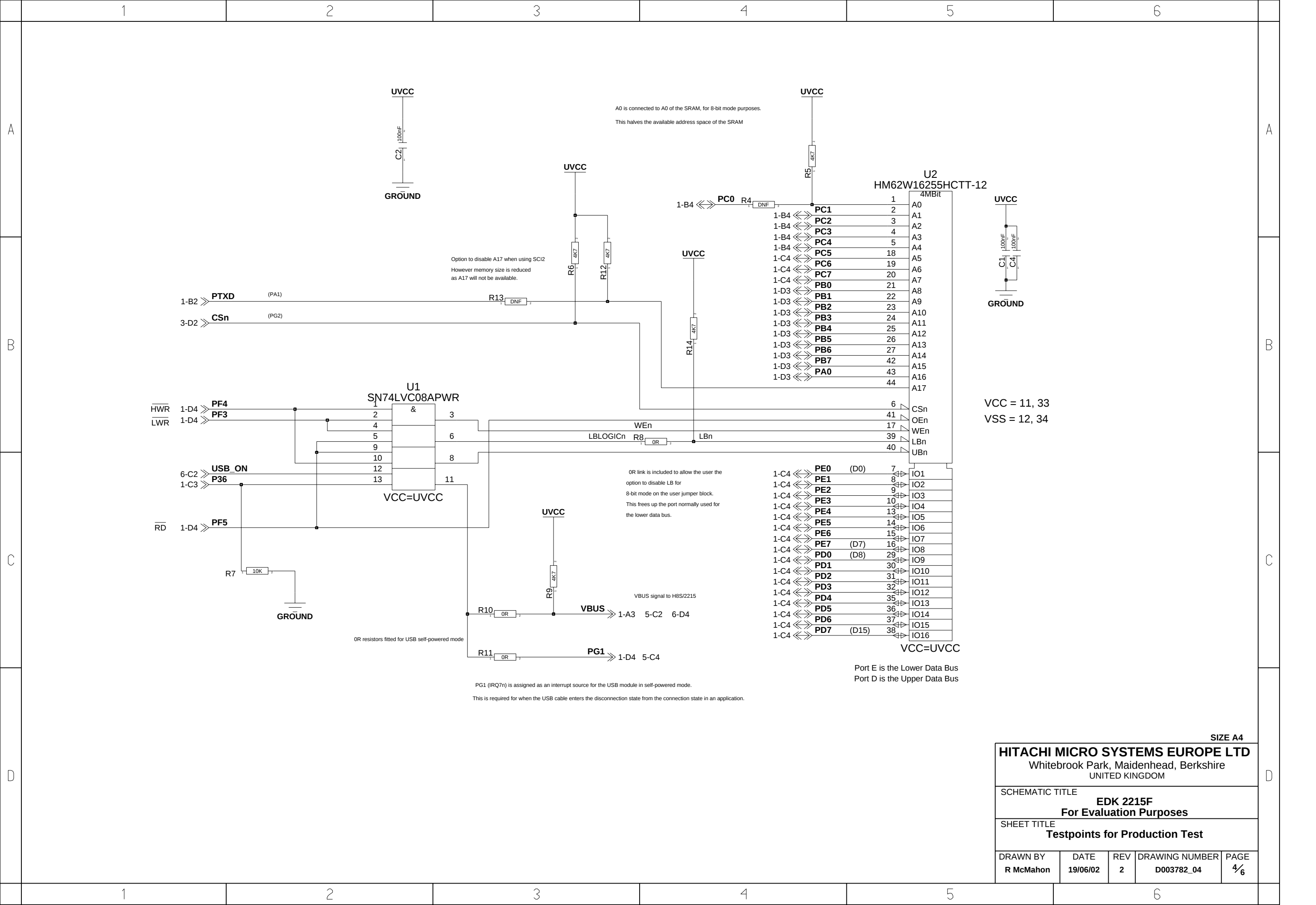
SCHEMATIC TITLE

EDK 2215F
For Evaluation Purposes

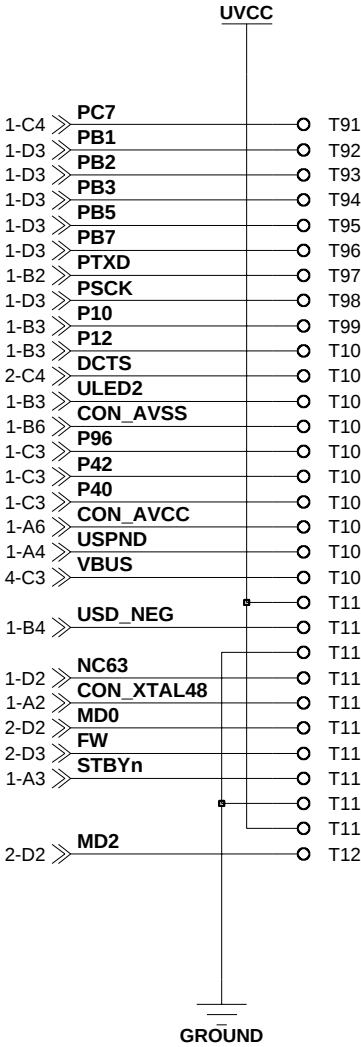
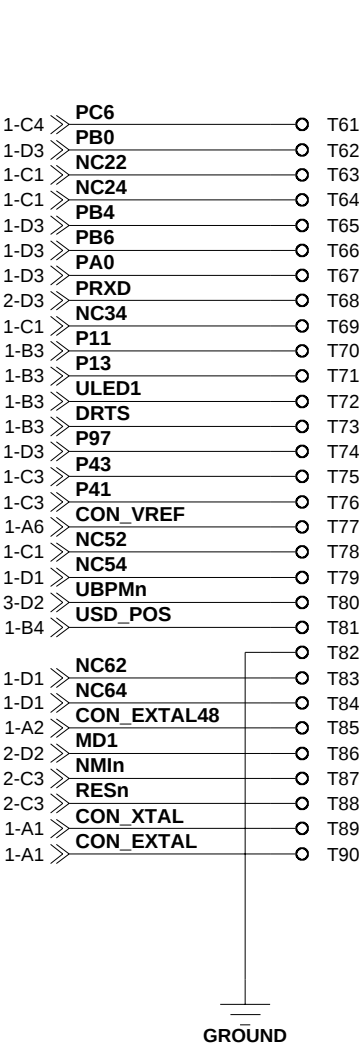
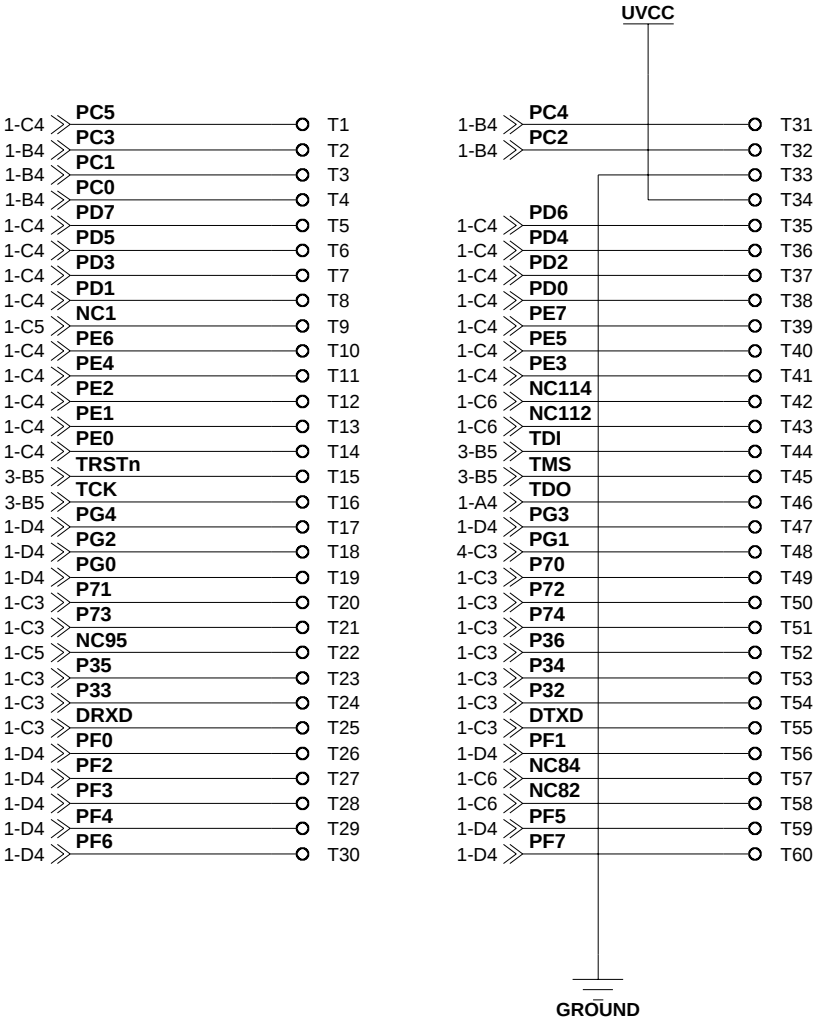
SHEET TITLE

Programming and Config Headers and LCD

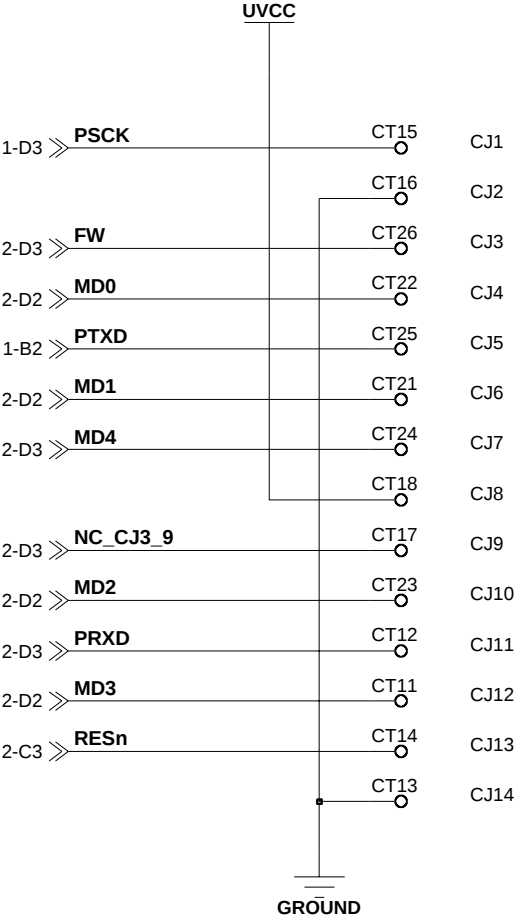
DRAWN BY	DATE	REV	DRAWING NUMBER	PAGE
R McMahon	19/06/02	2	D003782_04	3/6



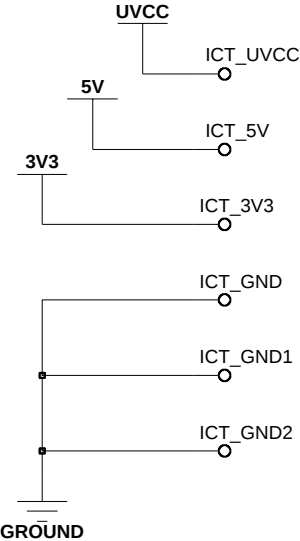
Microcontroller Expansion Connector Test Points



RS232 Programming Header CJ3 Testpoints

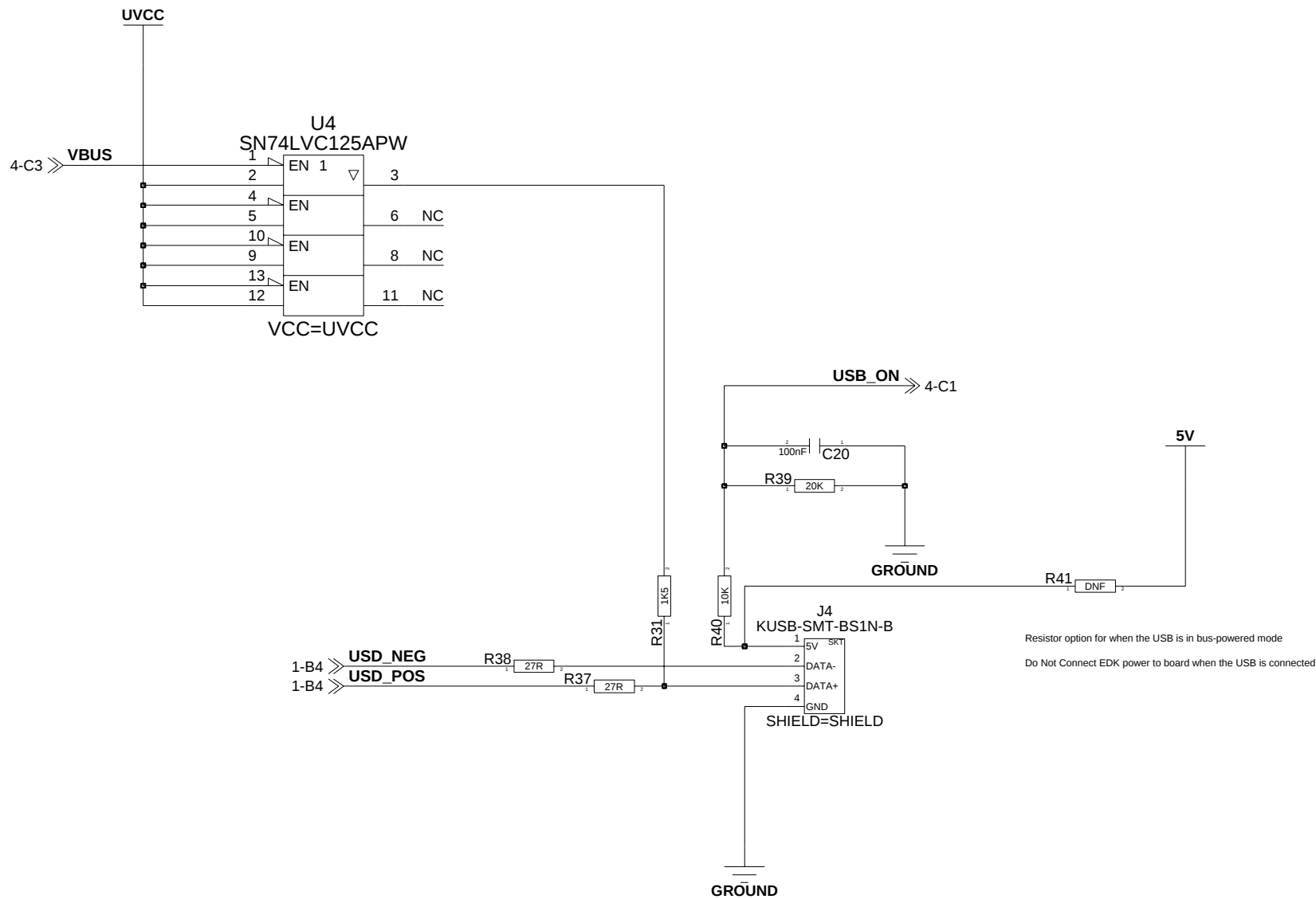


ALL TESTPOINTS ON UNDERSIDE OF PCB AND NOT MARKED FOR MANUFACTURING USE ONLY



ICT Power Injection

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SCHEMATIC TITLE				
EDK 2215F				
For Evaluation Purposes				
SHEET TITLE				
Testpoints for Production Test				
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UNITED KINGDOM				
SCHEMATIC TITLE				
EDK 2215F				
For Evaluation Purposes				
SHEET TITLE				
USB Circuitry				
DRAWN BY	DATE	REV	DRAWING NUMBER	PAGE
R McMahon	19/06/02	2	D003782_04	6/6