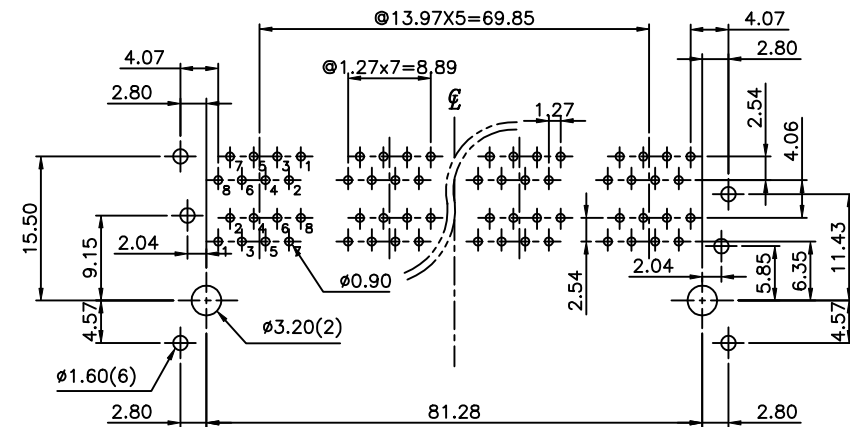
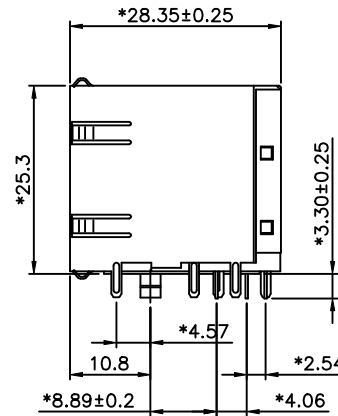
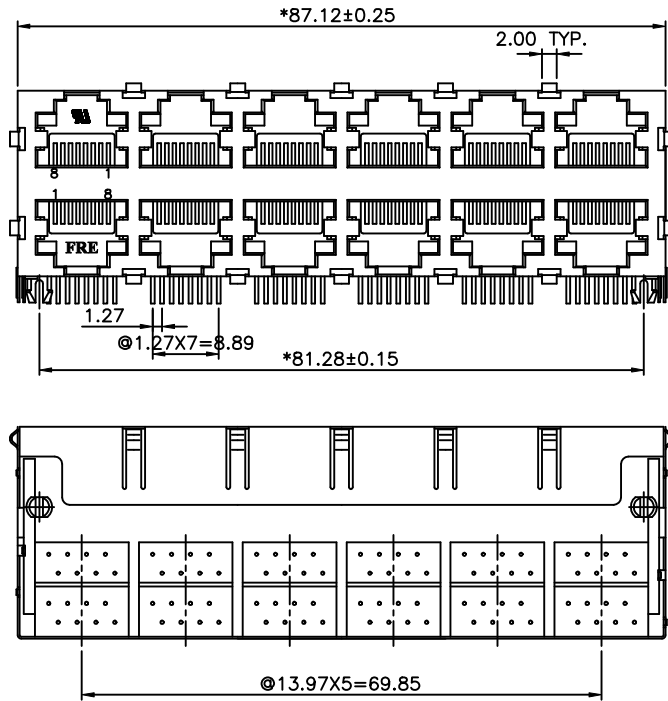


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



PC Board Layout
Component Side Shown

NOTES:

ELECTRICAL:

1. VOLTAGE RATING : 125 VAC RMS.
2. CURRENT RATING : 1.5 AMP.
3. CONTACT RESISTANCE : 50 MILLIOHMS MAX.
4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
5. DIELECTRIC STRENGTH : 1000 VAC RMS 60Hz, 1MIN.

MECHANICAL:

1. HOUSING MATERIAL : GLASS FILLED POLYESTER UL94V-0.
2. CONTACT MATERIAL : PHOSPHOR BRONZE t=0.35mm.
3. PLATING : GOLD PLATING OVER NICKEL.
4. OPERATING LIFE : 750 CYCLES MIN.
5. PCB RETENTION PRE-SOLDER : 1 LB MIN.
6. PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

1. STORAGE : -40°C TO +85°C.
2. OPERATION: 0°C TO 70°C.
3. WAVE SOLDERING TEMPERATURE 255°C~265°C (5~10 SECONDS)

MATES WITH MODULAR PLUG CONFORMING TO
FCC PART 68, SUBPART F.

ROHS PART NUMBER: E5908-5XE15X-L

Shield Plating:

1- Tin
7- Nickel
Q- GOLD

Gold Thickness:

1- 3u" 2- 6u" 3-15u"
4-30u" 5-50u"

DETACHED LISTS	MM (INCH)	DFTO JAMY	DATE 7/26/05	FULL RISE ELECTRONIC CO., LTD	
	TOLERANCES EXCEPT AS NOTED	CHKD JAMY	DATE 8/20/05		
	MM	MFO	DATE		
	.0 ±0.2	APPL LUSHKO	DATE 8/20/05		
	.00 ±0.15				
	.000 ±0.075			TITLE RJ45 STACKED GANGED JACK, 2X6	
	ANGLES ± 0.5	QT'Y :		DRAWING NO. GE593267-LA	
	THIRD ANGLE PROJECTION	FINISH :		/PART NO. SEE NOTE	
		SCALE : 1.5 : 1		DO NOT SCALE DRAWING	
				SHEET OF	