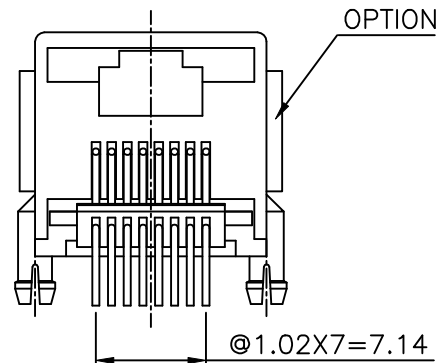
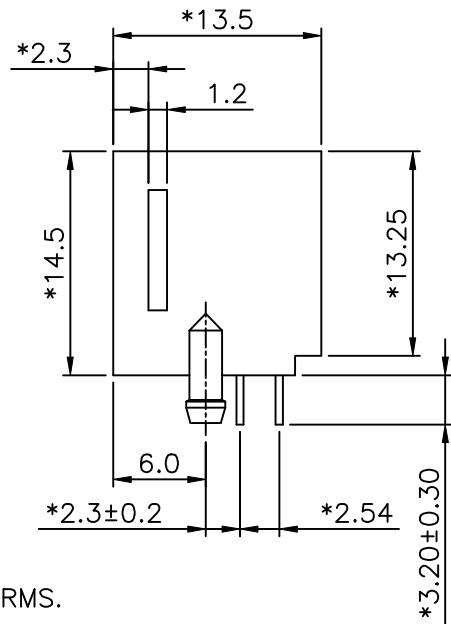
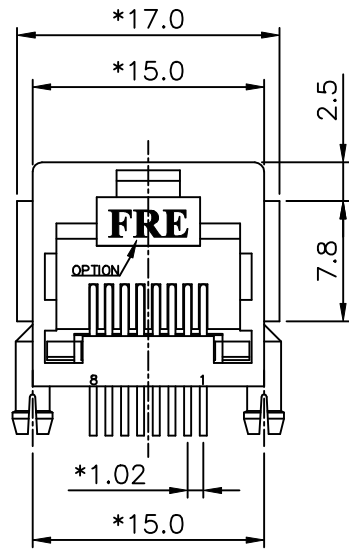


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD
△		ADD P/N	JAMY 4/22/06	



NOTES:

ELECTRICAL:

1. VOLTAGE RATING : 125 VAC RMS.
2. CURRENT RATING : 1.5 AMP.
3. CONTACT RESISTANCE : 30 MILLIOHMS MAX.
4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
5. DIELECTRIC STRENGTH : 1000 VAC RMS 60Hz, 1MIN.

MECHANICAL:

1. HOUSING AND INSERT MATERIAL: NYLON+30%GF(FR52) UL94V-0.
2. CONTACT MATERIAL : PHOSPHOR BRONZE $\phi 0.46\text{mm}$.
3. PLATING : GOLD PLATING OVER NICKEL.
4. OPERATING LIFE : 750 CYCLES MIN.
5. PCB RETENTION PRE-SOLDER : 1 LB MIN.
6. PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

1. STORAGE : -40°C TO $+85^{\circ}\text{C}$.
2. OPERATION: : -40°C TO $+85^{\circ}\text{C}$.
3. WAVE SOLDERING TEMPERATURE: $255\sim 265^{\circ}(5\sim 10\text{ SECONDS})$.

MATES WITH MODULAR PLUG CONFORMING TO
FCC PART 68, SUBPART F.

CUL FILE NO. E163191

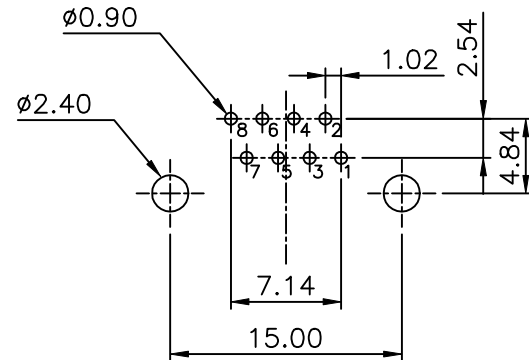
PART NUMBER: E558X-20LXXX-L

NO. OF CONTACT:

2- 2C 4- 4C
6- 6C 8- 8C

1- 3u" 2- 6u"
3- 15u" 4- 30u"
5- 50u" 6- Pd 20u"

U1-W/ PANEL STOP BK W/O LOGO
U2-W/O PANEL STOP BK W/O LOGO
△UP-W/O PANEL STOP GY W/FRE LOGO



PC Board Layout Component Side Shown

DETACHED LISTS	MM (INCH)		DFTO QSJ	DATE 3/31/05'	FULL RISE ELECTRONIC CO., LTD		
	TOLERANCES EXCEPT AS NOTED		CHKD YICAN	DATE 2006/4/22			
			MFO	DATE			
			APPVILLUSHENG	DATE 2006/4/22			
	MM		MATERIAL :			TITLE MINI L/P SIDE ENTRY PCB JACK 8P	
	.0 ± 0.2	±	QTY :				
	.00 ± 0.15	±	FINISH :			DRAWING NO. GE553003-LA	
	.000 ± 0.075	±	SCALE : 3:1			/PART NO. SEE NOTE	
	ANGLES ± 0.5		DO NOT SCALE DRAWING			SHEET	OF
	 THIRD ANGLE PROJECTION					SIZE A3	
					REV 1		