

## TOSHIBA Schottky Barrier Diode

## CLS01

Switching-Mode Power Supply (Secondary-Rectification)  
Applications (Low Voltage)

DC/DC Converter Applications

- Forward voltage:  $V_{FM} = 0.47 \text{ V (max)}$
- Average forward current:  $I_F (AV) = 10 \text{ A}$
- Repetitive peak reverse voltage:  $V_{RRM} = 30 \text{ V}$
- Suitable for compact assembly due to small surface-mount package:  
“L-FLAT™” (Toshiba package name)

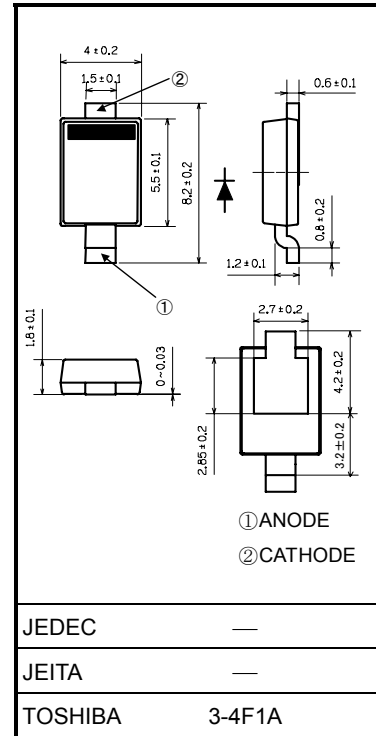
Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	$V_{RRM}$	30	V
Average forward current	$I_F (AV)$	10 (Note)	A
Non-repetitive peak surge current	$I_{FSM}$	100 (50 Hz)	A
Junction temperature	$T_j$	$-40 \sim 125$	$^\circ\text{C}$
Storage temperature range	$T_{stg}$	$-40 \sim 150$	$^\circ\text{C}$

Note:  $T_l = 88^\circ\text{C}$

Rectangular waveform ( $\alpha = 180^\circ$ ),  $V_R = 15 \text{ V}$

Unit: mm



Weight: 0.15 g (typ.)

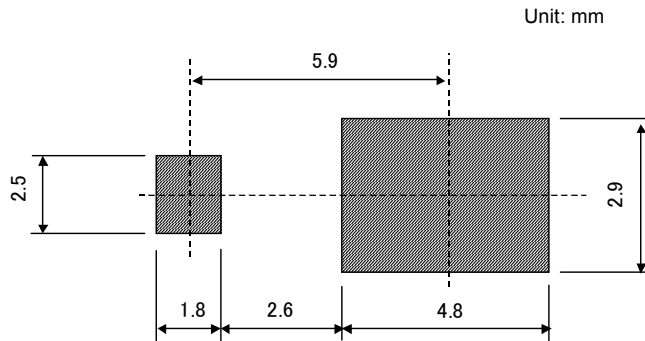
Electrical Characteristics ( $T_a = 25^\circ\text{C}$ )

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Peak forward voltage	$V_{FM} (1)$	$I_{FM} = 3.0 \text{ A (pulse test)}$	—	0.34	—	V
	$V_{FM} (2)$	$I_{FM} = 5.0 \text{ A (pulse test)}$	—	0.38	—	
	$V_{FM} (3)$	$I_{FM} = 10 \text{ A (pulse test)}$	—	0.45	0.47	
Peak repetitive reverse current	$I_{RRM} (1)$	$V_{RRM} = 5 \text{ V (pulse test)}$	—	8.0	—	$\mu\text{A}$
	$I_{RRM} (2)$	$V_{RRM} = 30 \text{ V (pulse test)}$	—	0.1	1.0	mA
Junction capacitance	$C_j$	$V_R = 10 \text{ V, } f = 1.0 \text{ MHz}$	—	530	—	pF
Thermal resistance (junction to ambient)	$R_{th (j-a)}$	Device mounted on a glass-epoxy board (board size: 50 mm × 50 mm) (board thickness: 1.6 t) (soldering land) Cathode 5.7 mm × 6.2 mm Anode 4.5 mm × 3.4 mm	—	—	100	$^\circ\text{C/W}$
Thermal resistance (junction to lead)	$R_{th (j-l)}$	—	—	—	5	$^\circ\text{C/W}$

## Marking

Abbreviation Code	Part No.
S01	CLS01

## Standard Soldering Pad



## Handling Precautions

- 1) Schottky barrier diodes have reverse current characteristics compared to other diodes. There is a possibility that SBDs will cause thermal runaway when used under high-temperature or high-voltage conditions. Be sure to take forward and reverse loss into consideration during design.
- 2) The maximum ratings denote the absolute maximum ratings, which are rated values that must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend for when designing a circuit incorporating this device.

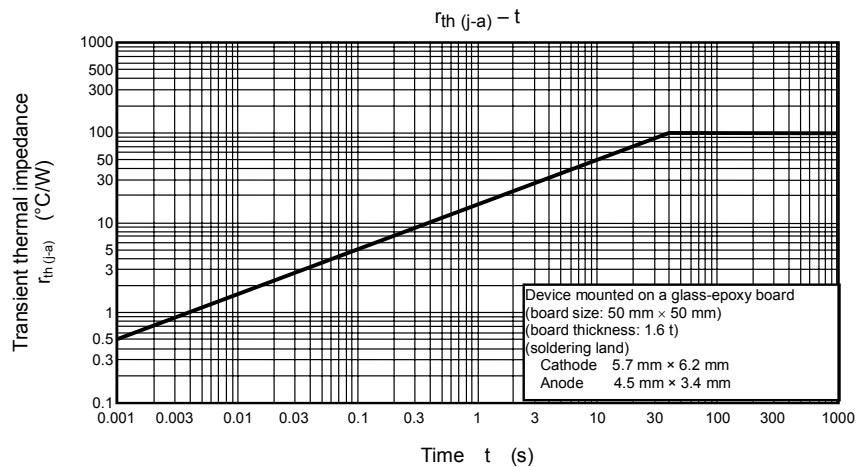
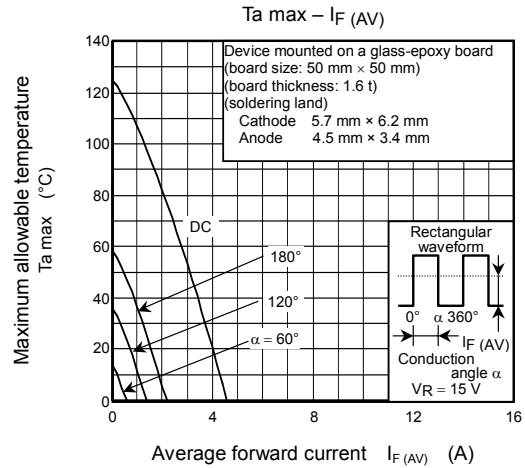
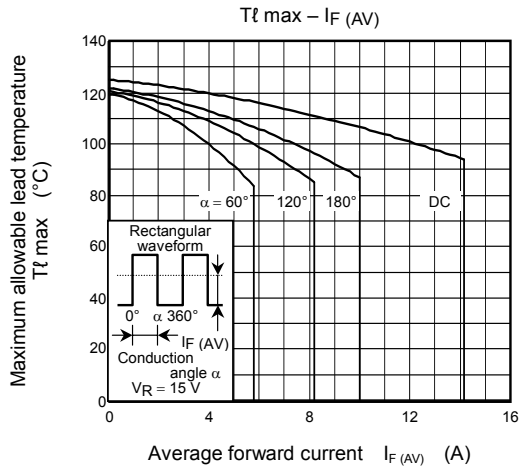
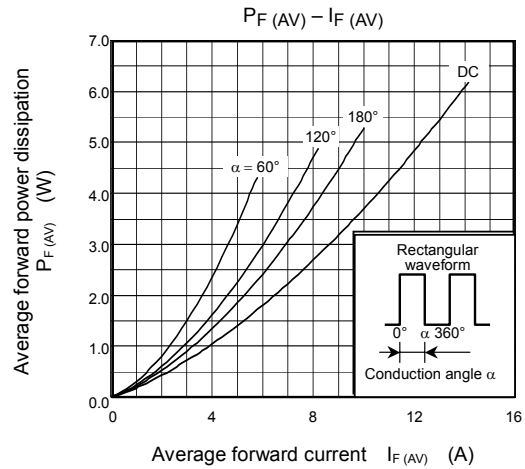
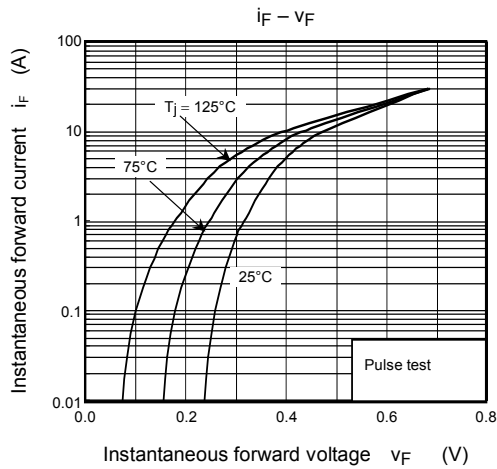
**VRRM:** Use this rating with reference to (1) above. VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account when designing a device for operation at low temperatures.

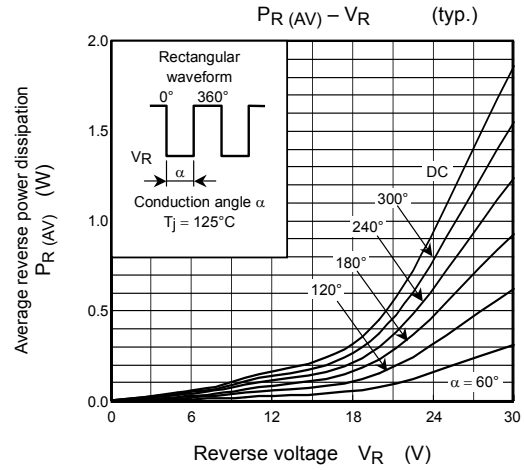
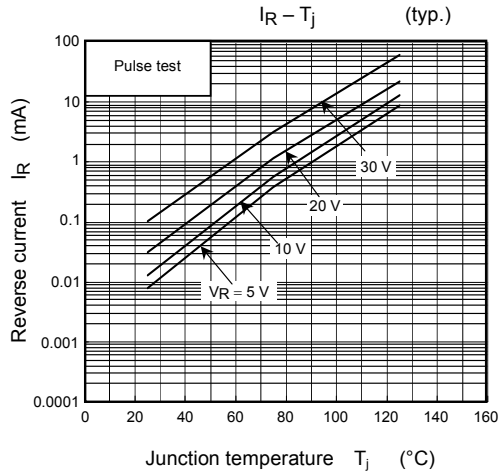
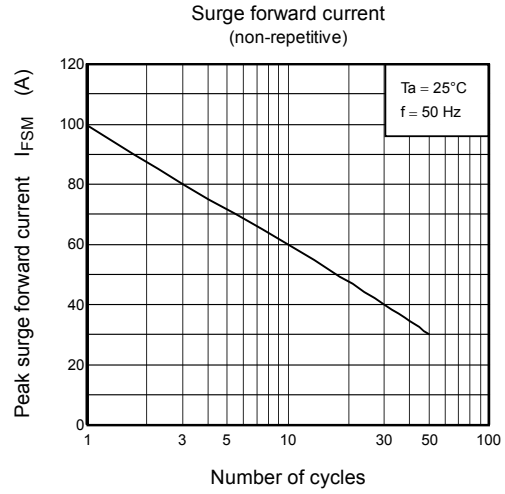
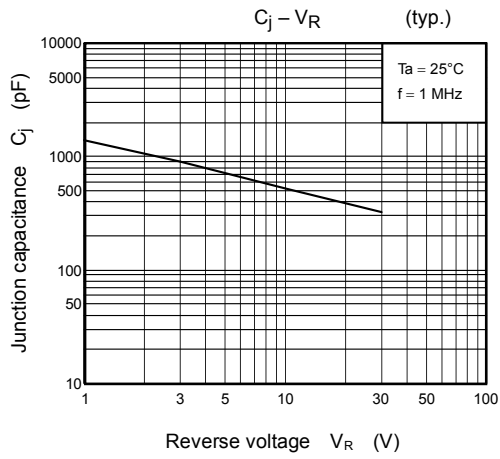
**IF (AV):** We recommend that the worst case current be no greater than 80% of the maximum rating of IF (AV) and that  $T_j$  be below 100°C. When using this device, take the margin into consideration by using an allowable  $T_{max}$ -IF (AV) curve.

**IFSM:** This rating specifies the non-repetitive peak current. This applies only to abnormal operation, which seldom occurs during the lifespan of the device.

**$T_j$ :** Derate this rating when using the device in order to ensure high reliability. We recommend that the device be used at a  $T_j$  of below 100°C.

- 3) Thermal resistance between junction and ambient fluctuates depending on the mounting condition of the device. When using the device, design the circuit board and soldering land size to match the appropriate thermal resistance value.
- 4) Refer to the databook on Rectifiers for further information.





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