TOSHIBA High Efficiency Rectifier

Silicon Epitaxial Type

# CLH07

# Switching Mode Power Supply Applications

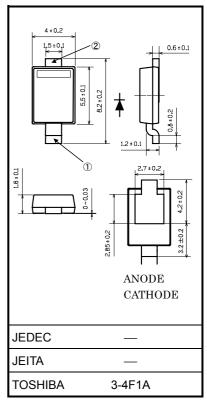
Unit: mm

- Forward voltage: V<sub>FM</sub> = 1.8 V (Max.)
- Average forward current:  $I_{F(AV)} = 5.0 A$
- Repetitive peak reverse voltage:  $V_{RRM} = 400 \text{ V}$
- Surface-mount package
  - "L-FLAT" (Toshiba package name)

# **Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit
Repetitive peak reverse voltage	$V_{RRM}$	400	V
Average forward current	I <sub>F(AV)</sub>	5.0	Α
Peak one cycle surge forward current (non-repetitive)	I <sub>FSM</sub>	50 (50 Hz)	Α
Junction temperature	Tj	-40~150	°C
Storage temperature range	T <sub>stg</sub>	-40~150	°C

Note:  $T\ell = 92^{\circ}C$  Rectangular waveform:  $(\alpha = 180^{\circ})$ 



Weight: 0.15 g (typ.)

# **Electrical Characteristics (Ta = 25°C)**

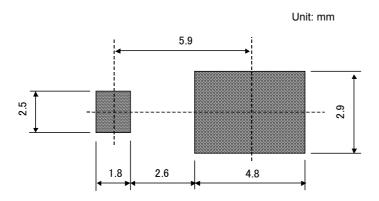
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	V <sub>FM (1)</sub>	I <sub>FM</sub> = 1.0 A (pulse test)	_	0.98	_	V
Peak forward voltage	V <sub>FM (2)</sub>	I <sub>FM</sub> = 3.0 A (pulse test)	_	1.23	_	v
	V <sub>FM (3)</sub>	I <sub>FM</sub> = 5.0 A (pulse test)	_	1.40	1.80	
Repetitive peak reverse current	I <sub>RRM</sub>	V <sub>RRM</sub> = 400 V (pulse test)	_	_	10	μА
Reverse recovery time	t <sub>rr</sub>	$I_F = 2A$ , di/dt = $-50$ A/ $\mu$ s	_	_	35	ns
Forward recovery time	t <sub>fr</sub>	I <sub>F</sub> = 1.0 A	_	_	100	ns
(junction to ambient) Board (board (board thick Cathode: 5.		Device mounted on a glass-epoxy board (board size: 50 mm x 50 mm) (board thickness: 1.6 t) (soldering land) Cathode: 5.7 mm x 6.2 mm, Anode: 4.5 mm x 3.4 mm	_	_	100	°C/W
Thermal resistance (junction to ambient)	R <sub>th (j-ℓ)</sub>	_	_	_	5	°C/W

1

#### Marking

Abbreviation Code	Part No.
H07	CLH07

# **Standard Soldering Pad**



# **Handling Precautions**

1) The maximum rating denotes the absolute maximum ratings, which are rated values that must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend for designing a circuit incorporating this device

VRRM: Use this rating with reference to (1) above. The  $V_{RRM}$  has a temperature coefficient of 0.1%/ ° C. Take this temperature coefficient into account when designing a device at low temperature.

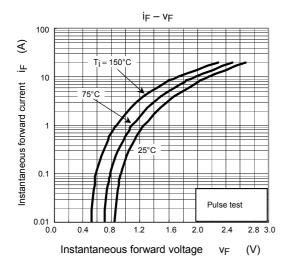
IF (AV): We recommend that the worst case current be no greater than 80% of the maximum rating of  $I_{F(AV)}$  and that Tj be below 120 ° C . When using this device, take the margin into consideration by using an allowable Ta (max)-I<sub>F</sub> (AV) curve.

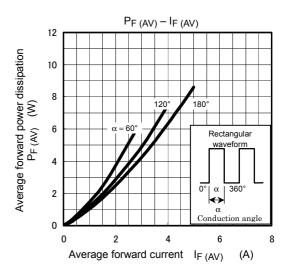
IFSM: This rating specifies the non-repetitive peak current. This applies to abnormal operation only, When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.

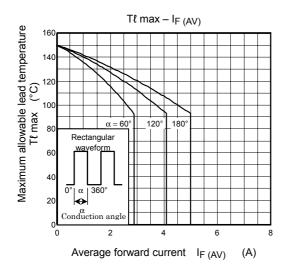
IFSM: This rating specifies the non-repetitive peak current. This applies to abnormal operation only, When using the device, design the circuit board and the soldering land size to match the appropriate thermal resistance value.

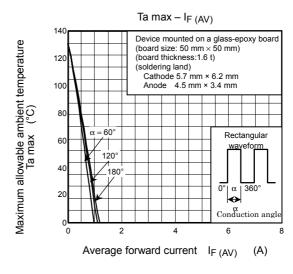
Tj: Derate this rating when using the device to ensure high reliability. We recommend that the device be used at a Tj of below  $120\,^{\circ}$  C.

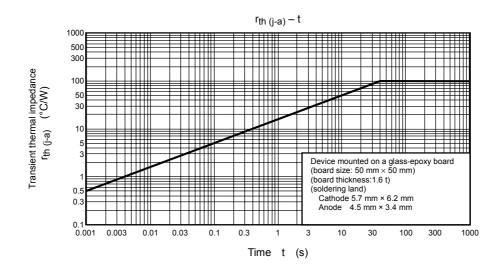
- 2) The thermal resistance between junction and ambient varies depending on the mounting condition of the See. When using the device, design the circuit board and the soldering land size to match the appropriate thermal resistance value.
- 3) See the Rectifiers databook for further information.



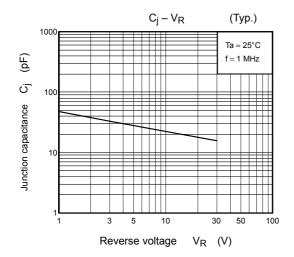


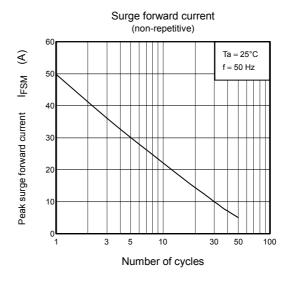






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4

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