



4-Channel Regulated Charge Pump White LED Driver



FEATURES

- Drives individually up to 4 LEDs
- Output current up to 30mA per LED
- Digital control On/Off of each LED
- Compatible with supply voltage of 3V to 5.5V
- Power efficiency up to 93%
- 2 modes of operation 1x and 1.5x
- Automatic short circuit detect/disable
- High-frequency Operation at 1MHz
- Low value ceramic capacitors
- "604" compatible pinout
- Soft start and current limiting
- TQFN 16-pad package, 4 x 4mm, 0.8mm max height

APPLICATION

- Color LCD and keypad Backlighting
- Cellular Phones
- Handheld Devices
- Digital Cameras
- PDAs
- Portable MP3 players

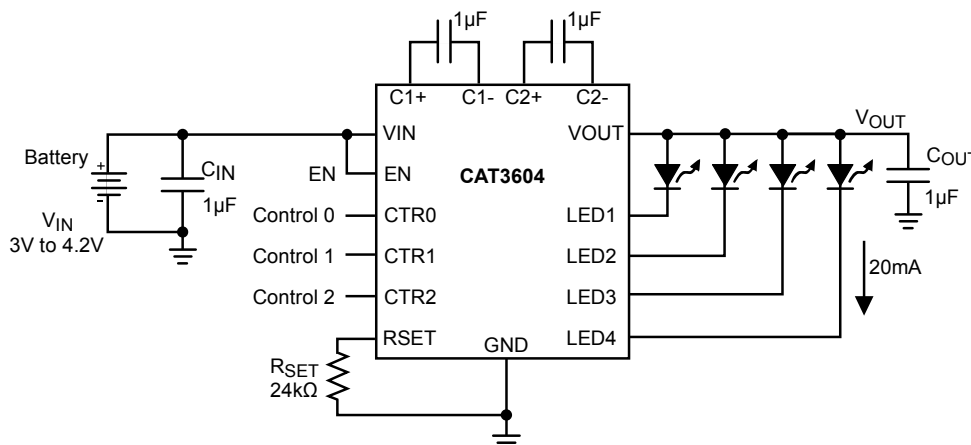
ORDERING INFORMATION

Part Number	Package	Quantity per Reel	Marking
CAT3604HV4-T2	TQFN-16 ⁽¹⁾	2000	G364
CAT3604HV4-GT2	TQFN-16 ⁽²⁾	2000	CDBA

Notes: (1) Matte-Tin Plated Finish (RoHS-compliant).

(2) NiPdAu Plated Finish (RoHS-compliant).

TYPICAL APPLICATION CIRCUIT



DESCRIPTION

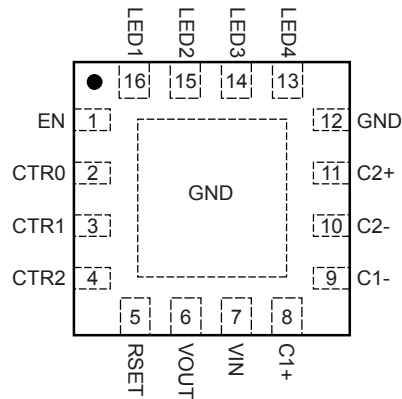
The CAT3604 is a charge pump operating in either 1x (LDO) mode or 1.5x fractional mode regulating current through each of the 4 LED pins. Operation at a fixed high frequency of 1MHz typical allows the use of very small value ceramic capacitors.

The CAT3604 drives white light-emitting diodes (LEDs) connected in parallel and provides tightly matched regulated current to achieve uniformity of brightness in LCD backlighting applications. An external resistor RSET controls the output current level. LED currents of up to 30mA are supported over a range of input supply voltages from 3V to 5.5V, making the device ideal for battery-powered applications.

LED dimming can be accomplished by several methods including using a DC voltage to set the RSET pin current, applying a PWM signal on the Control signals, or adding a switched resistor in parallel with RSET. The Enable input pin allows the device to be placed in power-down mode with "zero" quiescent current.

The CAT3604 features short circuit and overcurrent limiting protection. The device is available in a 16-pad TQFN package with a max height of 0.8mm.

For Ordering Information details, see page 11.

PIN CONFIGURATION⁽¹⁾TQFN 16-pad (4 x 4mm)
(Top View)

PIN DESCRIPTIONS

Pin Number	Name	Function
1	EN	Enable input, active HIGH
2	CTR0	Digital control input 0
3	CTR1	Digital control input 1
4	CTR2	Digital control input 2
5	RSET	The LED output current is set by the current sourced out of the RSET pin
6	VOUT	Charge pump output connected to the LED anodes
7	VIN	Supply voltage
8	C1+	Bucket capacitor 1 terminal
9	C1	Bucket capacitor 1 terminal
10	C2	Bucket capacitor 2 terminal
11	C2+	Bucket capacitor 2 terminal
12	GND	Ground reference
13	LED4	LED 4 cathode terminal
14	LED3	LED 3 cathode terminal
15	LED2	LED 2 cathode terminal
16	LED1	LED 1 cathode terminal
Pad	GND Pad	Ground reference

Notes:

(1) The package exposed pad is electrically connected inside the package to GND and to pin 12.

ABSOLUTE MAXIMUM RATINGS

Parameter	Rating	Unit
VIN, VOUT, LEDx voltage	-0.3 to 7.0	V
EN, CTRx voltage	-0.3 to VIN	V
RSET voltage	-0.3 to VIN	V
RSET current	±1	mA
Ambient Temperature Range	-40 to +85	°C
Storage Temperature Range	-65 to +160	°C
Lead Temperature	300	°C
ESD Ratings		
Human Body Model (HBM)	2000	V
Machine Model (MM) ⁽¹⁾	200	V

RECOMMENDED OPERATING CONDITIONS

Parameter	Range	Unit
VIN	3.0 to 5.5	V
Ambient Temperature Range	-40 to +85	°C
Input/Output/Bucket Capacitors	1 ± 20% typical	µF
I _{LED} per LED pin	0 to 30	mA

Typical application circuit with external components is shown on page 1.

ELECTRICAL OPERATING CHARACTERISTICS

Limits over recommended operating conditions unless specified otherwise. Typical values at T_A = 25°C, V_{IN} = 3.5V, I_{RSET} = 5µA

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _Q	Quiescent Current	V _{EN} = 0V Shutdown Mode 1x Mode, No Load 1.5x Mode, No Load		0.05 0.3 2.6	1 1 5	µA mA mA
V _{RSET}	RSET Regulated Voltage		1.17	1.2	1.23	V
I _{LED}	Programmed LED Current	I _{RSET} = 5µA I _{RSET} = 37µA I _{RSET} = 78µA		2.4 15.0 30.0		mA mA mA
I _{LED-A_{CC}}	LED Current Accuracy	0.5mA < I _{LED} < 3mA 3mA < I _{LED} < 30mA		+15 +5		% %
I _{LED-DEV}	LED Channel Matching	(I _{LED} - I _{LEDAVG}) / I _{LEDAVG}		+3		%
R _{OUT}	Output Resistance (Open Loop)	1x Mode 1.5x Mode, I _{OUT} = 100mA		1.4 6.5	2.5 10	Ω Ω
f _{OSC}	Charge Pump Frequency		0.8	1.0	1.3	MHz
T _{DROPOUT}	1x to 1.5x Mode Transition Dropout Delay		0.4	0.6	0.9	ms
I _{EN-CTR}	Input Leakage Current	On Inputs EN, CTR0, 1 & 2		0.001	1	µA
V _{EN-CTR}	High Detect Threshold Low	On Inputs EN, CTR0, 1 & 2	0.4	0.8	1.3	V
I _{SC}	Input Current Limit	VOUT = GND	30	45	60	mA
I _{LIM}	Maximum Input Current	VOUT > 1V	200	400	600	mA

Notes:

(1) Machine model is with 200pF capacitor discharged directly into each pin.

BLOCK DIAGRAM

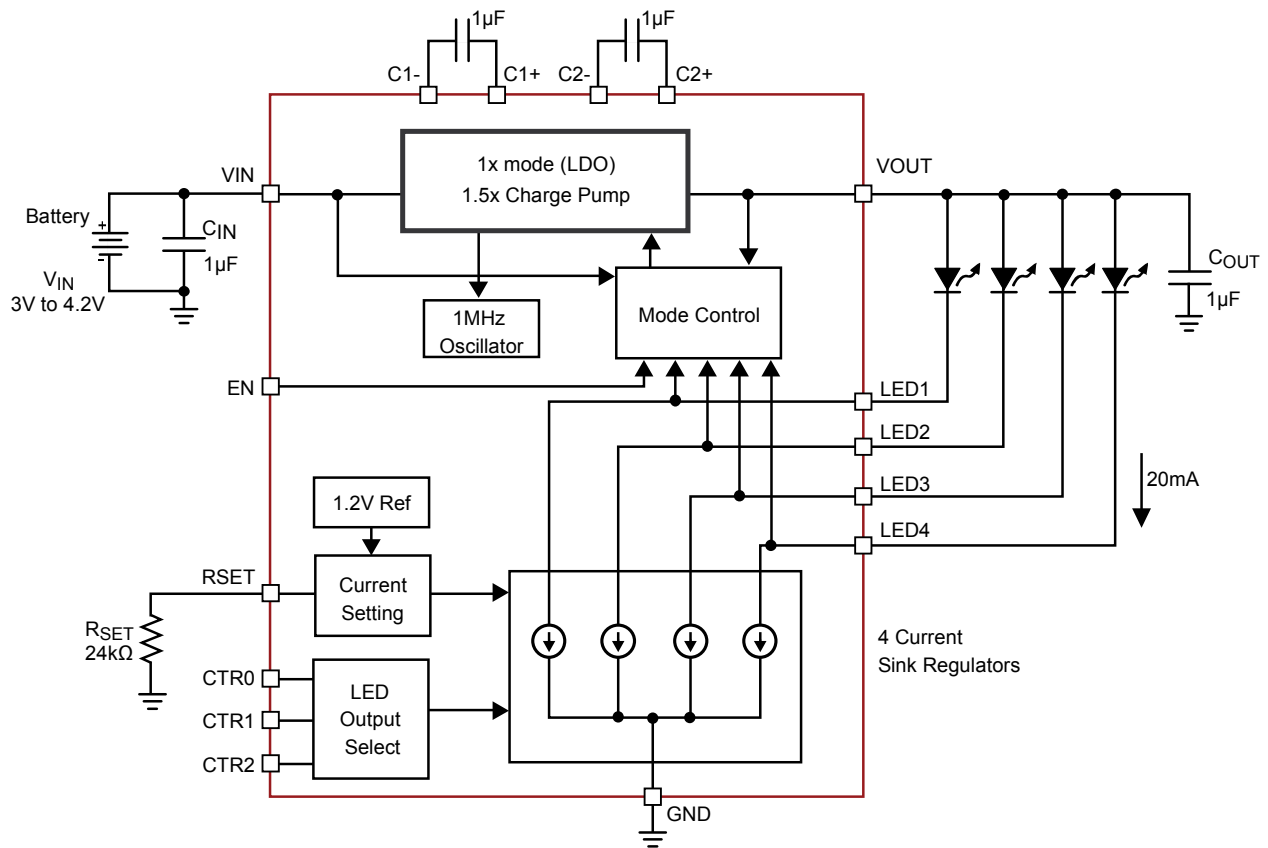


Figure 1. CAT3604 Functional Block Diagram

BASIC OPERATION

At power-up, the CAT3604 starts operation in 1x mode. If it is able to drive the programmed LED current, it continues in 1x mode. If the battery voltage drops to a level where the LED current cannot be met, the driver automatically switches into 1.5x mode. The 1.5x charge pump will boost the output voltage accordingly to achieve the nominal LED current.

The operating mode is reinitialized each and every time the chip is powered up or is taken out of shutdown mode (via EN pin). The use of the control pins (CTR0, CTR1, CTR2) does not reconfigure the mode of operation.

LED CURRENT SETTING

The LED current is set by the external resistor R_{SET} connected between the RSET pin and ground. Table 1 lists various LED currents and the associated R_{SET} resistor value for standard 1% precision surface mount resistors.

Table 1. RSET Resistor Selection

LED Current (mA)	R_{SET} (k Ω)
1	649
2	287
5	102
10	49.9
15	32.4
20	23.7
30	15.4

The digital control lines CTR0, CTR1 and CTR2 allow to turn On or Off a combination of LEDs as shown in Table 2.

Table 2: LED Selection

Control Lines			LED Outputs			
CTR2	CTR1	CTR0	LED4	LED3	LED2	LED1
0	0	0	–	–	–	ON
0	0	1	–	–	ON	–
0	1	0	–	ON	–	–
0	1	1	ON	–	–	–
1	0	0	–	–	ON	ON
1	0	1	–	ON	ON	ON
1	1	0	ON	ON	ON	ON
1	1	1	–	–	–	–

Notes:

1 = logic high (or VIN)

0 = logic low (or GND)

– = LED output OFF

SHORT CIRCUIT DETECT/DISABLES

The unused LED channels can also be turned off by connecting the respective LED pins to VOUT. In which case, the corresponding LED driver is disabled and the typical LED sink current is only about 20 μ A. When the following equation is true on any channel, the driver turns off the LED channel:

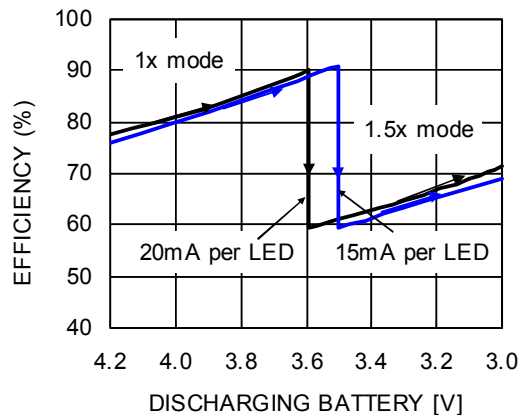
$$V_{OUT} - V_{LED} \leq 1V \text{ (LED channel OFF)}$$

Note: The CAT3604 is designed to drive LEDs with forward voltage greater than 1V and is not compatible with resistive loads.

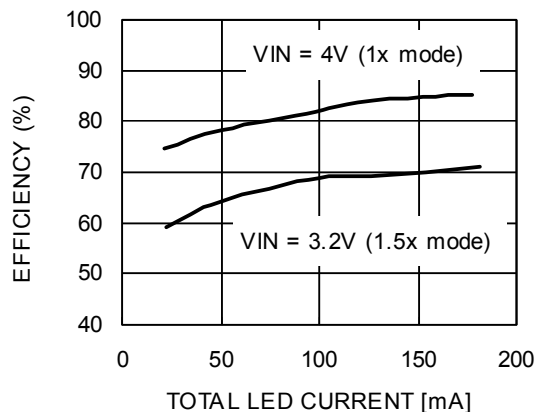
TYPICAL CHARACTERISTICS

$V_{IN} = 3.6V$, $EN = V_{IN}$, $C_{IN} = C_{OUT} = 1\mu F$, $R_{SET} = 24k\Omega$, $T_{AMB} = 25^{\circ}C$, unless otherwise specified.

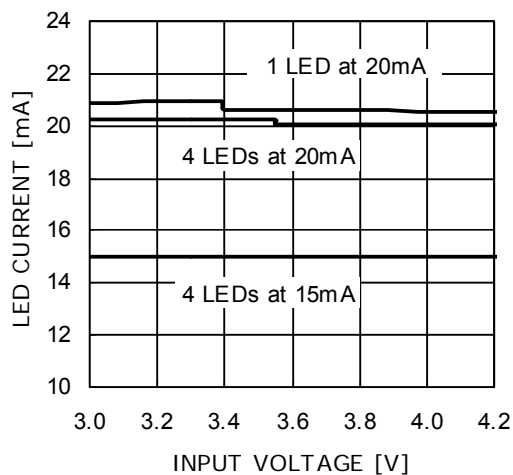
Efficiency vs. Input Voltage (4 LEDs)



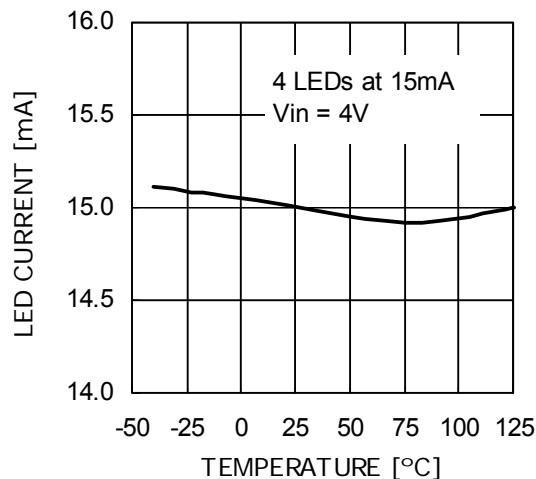
Efficiency vs. Total LED Current (4 LEDs)



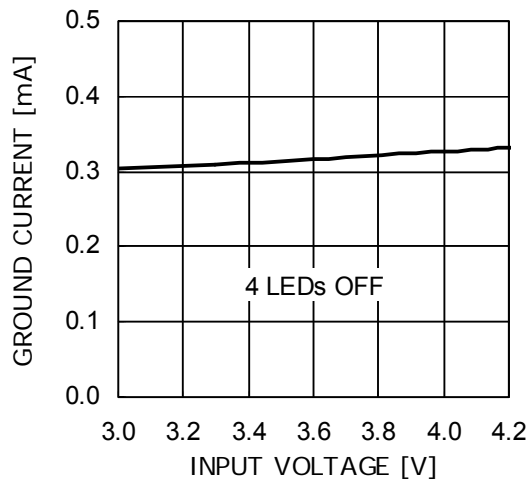
LED Current vs. Input Voltage



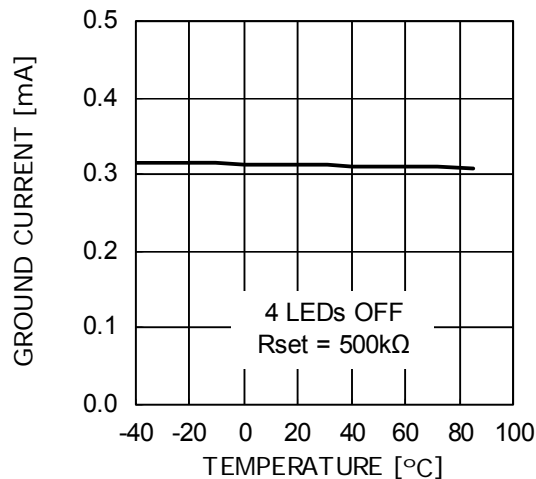
LED Current vs. Temperature



Ground Current vs. Input Voltage (1x mode)



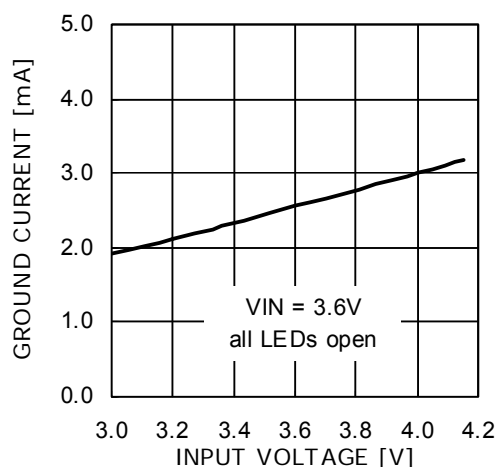
Ground Current vs. Temperature (1x mode)



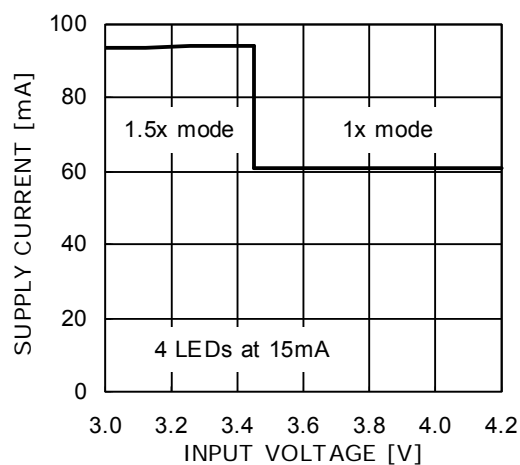
TYPICAL CHARACTERISTICS

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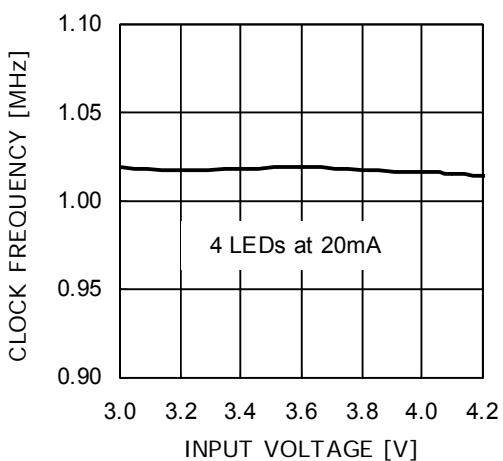
Ground Current vs. Input Voltage (1.5x mode)



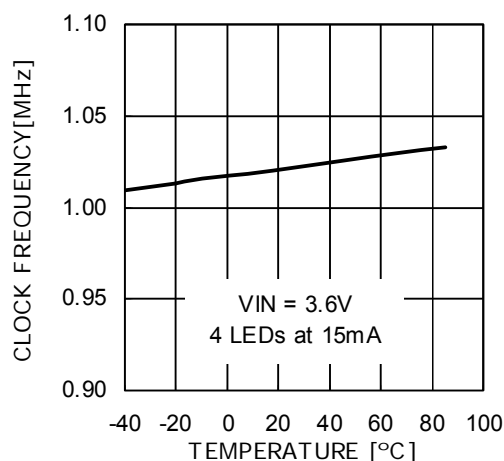
Supply Current vs. Input Voltage



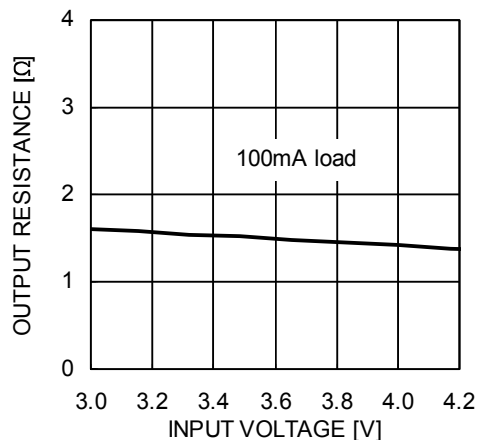
Oscillator Frequency vs. Input Voltage



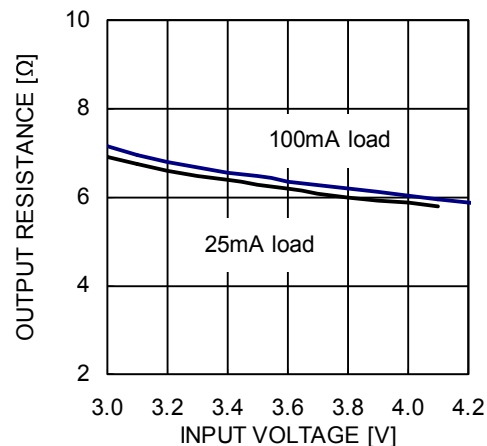
Oscillator Frequency vs. Temperature



Output Resistance vs. Input Voltage (1x mode)



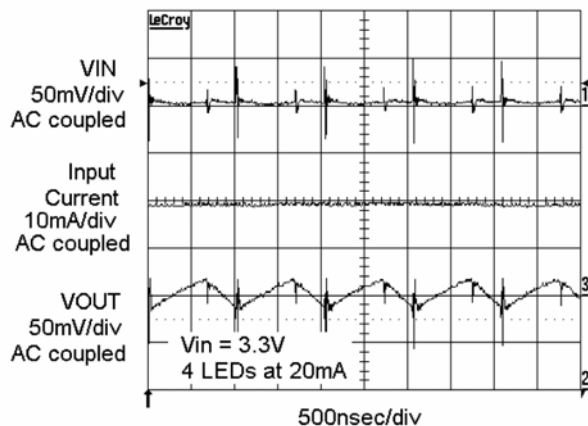
Output Resistance vs. Input Voltage (1.5x mode)



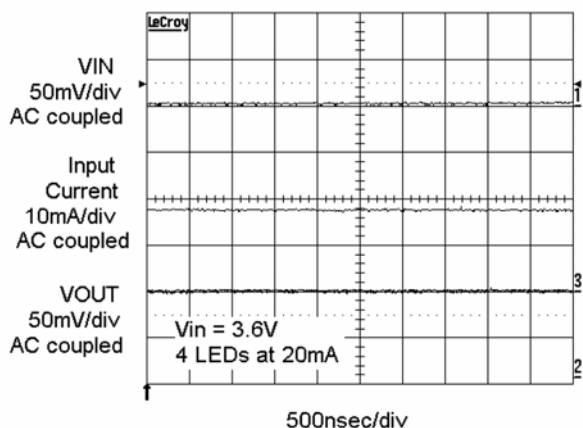
TYPICAL CHARACTERISTICS

$V_{IN} = 3.6V$, $EN = V_{IN}$, $C_{IN} = C_{OUT} = 1\mu F$, $R_{SET} = 24k\Omega$, $T_{AMB} = 25^{\circ}C$, unless otherwise specified.

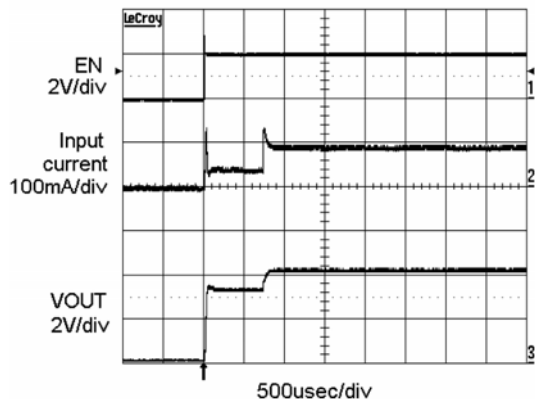
Switching Waveforms in 1.5x Mode



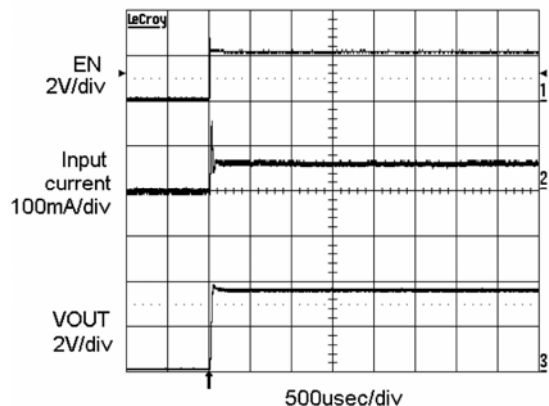
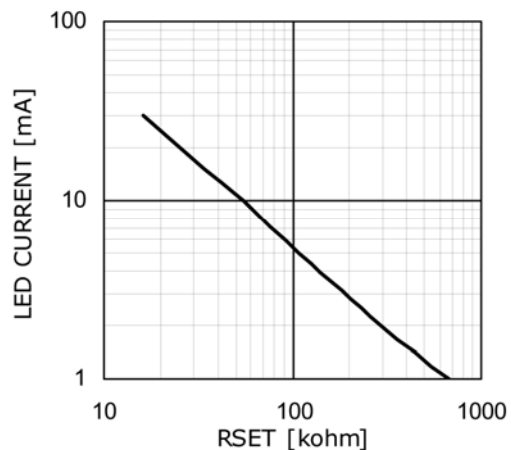
Operating Waveforms in 1x Mode



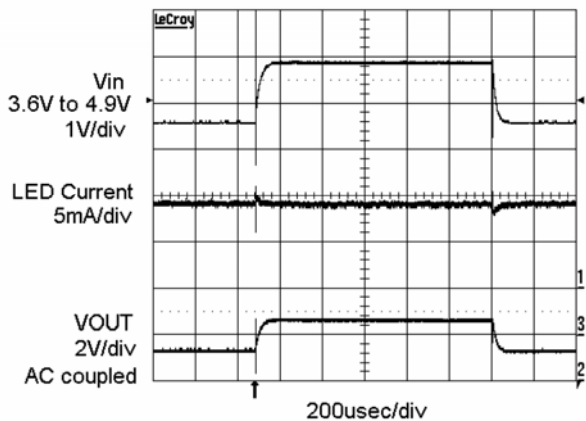
Power Up 4 LEDs at 15mA, Vin = 3V (1.5x Mode)



Power Up 4 LEDs at 15mA, Vin = 3.6V (1x Mode)

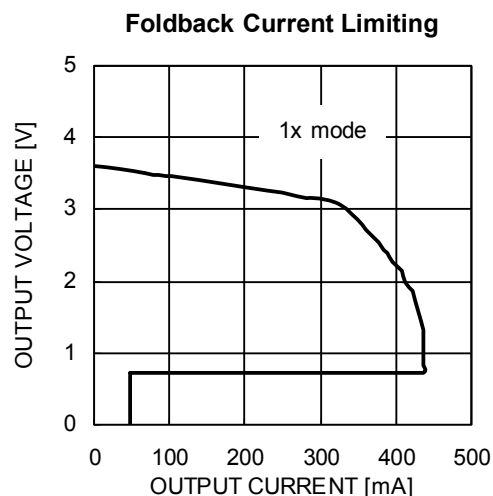
LED Current vs. R_{SET} 

Line Transient Response in 1x Mode



TYPICAL CHARACTERISTICS

$V_{IN} = 3.6V$, $EN = V_{IN}$, $C_{IN} = C_{OUT} = 1\mu F$, unless otherwise specified.



RECOMMENDED LAYOUT

When the driver is in the 1.5x charge pump mode, the 1MHz switching frequency operation requires to minimize trace length and impedance to ground on all 4 capacitors. A ground plane should cover the area on the bottom side of the PCB opposite to the IC and the bypass capacitors. Capacitors C_{in} and C_{out} require short connection to ground which can be done with multiple vias as shown on Figure 2. A square copper

area matches the QFN16 exposed pad (GND) which is connected by a trace to the pin 12 pad (GND). A large via (metalized hole) centered in the square pad provides a low impedance connection to the ground plane on the opposite side of the PCB and allows the heat dissipated by the driver IC to spread out resulting in excellent thermal performance.

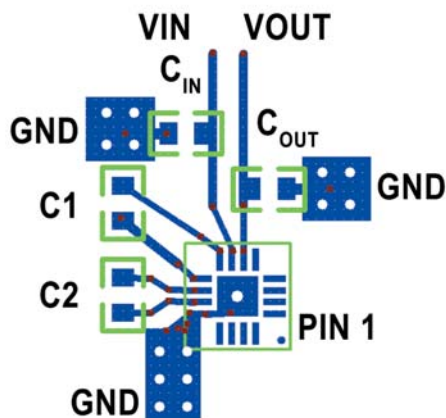


Figure 2. PCB Layout

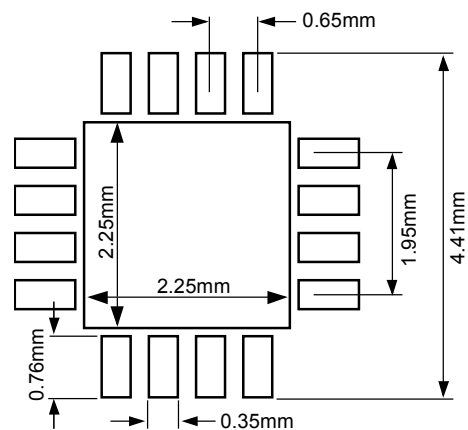
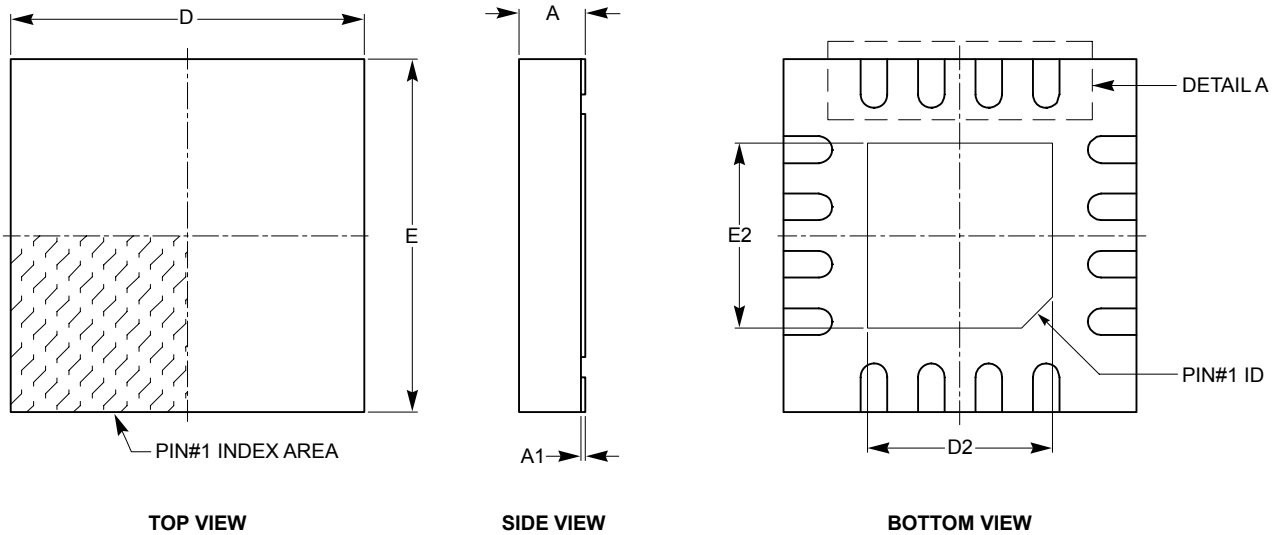
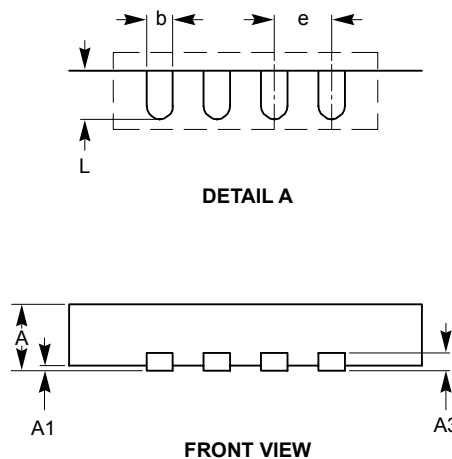


Figure 3. Recommended QFN 16 Package Land Pattern

PACKAGE OUTLINE DRAWING

TQFN 16-Pad 4 x 4mm (HV4) ⁽¹⁾⁽²⁾

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A3	0.20 REF		
b	0.25	0.30	0.35
D	3.90	4.00	4.10
D2	2.00	—	2.25
E	3.90	4.00	4.10
E2	2.00	—	2.25
e	0.65 BSC		
L	0.45	—	0.65

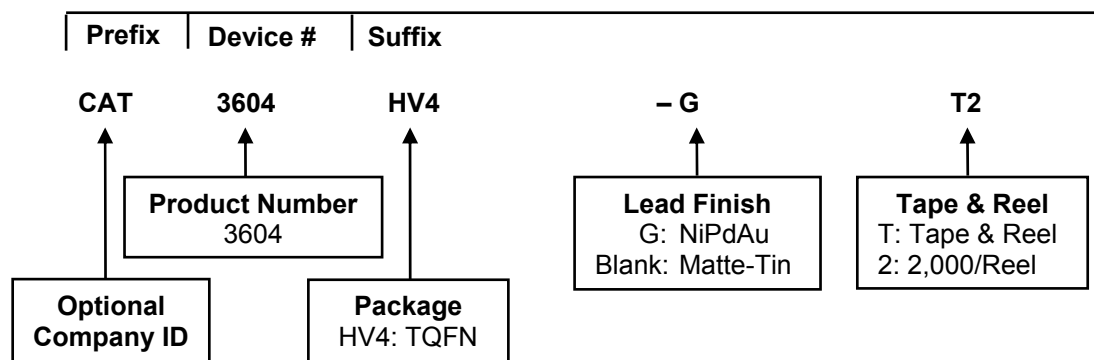


For current Tape and Reel information, download the PDF file from:
<http://www.catsemi.com/documents/TapeandReel.pdf>

Notes:

- (1) All dimensions are in millimeters.
- (2) Complies with JEDEC standard MO-220.

EXAMPLE OF ORDERING INFORMATION




For Product Top Mark Codes, click here:
<http://www.catsemi.com/techsupport/producttopmark.asp>

Notes:

- (1) All packages are RoHS-compliant (Lead-free, Halogen-free).
- (2) The standard lead finish is NiPdAu.
- (3) The device used in the above example is a CAT3604HV4-GT2 (TQFN, NiPdAu Plated Finish, Tape & Reel, 2,000/Reel).
- (4) For additional package and temperature options, please contact your nearest ON Semiconductor Sales office.

REVISION HISTORY

Date	Rev.	Reason
19-Aug-04	A	Initial issue
21-Sept-04	B	Minor Changes
22-Sept-04	C	Added Green package in all areas
29-Sept-04	D	Minor Changes
4-Jan-05	E	Added QFN land pattern (figure 3)
21-Jan-05	F	Minor Changes
15-Apr-05	G	Update Features Update Short Circuit Detect/Disable
06-Jun-05	H	Update Ordering Information
15-Jul-05	I	Update Basic Operation
30-May-08	J	Add MD- to document number Update Package Outline Drawing Delete PbSn plated finish; add NiPdAu plated finish Add Example of Ordering Information
11-Nov-08	K	Change logo and fine print to ON Semiconductor

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