



BC857BLP4

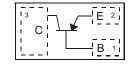
PNP SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Die Construction
- Ultra-Small Leadless Surface Mount Package
- Ultra-low Profile (0.40mm max)
- Complementary NPN Type Available (BC847BLP4)
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Mechanical Data
- Case: DFN1006H4-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections Indicator: Collector Dot
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Ordering Information: See Page 3
- Marking Information: See Page 3
- Weight: 0.0008 grams



BOTTOM VIEW



TOP VIEW (Internal Schematic)

DFN1006H4-3

Maximum Ratings @T _A = 25°C unless otherwise specified				
Characteristic	Symbol	Value	Unit	
Collector-Base Voltage	V _{CBO}	-50	V	
Collector-Emitter Voltage	V _{CEO}	-45	V	
Emitter-Base Voltage	V _{EBO}	-5.0	V	
Collector Current	Ι _C	-100	mA	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @T _A = 25°C	PD	250	mW
Thermal Resistance, Junction to Ambient Air (Note 3) $@T_A = 25^{\circ}C$	$R_{ ext{ heta}JA}$	500	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic (Note 4)	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-50	—	—	V	$I_{C} = 10 \mu A, I_{B} = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-45		_	V	$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-5		_	V	$I_E = 1 \mu A, I_C = 0$
DC Current Gain	h _{FE}	220	300	475		$V_{CE} = -5.0V, I_{C} = -2.0mA$
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_	-90 -250	-300 -650	mV	$I_{C} = -10mA$, $I_{B} = -0.5mA$ $I_{C} = -100mA$, $I_{B} = -5.0mA$
Base-Emitter Saturation Voltage	V _{BE(SAT)}	_	-700 -850	—	mV	$I_{C} = -10mA$, $I_{B} = -0.5mA$ $I_{C} = -100mA$, $I_{B} = -5.0mA$
Base-Emitter Voltage	V _{BE(ON)}	-600	-670 -710	-750 -820	mV	$V_{CE} = -5.0V, I_C = -2.0mA$ $V_{CE} = -5.0V, I_C = -10mA$
Collector-Cutoff Current	I _{CBO}	_		-15 -4.0	nA μA	V _{CB} = -30V V _{CB} = -30V, T _A = 150°C
Gain Bandwidth Product	f _T	100	_	_	MHz	$V_{CE} = -5.0V, I_C = -10mA, f = 100MHz$
Collector-Base Capacitance	C _{CBO}	_	3.0	_	pF	V _{CB} = -10V, f = 1.0MHz

Notes: 1. No purposefully added lead.

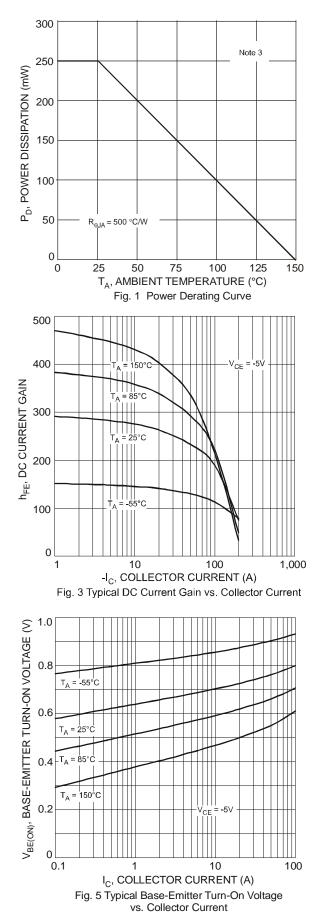
3. Device mounted on FR-4 PCB, pad layout as shown on page 3, or Diodes Inc. suggested pad layout document AP02001 on our website at

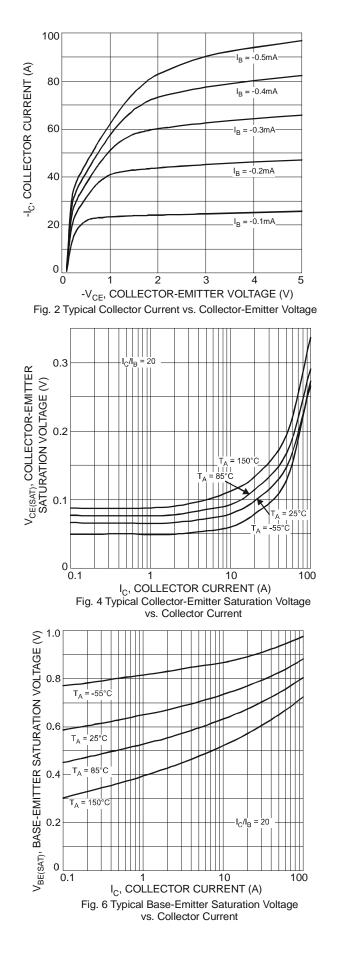
http://www.diodes.com/datasheets/ap02001.pdf.

4. Short duration pulse test used to minimize self-heating effect.

^{2.} Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php









Ordering Information (Note 5)

Device	Packaging	Shipping
BC857BLP4-7	DFN1006H4-3	3000/Tape & Reel

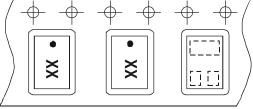
Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



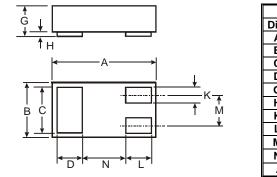
F2 = Product Type Marking Code Dot Denotes Collector, Terminal 3

DFN1006H4-3 Taping orientation



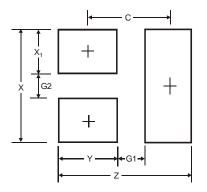
Direction of feed

Mechanical Details



DFN1006H4-3				
Dim	Min	Max	Тур	
Α	0.95	1.075	1.00	
В	0.55	0.675	0.60	
С	0.45	0.55	0.50	
D	0.20	0.30	0.25	
G		0.40		
Н	0	0.05	0.02	
Κ	0.10	0.20	0.15	
L	0.20	0.30	0.25	
Μ	_	_	0.35	
Ν			0.40	
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
Х	0.7
X1	0.25
Y	0.4
C	0.7

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