AN5279

Mono channel SEPP audio power amplifier IC

Overview

The AN5279 is a monolithic integrated circuit designed for 5.0 W (19 V, 8 Ω) output audio power amplifier. It is a mono channel SEPP IC suitable for TV application.

■ Features

- Few external components:
- No Boucherot cells(output C, R)
- No Bootstrap Capacitors
- No Negative Feeback Capacitors
- Built-in muting circuit
- Built-in stand-by circuit
- Built-in various protection circuits (Load-short, thermal, over-voltage and current)
- High ripple rejection(55 dB)
- Operating voltage range 10 V to 24 V(19 V typ.)

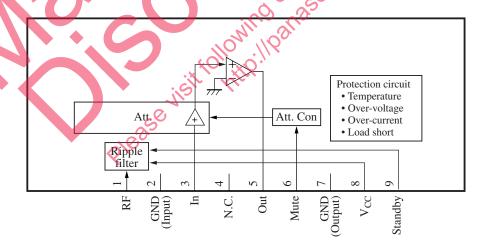
Applications

• TV

■ Package

• HSIP009-P-0000E

■ Block Diagram



■ Pin Descriptions

Pin No.	Descriptions	Pin No.	Descriptions
1	Ripple filter	6	Mute
2	Input GND	7	Output GND
3	Input	8	V _{CC}
4	Not connected	9	Standby 💧
5	Output		

■ Absolute Maximum Ratings

Parameter	Symbol	Rating		Unit
Supply voltage	V _{CC}	26.0		V
Supply current	I_{CC}	1.6		, A
Power dissipation *2	P _D	6.2		W W
Operating ambient temperature *1	T_{opr}	-25 to +75	io,	°C
Storage temperature *1	T _{stg}	55 to +150	in	°C

Note) *1: Except these items, all other measurements are taken at $T_a = 25$ °C.

■ Recommended Operating Range

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Parameter	Symbol	Range	Unit
Supply voltage	Vcc	10.0 to 24.0	V
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^{*2:} $T_a = 75$ °C with infinite heat sink.

\blacksquare Electrical Characteristics at V_{CC} = 19 V, f = 1 kHz, R_L = 8 Ω , T_a = 25 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Quiescent current	I_{CQ}	$V_{IN} = 0 \text{ mV}$	_	25	50	mA
Output end noise voltage *1	V _{NO}	No input, $R_g = 10 \text{ k}\Omega$	_	0.22	0.4	mV
Voltage gain	G _V	$V_{IN} = 57 \text{ mV}$	32	34	36	dB
Total harmonic distortion	THD	$V_{IN} = 57 \text{ mV}$	_	0.2	0.4	%
Maximum Output Power	P _{O1}	$V_{CC} = 19 \text{ V}, \text{ THD} = 10 \%$	4.0	5.0		W
Maximum Output power	P _{O2}	V _{CC} = 22 V, THD = 10 %	5.6	7.0		W
Ripple rejection ratio *1	RR	$V_r = 1 V_{rms}$ $f_r = 120 \text{ Hz}, R_g = 10 \text{ k}\Omega$	45	55	_	dB
Muting Ratio	MR	$V_{IN} = 57 \text{ mV}, V_{MUTE} > 3.0 \text{ V}$	70	80		dB
Muting control voltage	V _{MUTE}	$V_{IN} = 57 \text{ mV}, MR > 70 \text{ dB}$	3.0		E	V
Standby on voltage	V _{STD-ON}	No input, $I_{CC} \le 0.1 \text{ mA}$			5.0	V
Standby off voltage	V _{STD-OFF}	No input, $I_{CC} \ge 9.5 \text{ mA}$	8.5			V
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Note) *1: For this measurement, use the 20 Hz to 20 kHz (12 dB/OCT) filter.

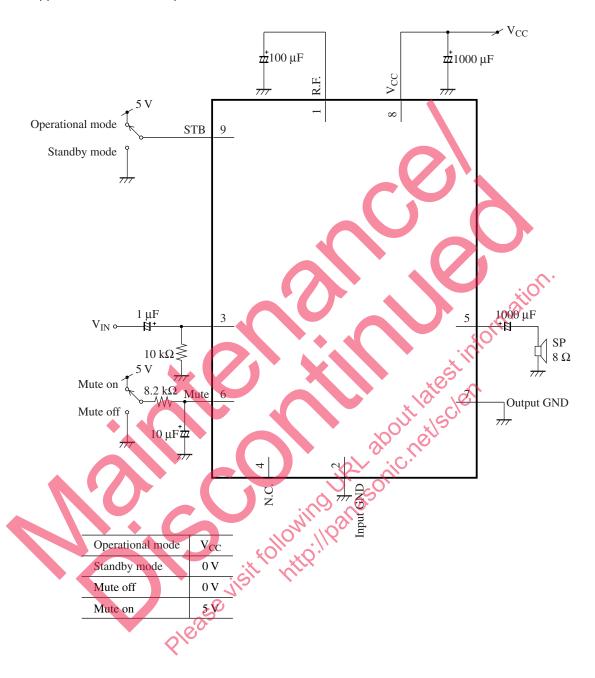
■ Terminal Equivalent Circuits

Pin No.	Equivalent circuit	Description	DC voltage (V)
1	8 30 kΩ 1 20 kΩ 1 20 kΩ 1 20 kΩ 1	Ripple Filter This is the pin to connect the positive terminal of a ripple filter capacitor.	V _{CC} - 1.5V _{BE}
2	2 -	Input GND Input ground pin	ration.
3	200 Ω 400 Ω	7	0
	3 30 kΩ	This is the amplifier input pin.	
4		Not connected	_
5	Pre amp. Driver Cct $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$	Output Output pin	V _{cc} /2

■ Terminal Equivalent Circuits (continued)

Pin No.	Equivalent circuit	Description	DC voltage (V)
6	$\begin{array}{c} 3 \text{ k}\Omega \\ \hline \\ 3 \text{ k}\Omega \\ \hline \\ \end{array}$	Mute input pin. Mute 'on' = 5 V Mute 'off' = 0 V	-ation.
7	-	Output GND Output ground.	0
8	X	V _{CC} This is the power supply pin.	19 V(typ.)
9	$\begin{array}{c} 8 \\ \hline \\ 9 \\ \hline \\ 5 \text{ k}\Omega \\ \hline \\ 7 \text{ k}\Omega \\ \hline $	This is the power supply pin. Standby Standby control pin. Standby mode = 0 V Operational mode = V _{CC}	_

■ Application Circuit Example

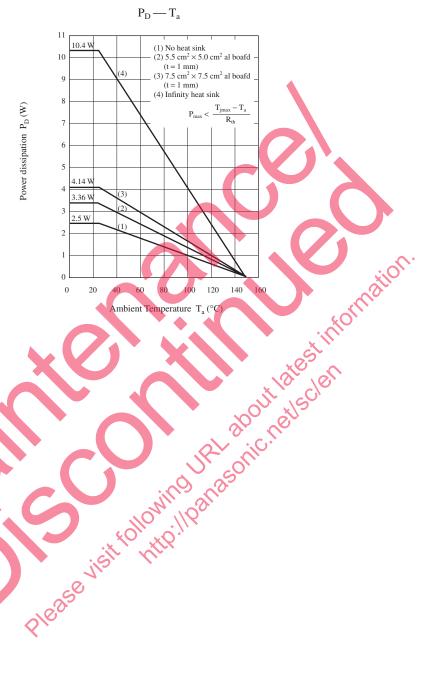


■ Usage notes

- 1) External heatsink is needed when used. External heatsink should be fixed to the chassis.
- 2) Fin of the IC can be connected to GND.
- 3) Please prevent "Output to V_{CC} short", "Output to GND short" and "Reverse Insertion" to avoid damaging the IC.
- 4) The temperature protection circuit will operate at T_j around 150 °C. However, if temperature decreases, the protection circuit would automatically be deactivated and resume normal operation.

■ Technical Information

• P_D — T_a curves of HSI P009-P-0000E



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