

FCI

A

B

PRODUCT NO. *95706-XXX	STAND OFF DIM "M"(mm)	SOLDER TAIL	DIM "P" (mm)	SHEET NO.	MOUNTING STYLE TO PCB
000	0	R/A	2.8	NO.2	
000LF	0	R/A	2.8		
001	0	SMT STAGGERED	—//—		
002	0	SMT IN LINE	—//—		
010	4	R/A	2.8	NO.3	
020	5	R/A	2.8		
040	2	R/A	2.5		
010LF	4	R/A	2.8		
020LF	5	R/A	2.8		
040LF	2	R/A	2.5		
500	0	R/A	2.8	NO.4	
500LF	0	R/A	2.8		
501	0	SMT STAGGERED	—//—		
502	0	SMT IN LINE	—//—		
510	4	R/A	2.8	NO.5	
520	5	R/A	2.8		
540	2	R/A	2.5		
510LF	4	R/A	2.8		
520LF	5	R/A	2.8		
540LF	2	R/A	2.5		

*95706-X X X X

LEAD FREE LF

SOLDER TAIL

STAND OFF

MOUNTING STYLE

0 : R/A (FOR LOWER & SINGLE)
1 : SMT STAGGERED (FOR LOWER ONLY)
2 : SMT IN LINE (FOR LOWER ONLY)
5 : R/A (FOR UPPER ONLY)

0 : 0mm
1 : 4mm
2 : 5mm
4 : 2mm
0 : TOP MOUNTING
5 : BOTTOM MOUNTING

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		Electronics FCI	
ltr	ecn no	dr	date	linear	.X±.3	projection		title	
A	V50549	SSD	7/20/95		.XX±.13			68 POS. EJECTOR HEADER ASSY.	
B	V00619	RGD	3/30/00		.XXX±.051			FOR LOW VOLTAGE	
C	N05-0048	WB	05/29/03	angles	0° ±2°			product family	MCS
				dr	S.DUESTERHOEFT	7/20/95		size	dwg no
				enr	D.BRANN	7/20/95			
				chr	D.BRANN	7/20/92			
				appd	D.BRANN	7/20/95			
sheet	revision	C	C	C	C	C			
index	sheet	1	2	3	4	5			

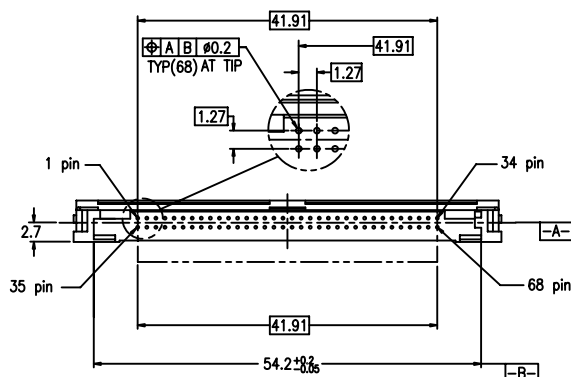
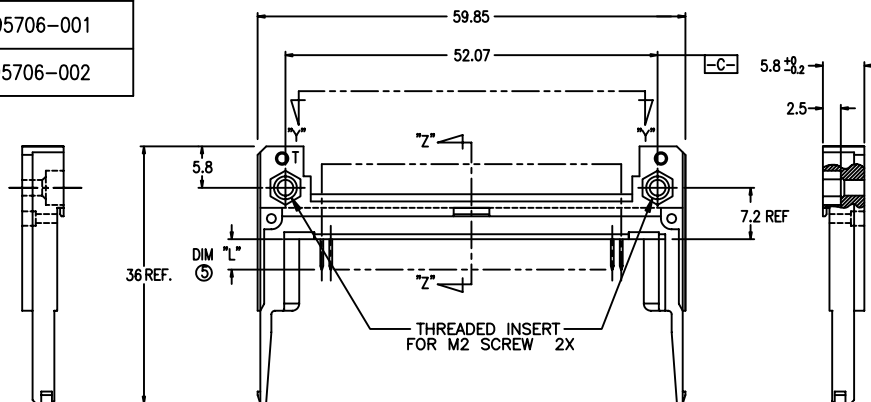
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B

FCI

B

PRODUCT NO.
95706-000/000LF
95706-001
95706-002



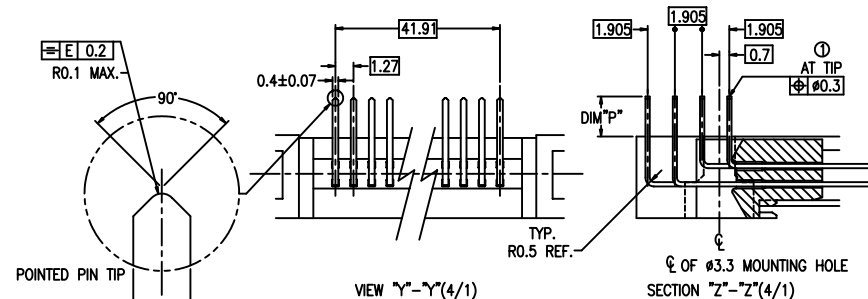
NOTES

- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF
- SEE TA-946 FOR PCB LAYOUT
- MATERIAL
HOUSING : LCP UL94V-0 BLACK
PIN : PHOSPHOR BRONZE
- FINISH
PIN
UNDER PLATING : 0.5µm MIN. Ni
CONTACT AREA : 0.1µm MIN. Au
OVER 0.5µm MIN. Pd-Ni
SOLDER AREA(TIN LEAD OPTION): 2.5µm MIN. Sn-Pb
SOLDER AREA(LEAD FREE OPTION): 2.5µm MIN. PURE TIN

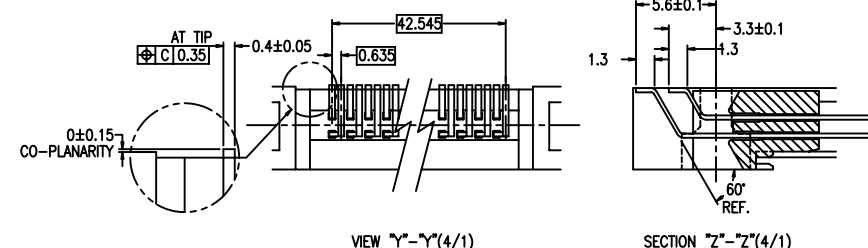
SEQUENCE PIN ASSIGNMENT

	DIM "L"
PIN No.	4.25±0.1
OTHERS	3.67

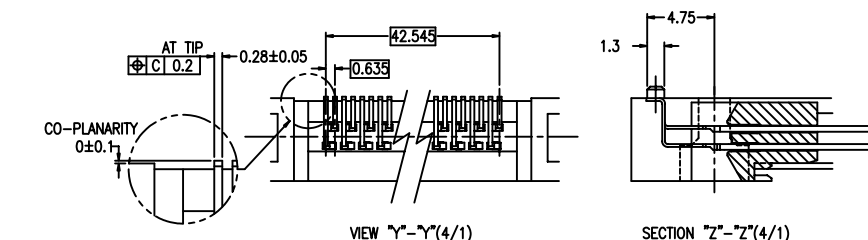
- GENERAL TOLERANCE : ±0.3
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE
- SOLDER TAIL : SMT-IL, SMT-STG CAN RESIST 40 SECONDS IN A CONVECTION, INFRA-RED OR VAPOR PHASE REFLOW OVEN.
SOLDER TAIL : R/A CAN RESIST 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- IF LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION



R/A (95706-000/000LF)



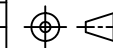
SMT STAGGERED (95706-001)



SMT IN LINE (95706-002)

mat'l. code	tolerances unless otherwise specified	CUSTOMER COPY	Electronics FCI
ltr	ecn no	dr	date
C			
	linear	.X±.3	.XX±.13
	angles	.XXX±.051	0° ±2°
	dr	S.DUESTERHOEFT	7/20/95
	enr	D.BRANN	7/20/95
	chr	D.BRANN	7/20/92
	appd	D.BRANN	7/20/95
sheet index	revision sheet		

projection



MM

scale

1:1

title

68 POS. EJECTOR HEADER ASSY. FOR LOW VOLTAGE

product family MCS

size dwg no

A4 95706

code

sheet 2 of

cage code

STATUS: Released 26 Printed: Feb 06, 2008

ACAD

PDM: Rev:C

①
AT TIP

R0.5 TYP. REF.

Ø OF Ø0.3 MOUNTING HOLE

1.90

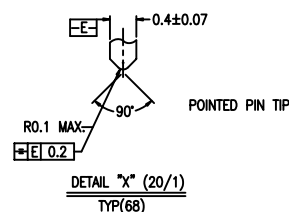
1.90

0.7

1.90

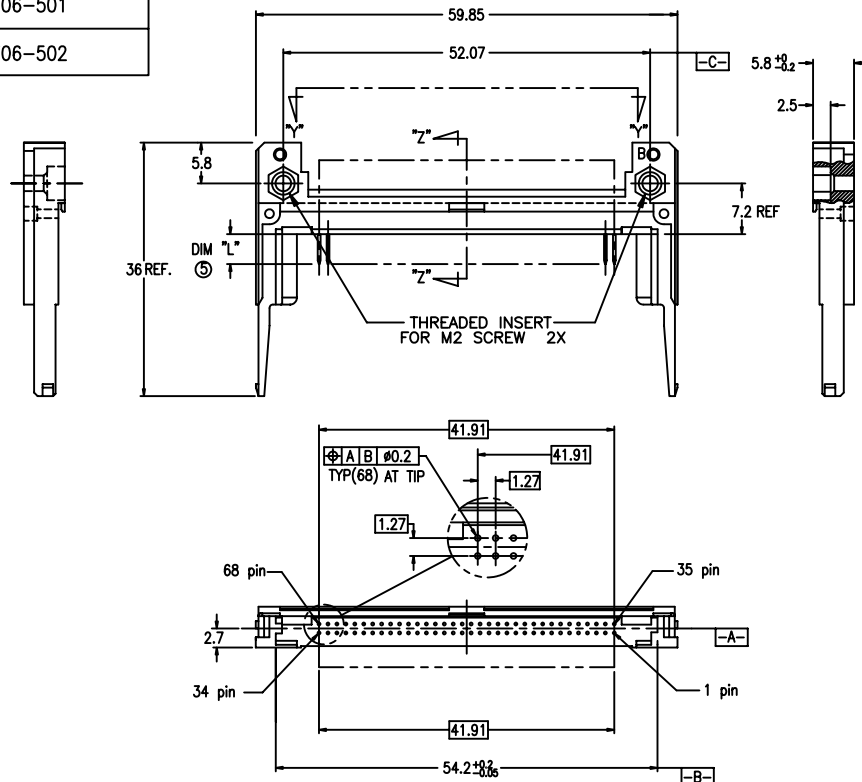
SECTION "Z"-"Z" (5/1)

8 IF LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION



	cage	code	

PRODUCT NO.
95706-500/500LF
95706-501
95706-502

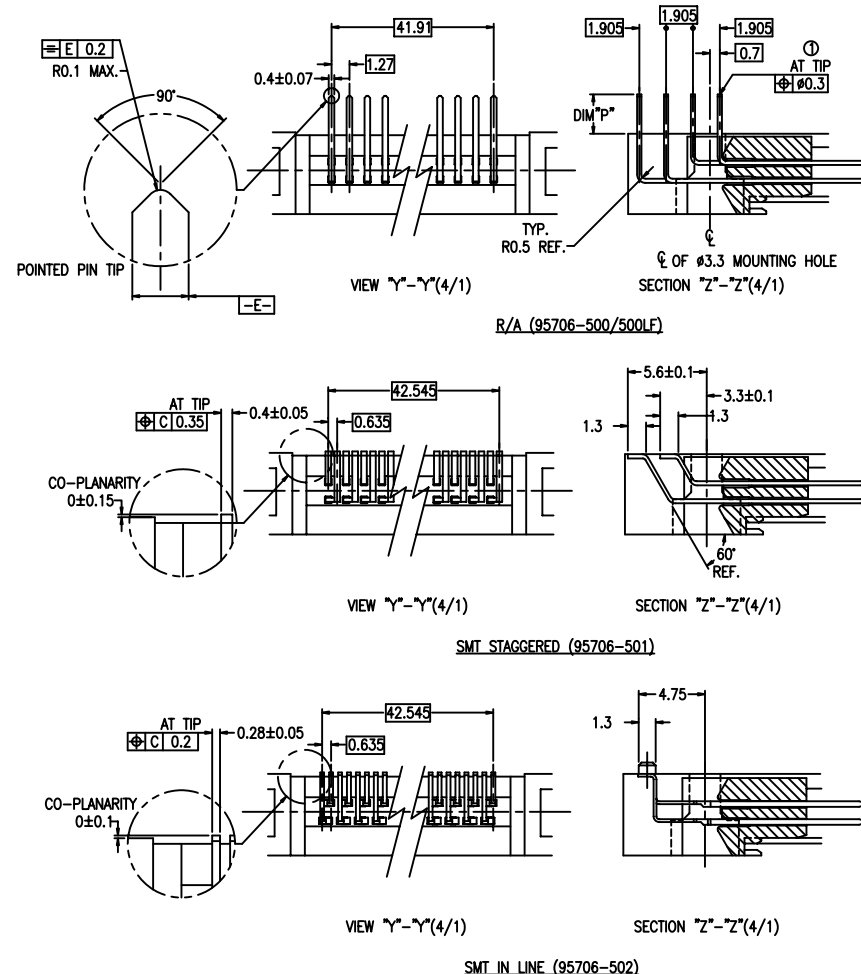


NOTES

- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF
- SEE TA-946 FOR PCB LAYOUT
- MATERIAL
HOUSING : LCP UL94V-0 NATURAL
PIN : PHOSPHOR BRONZE
- FINISH
PIN
UNDER PLATING : 0.5µm MIN. Ni
CONTACT AREA : 0.1µm MIN. Au
OVER 0.5µm MIN. Pd-Ni
SOLDER AREA(TIN LEAD OPTION): 2.5µm MIN. Sn-Pb
SOLDER AREA(LEAD FREE OPTION): 2.5µm MIN. PURE TIN
- SEQUENCE PIN ASSIGNMENT

	DIM "L"
PIN No.	OTHERS
	4.25±0.1
	3.5±0.1
	5.0±0.1
	36.67
	1.17
	34.51
	68

- GENERAL TOLERANCE : ±0.3
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- IF LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION

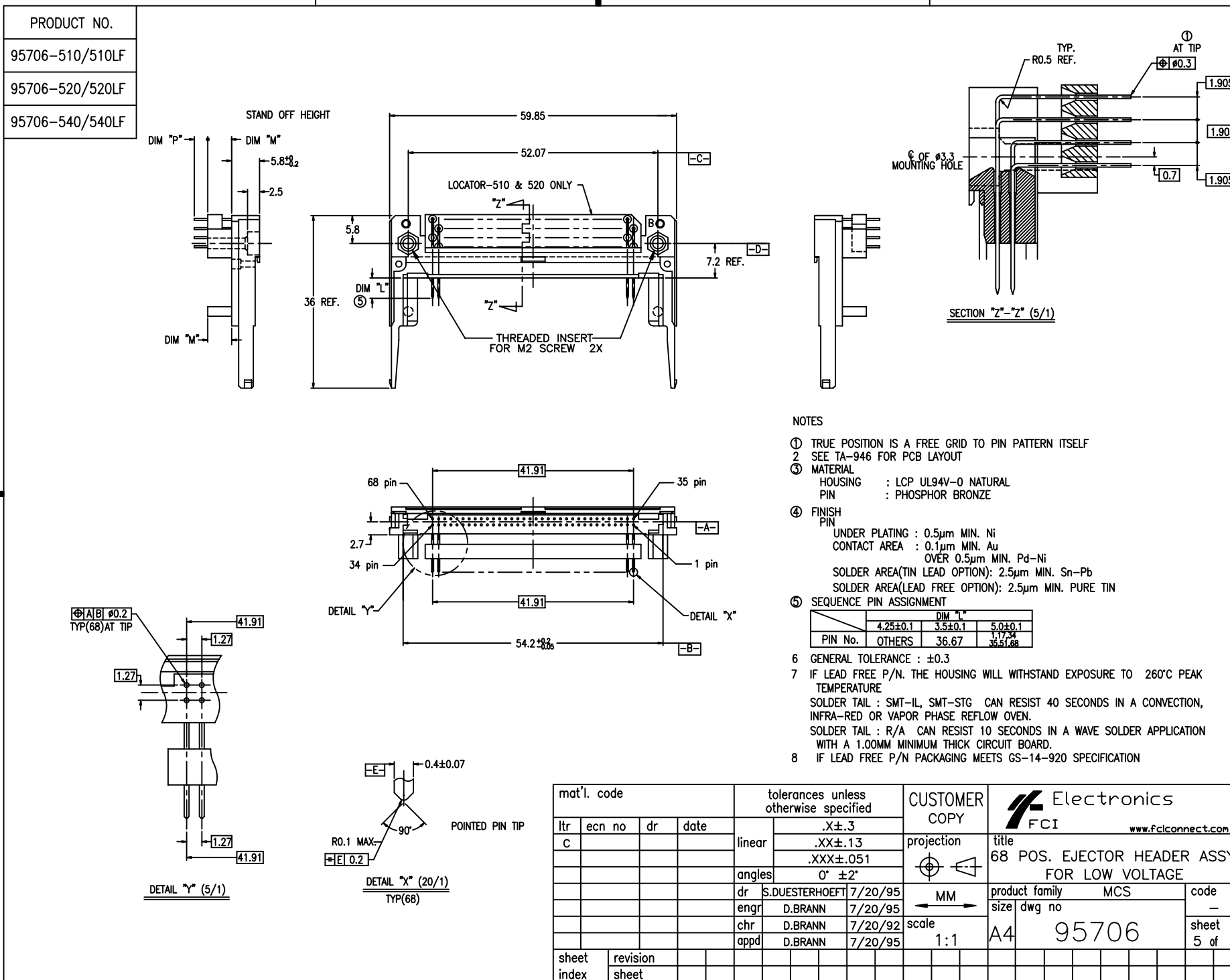


mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		Electronics FCI	
ltr	ecn no	dr	date	linear	.X±.3	projection	title	www.fciconnect.com	
C				linear	.XX±.13	MM	68 POS. EJECTOR HEADER ASSY.		
				angles	.XXX±.051	1:1	FOR LOW VOLTAGE		
				dr	S.DUESTERHOEFT 7/20/95		product family	MCS	code
				enr	D.BRANN 7/20/95		size	dwg no	—
				chr	D.BRANN 7/20/92		A4	95706	sheet
				appd	D.BRANN 7/20/95				4 of
sheet index	revision sheet								

A

FCI

B



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B