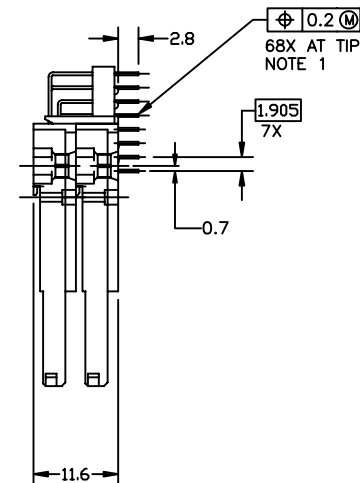
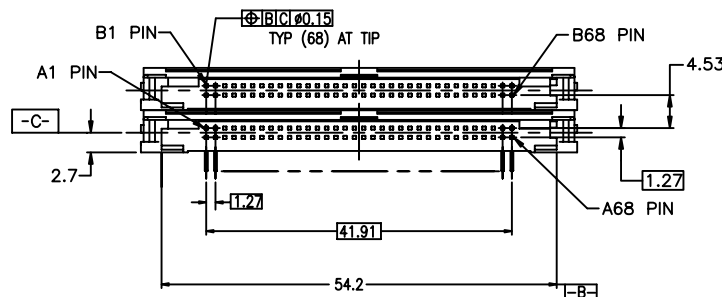
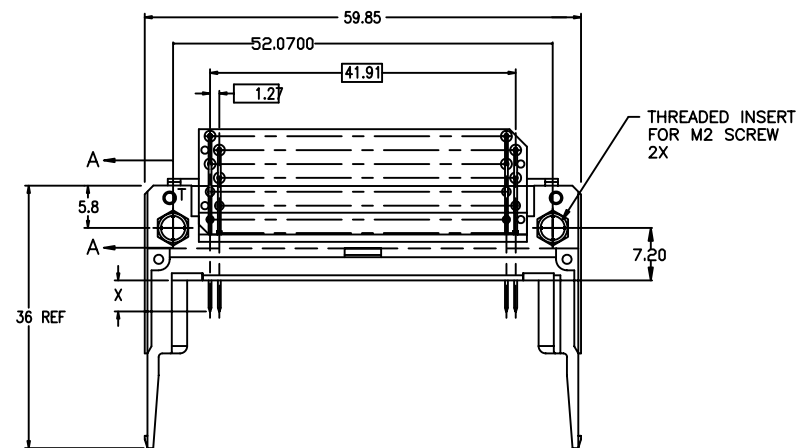


FCI

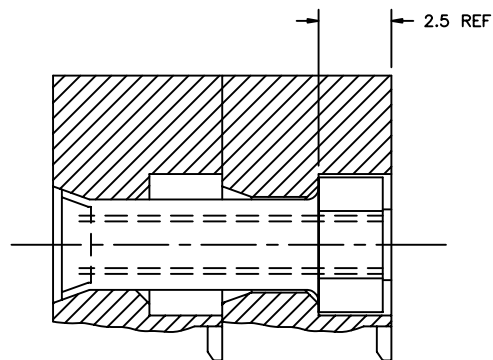
B

PRODUCT NO	DIM X			MOUNTING STYLE	COLOR
	4.25±0.1	3.5±0.1	5.0±0.1		
95705-000	OTHERS	36,67	1,17,34 35,51,68	TOP W\O LOCATOR	BLACK
95705-000LF	OTHERS	36,67	1,17,34 35,51,68	TOP W\O LOCATOR	BLACK



NOTES:

- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL:
PLASTIC: LCP UL94V-0
PIN: PHOSPHOR BRONZE
- FINISH (PIN):
UNDER PLATING: 0.5 μ M MIN. Ni
CONTACT AREA: 0.1 μ M MIN GOLD OVER 0.5 μ M MIN. Pd-Ni
SOLDER AREA: 2.5 μ M MIN. Sn-Pb
: 2.5 μ M MIN. PURE Sn (FOR -***LF)
- IF LEAD FREE P/N.THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- IF LEAD FREE P/N.THE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
- LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION



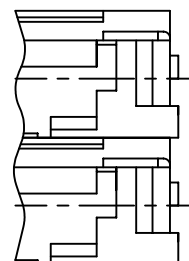
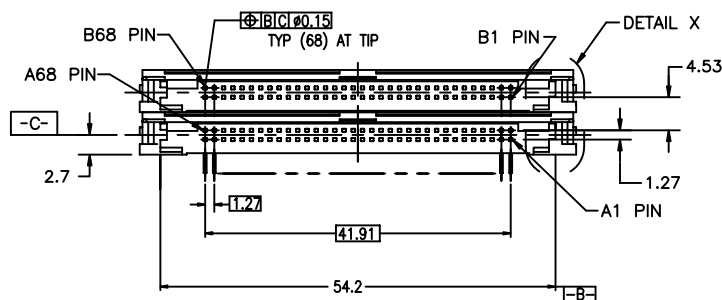
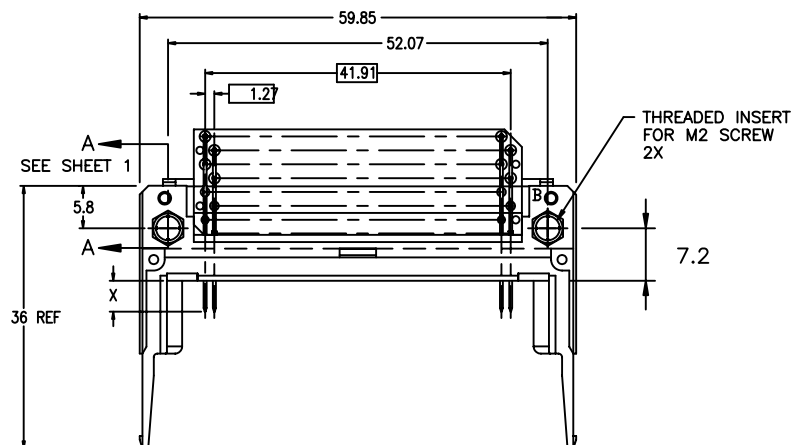
SECTION A-A
SCALE 8/1

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		Electronics FCI	
ltr	ecn no	dr	date	linear	.X±.3	projection	title	www.fciconnect.com	
A	V50549	SSD	7/17/95		.XX±.13		3.3 VOLT DOUBLE DECK HEADER		
B	T50281	GAH	10/20/95		.XXX±.051				
C	V12164	TFG	8/17/01	angles	0° ±2°	MM	product family MCS	code	
D	N05-0174	WB	06/16/05	dr	S.DUESTERHOEFT 7/17/95			size dwg no	
				enr	D.BRANN 7/17/95	scale	A4	95705	
				chr	D.BRANN 7/17/95			sheet	
				appd	D.BRANN 7/17/95	1:1		1 of 4	
sheet index	revision sheet	D	D	D	D			cage code	
		1	2	3	4				

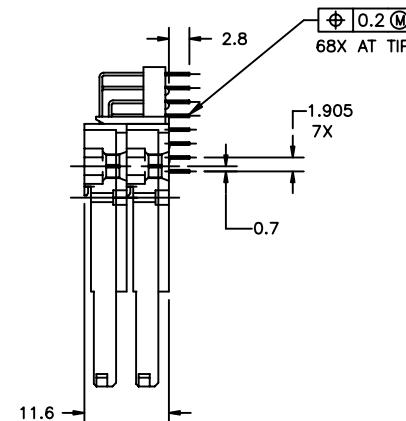
FCI

B

PRODUCT NO	DIM X			MOUNTING STYLE	COLOR
	4.25±0.1	3.5±0.1	5.0±0.1		
95705-500	OTHERS	36,67	1,17,34 35,51,68	BOTTOM W\O LOCATOR	NATURAL (WHITE)
95705-500LF	OTHERS	36,67	35,51,68	BOTTOM W\O LOCATOR	NATURAL (WHITE)



DETAIL X
SCALE: 3/1



NOTES:

- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL:
PLASTIC: LCP UL94V-0
PIN: PHOSPHOR BRONZE
- FINISH (PIN):
UNDER PLATING: 0.5 μ M MIN. Ni
CONTACT AREA: 0.1 μ M MIN GOLD OVER 0.5 μ M MIN. Pd-Ni
SOLDER AREA: 2.5 μ M MIN. Sn-Pb
: 2.5 μ M MIN. PURE Sn (FOR -***LF)
- IF LEAD FREE P/N.THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- IF LEAD FREE P/N.THE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
- LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY	Electronics		
ltr	ecn no	dr	date	linear	.X±.3		FCI		
D				linear	.XX±.13	projection	www.fciconnect.com		
				angles	.XXX±.051		title		
					0° ±2°		3.3 VOLT DOUBLE DECK HEADER		
				dr	S.DUESTERHOEFT 7/17/95	MM	product family MCS		code
				enr	D.BRANN 7/17/95	scale	size dwg no		—
				chr	D.BRANN 7/17/95	1:1	A4 95705		sheet
				appd	D.BRANN 7/17/95				2 of
sheet index	revision sheet						cage code		

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PDM: Rev:D

STATUS: Released 26 Printed: Sep 28, 2007

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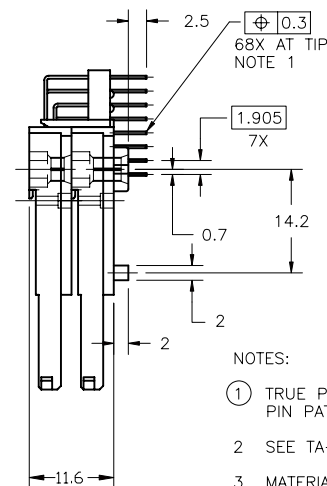
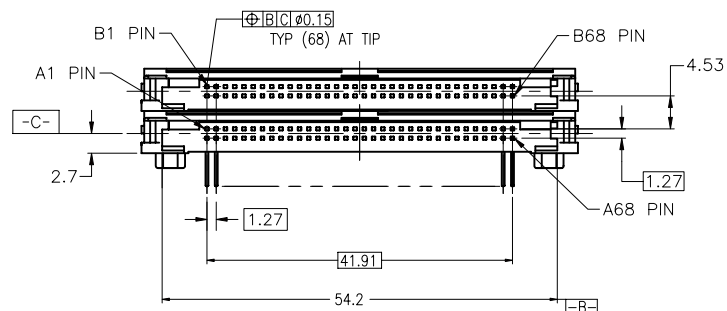
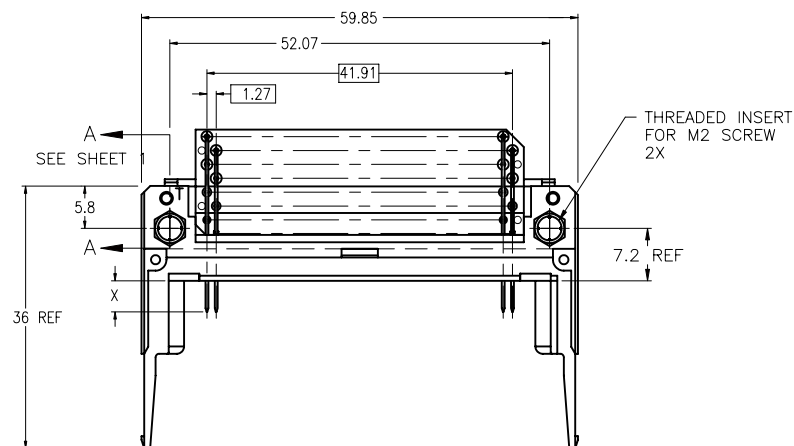
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FCI

A

B

PRODUCT NO.	DIM X			MOUNTING STYLE	COLOR
	4.25±0.1	3.5±0.1	5.0±0.1		
95705-040	OTHERS	36,67	1,17,34 35,51,68	TOP W\O LOCATOR	BLACK
95705-040LF	OTHERS	36,67	1,17,34 35,51,68	TOP W\O LOCATOR	BLACK



NOTES:

- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL:
PLASTIC: LCP UL94V-0
PIN: PHOSPHOR BRONZE
- FINISH (PIN):
UNDER PLATING: 0.5 μ M MIN. Ni
CONTACT AREA: 0.1 μ M MIN GOLD OVER 0.5 μ M MIN. Pd-Ni
SOLDER AREA: 2.5 μ M MIN. Sn-Pb
: 2.5 μ M MIN. PURE Sn (FOR -***LF)
- IF LEAD FREE P/N.THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- IF LEAD FREE P/N.THE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
- LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION

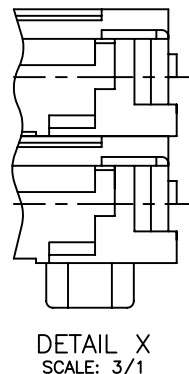
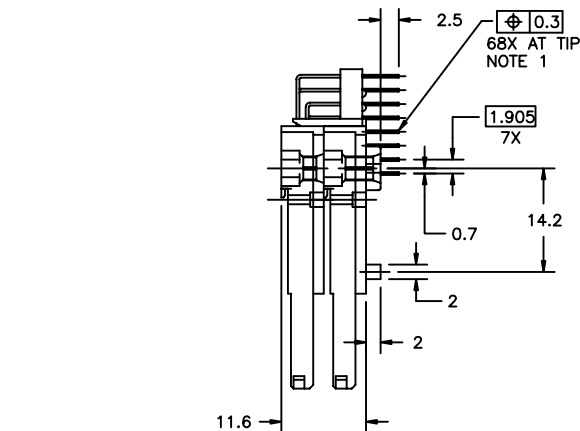
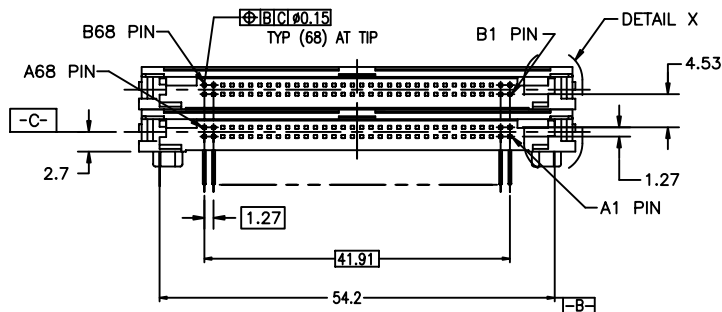
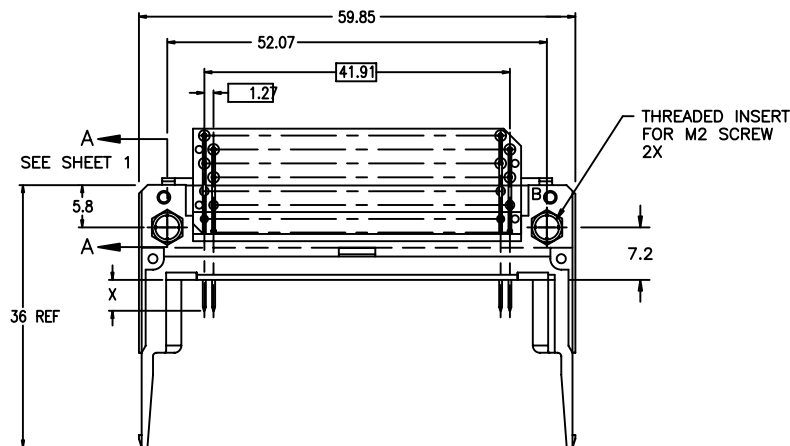
mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY	Electronics FCI www.fciconnect.com		
ltr	ecn no	dr	date	linear	.X±.3 .XX±.13 .XXX±.051 0° ±2°		title 3.3 VOLT DOUBLE DECK HEADER		
D				angles		projection	product family MCS		
				dr	S.DUESTERHOEFT 7/17/95	MM	size dwg no		
				enr	D.BRANN 7/17/95	scale	A4 95705		
				chr	D.BRANN 7/17/95	1:1	sheet 3 of		
				appd	D.BRANN 7/17/95				
sheet index	revision sheet						cage code		

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PDM: Rev:D

STATUS: Released 26 Printed: Sep 28, 2007

PRODUCT NO	DIM X			MOUNTING STYLE	COLOR
	4.25±0.1	3.5±0.1	5.0±0.1		
95705-540	OTHERS	36,67	$\frac{1,17,34}{35,51,68}$	BOTTOM W\O LOCATOR	NATURAL (WHITE)
95705-540LF	OTHERS	36,67	$\frac{1,17,34}{35,51,68}$	BOTTOM W\O LOCATOR	NATURAL (WHITE)



NOTES:

- TRUE POSITION IS A FREE GRID TO THE PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL:
PLASTIC: LCP UL94V-0
PIN: PHOSPHOR BRONZE
- FINISH (PIN):
UNDER PLATING: 0.5 μ M MIN. Ni
CONTACT AREA: 0.1 μ M MIN GOLD OVER 0.5 μ M MIN. Pd-Ni
SOLDER AREA: 2.5 μ M MIN. Sn-Pb
: 2.5 μ M MIN. PURE Sn (FOR -***LF)
- IF LEAD FREE P/N.THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- IF LEAD FREE P/N.THE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
- LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION

mat'l. code				tolerances unless otherwise specified		CUSTOMER COPY		Electronics FCI	
ltr	ecn no	dr	date	linear	.X±.3	projection	title	product family	code
D				linear	.XX±.13	ϕ ∇	3.3 VOLT DOUBLE DECK HEADER	MCS	-
				angles	.XXX±.051			size	sheet
					0° ±2°			dwg no	4 of
				dr	S.DUESTERHOEFT 7/17/95	MM			
				enr	D.BRANN 7/17/95	scale			
				chr	D.BRANN 7/17/95	1:1			
				appd	D.BRANN 7/17/95				
sheet index	revision sheet								

cage code

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STATUS: Released 26 Printed: Sep 28, 2007