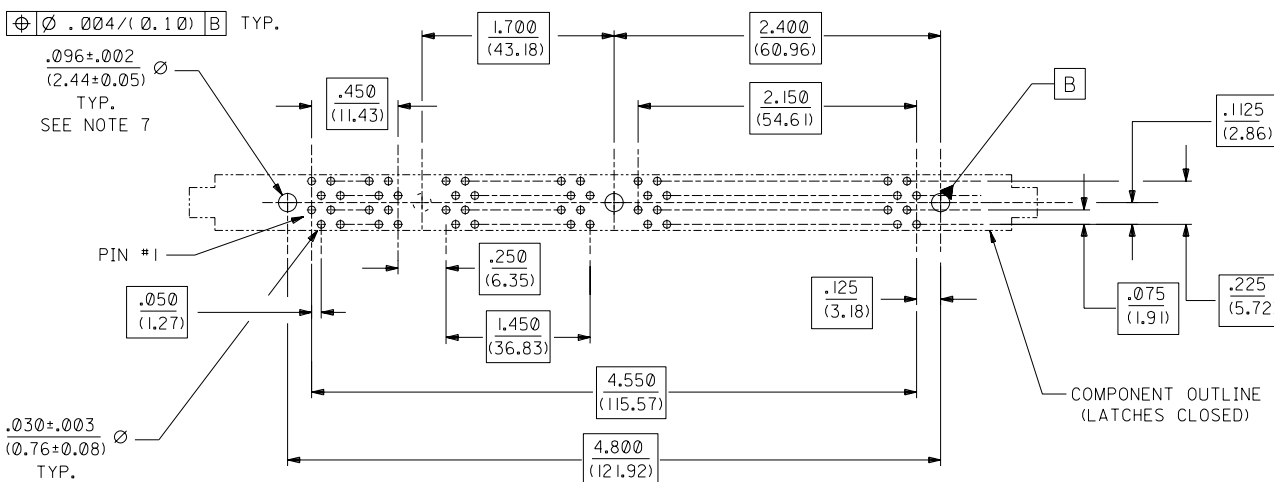
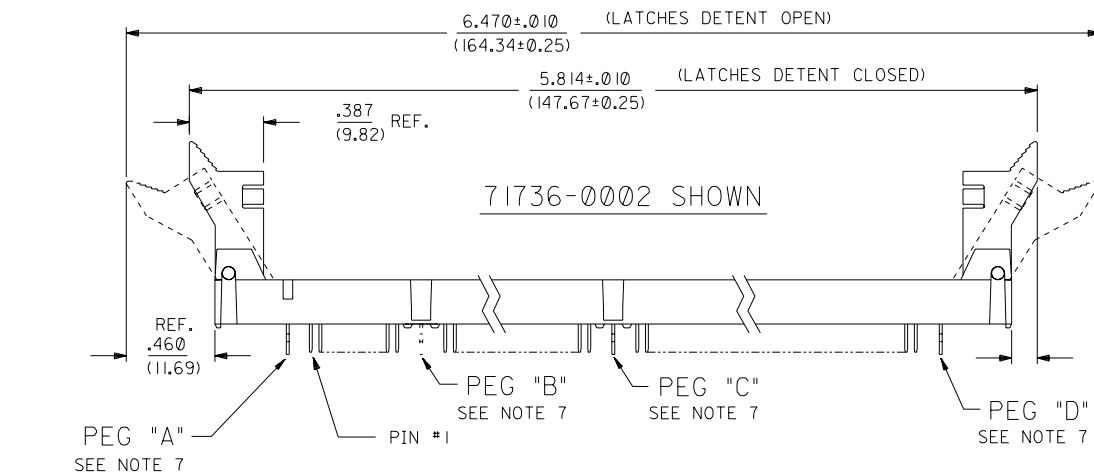
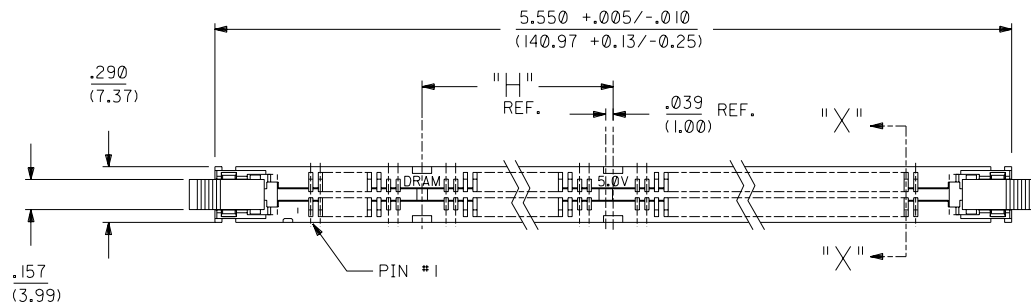




RECOMMENDED
P.C. BOARD HOLE PATTERN
(CONNECTOR SIDE)

NOTES:

1. CARD SLOT ACCEPTS .050 ± 0.004 (1.27 ± 0.10) MODULE THICKNESS. (MEASURED OVER P.C. PADS).
2. RECOMMENDED PCB THICKNESS: .062 ± 0.007 (1.57 ± 0.18)
3. REFER TO PRODUCT SPEC PS-71243-9999 FOR PERFORMANCE SPECIFICATIONS.
4. PRODUCT IS PACKAGED IN TRAYS.
5. RECOMMENDED MODULE LAYOUT SHALL BE PER JEDEC MO-161.
6. RECOMMENDED PLATING ON MODULE PADS: 30 MICROINCH (0.76 MICROMETER) MINIMUM HARD GOLD (Au) OVER 79 MICROINCH (2.0 MICROMETER) MINIMUM NICKEL (Ni).
7. SEE CHART FOR FORK/HOLE PRESENCE AND PLATING OPTION.
8. PRODUCT WILL HAVE DATE CODE STAMPED ON SIDE OF HOUSING.

MATERIALS:

HOUSING - GLASS FILLED LIQUID CRYSTAL POLYMER (LCP), UL 94V-0, COLOR: BLACK.
TERMINAL - PHOSPHOR BRONZE
LATCHES - GLASS FILLED HIGH TEMPERATURE NYLON, UL 94V-0, COLOR: IVORY.
FORKLOCKS - STAINLESS STEEL

PLATING:

CONTACT AREA: OPTION A: GOLD (Au) FLASH; THICKNESS=2-10 MICROINCH (0.05-0.25 MICROMETER), OVER PALLADIUM-NICKEL (Pd-Ni); THICKNESS=30 MICROINCH (0.76 MICROMETER) MINIMUM.
OPTION B: GOLD (Au) FLASH; THICKNESS=2-10 MICROINCH (0.05-0.25 MICROMETER), OVER PALLADIUM-NICKEL (Pd-Ni); THICKNESS=20 MICROINCH (0.51 MICROMETER) MINIMUM.

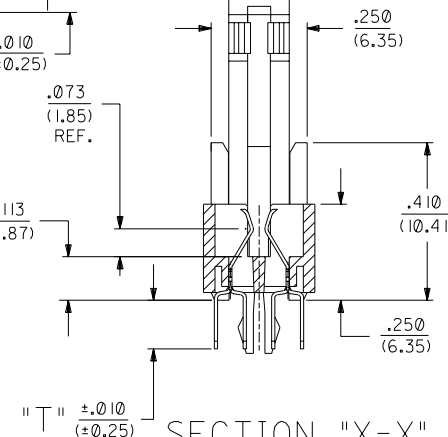
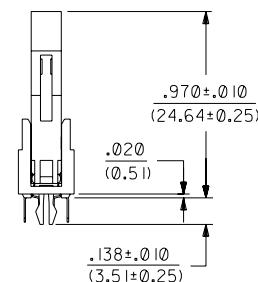
SOLDER TAILS: TIN (Sn); THICKNESS=150 MICROINCH (3.81 MICROMETER) MINIMUM.

UNDERPLATE: NICKEL (Ni) OVER ENTIRE CONTACT.

FORKLOCKS: NONE

NOTE FOR LEAD FREE CONVERSION:

THE PRIMARY SHIPPING CARTON WILL BE LABELED "COMPLIANT TO ROHS DIRECTIVE 2002/95/EC AND ELV ANNEX II OF DIRECTIVE 2000/53/EC". CARTONS WITHOUT THIS LABEL MAY CONTAIN PRODUCT WITH LEAD.



SCALE 4:1

			B	ECR# U61050 2-28-96 JCL
3	D3		A	INITIAL RELEASE PER ECR# U60675 12-6-95 JCL
2	D3			
1	E1			
MFG.	SH.	REV.	LTR.	REVISIONS

DIMENSIONS SHOWN (METRIC) INCH		UNLESS OTHERWISE SPECIFIED TOLERANCES: ANGULAR ± 1/2°	
INCH	METRIC	INCH	METRIC
3 PLACE ± .005	---	2 PLACE ± ---	± 0.13
1 PLACE ---	± 0.25	---	± 0.25
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			
DRWG. BY JCL	CHK'D. BY DCB	SCALE 2:1	DATE 12/06/95
FILE NAME 571736-01.DWG		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION	

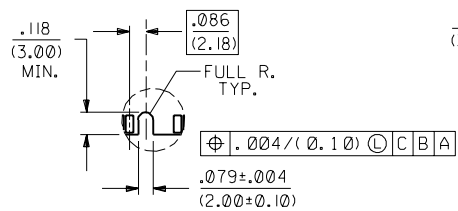
REVISIONS		REVISIONS	
ADD LEAD FREE NOTE ECN# UCPC2004-1732 DMORGAN 04/03/09	E1	CHG Sn-Pb THICKNESS ECN# U01999-0454 DMORGAN 99/08/09	E
REVISED PER ECR# U71162 97-4-15 LAURX	D3	REVISED PER ECR# U70731 97-1-8 LAURX	D2
REVISED PER ECR# U70361 96-10-9 LAURX	D1	REVISED PER ECR# U61133 96-9-26 LAURX	D
REVISED PER ECR# U61419 5-28-96 JCL	C	REVISED PER ECR# U61050 2-28-96 JCL	B
INITIAL RELEASE PER ECR# U60675 12-6-95 JCL	A		

SEE CHART SDA-71736-0***

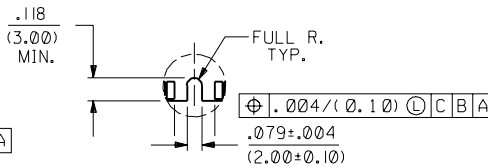
13		12		11		10		9		8		7		6		71736	
X= N.T.	ITEM NO. 71736-0***	CKT SIZE	DESCRIPTION	COLOR	FUNCTION KEY	VOLTAGE KEY	PEG "A"	PEG "B"	PEG "C"	PEG "D"	DIM. 'H'	DIM. 'T'	CONTACT AREA PLATING				
	-0001	168	3.3 VOLT STD DRAM	BLACK	CENTER	CENTER	FORK	NONE	FORK	FORK	1.700 (43.18)	.127 (3.23)	OPTION A				
	-0002	168	5.0 VOLT STD DRAM	BLACK	CENTER	OFFSET LEFT	FORK	NONE	FORK	FORK	1.700 (43.18)	.127 (3.23)	OPTION A				
	-0011	168	3.3 VOLT UNBUFFERED	BLACK	OFFSET RIGHT	CENTER	FORK	NONE	FORK	FORK	1.661 (42.19)	.127 (3.23)	OPTION A				
	-0005	168	5.0 VOLT UNBUFFERED	BLACK	OFFSET RIGHT	OFFSET LEFT	FORK	NONE	FORK	FORK	1.661 (42.19)	.127 (3.23)	OPTION A				
	-0006	168	3.3 VOLT SYNCHRONOUS	BLACK	OFFSET LEFT	CENTER	FORK	NONE	FORK	FORK	1.739 (44.17)	.127 (3.23)	OPTION A				
	-0007	168	5.0 VOLT SYNCHRONOUS	BLACK	OFFSET LEFT	OFFSET LEFT	FORK	NONE	FORK	FORK	1.739 (44.17)	.127 (3.23)	OPTION A				

NOTES:

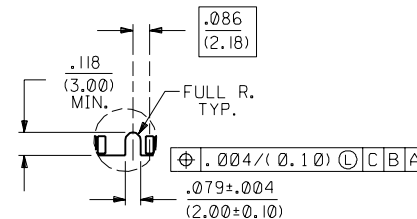
1. STRAIGHTNESS OF MODULE APPLIES TO THE AREA FROM THE BOTTOM OF THE CARD UP .157/(4.00).
2. IF TIE BARS ARE ATTACHED TO PADS, THE TIE BAR SHOULD BE ON AN INTERNAL LAYER, SO THAT THE REMNANT CANNOT CAUSE DAMAGE TO THE CONTACTS.



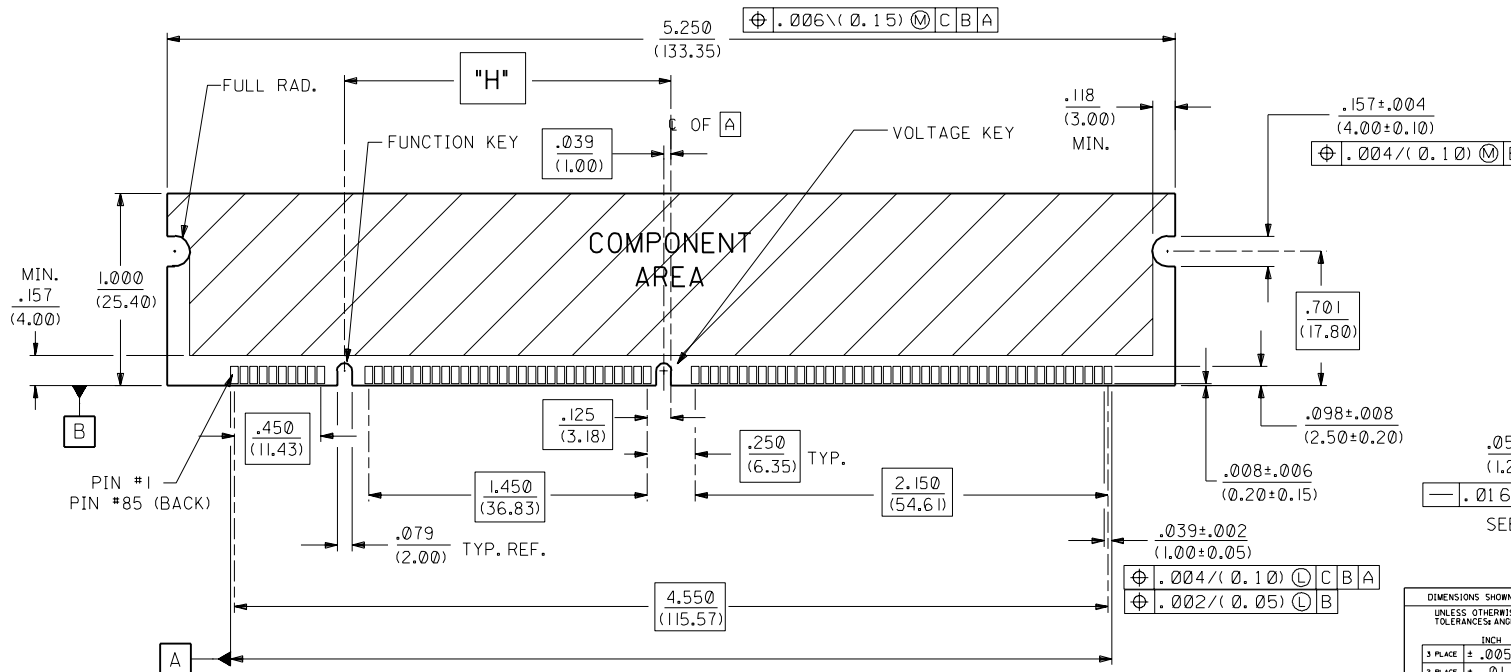
OFFSET LEFT KEY



CENTERED KEY



OFFSET RIGHT KEY



RECOMMENDED MODULE LAYOUT
168 CKT 5.0V DRAM SHOWN

D3	SEE SHEET I
D2	SEE SHEET I
D1	SEE SHEET I
D	SEE SHEET I
C	SEE SHEET I
B	SEE SHEET I
A	SEE SHEET I

DIMENSIONS SHOWN (METRIC) INCH		UNLESS OTHERWISE SPECIFIED TOLERANCES ANGULAR ± 1/2°	
5 PLACE	± .005	INCH	METRIC
2 PLACE	± .01	± 0.13	
1 PLACE	---	± 0.25	
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			
DRW'G. BY: JCL	CHK'D. BY:	SCALE: 2: 1	
APP'D. BY: DCB			
TITLE: .050 PITCH MULTI-KEY DIMM TALL LATCH RAM ASSEMBLY W/FORKLOCKS - SALES DWG		REVISE ONLY ON CAD SYSTEM	
PART NO. 71736-0...		DATE 12/06/95	
MOLEX INCORPORATED		U.S.A.	
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION.		DIV. CP	