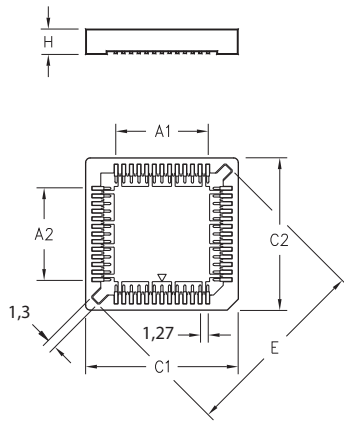
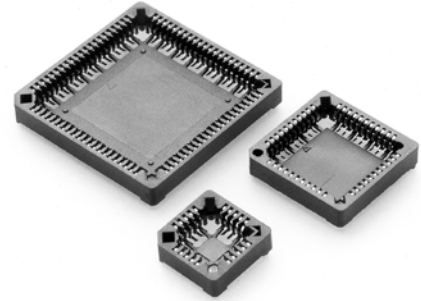


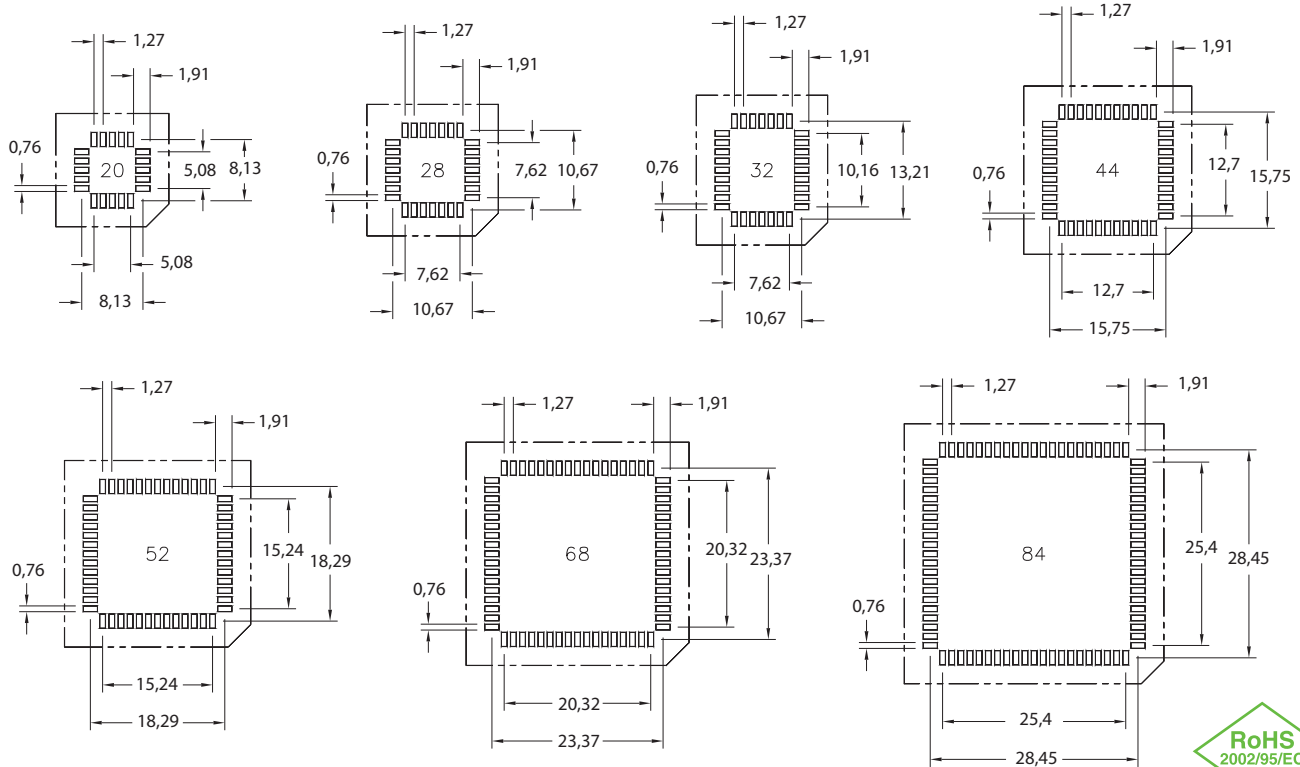


- Note: End stackable.
- Designed for JEDEC type devices.
- Open frame design in solder area improves results of IR soldering and facilitates visual inspection of solder pads.
- The insulator is molded PPS (Ryton R-4).
- PLCC chip extraction tool available, see page 96.



SURFACE MOUNT

PCB LAYOUT FOR SURFACE MOUNT



# of Pins	ORDERING INFORMATION	Qty. per Tube	Tape Width mm	Qty. per Reel	A1	A2	C1	C2	E	H
20	540-44-020-17-40000X	34	24	490	5,08	5,08	14,86	14,86	16,69	4,57
28	540-44-028-17-40000X	29	32	400	7,62	7,62	17,4	17,4	20,29	4,57
32*	540-44-032-17-40000X*	26	32	400	7,62	10,16	17,02	19,56	22,48	3,76
44	540-44-044-17-40000X	22	44	250	12,7	12,7	22,48	22,48	27,48	4,57
52	540-44-052-17-40000X	20	44	250	15,24	15,24	25,4	25,4	31,09	4,57
68	540-44-068-17-40000X	16	44	250	20,32	20,32	30,53	30,53	38,28	4,57
84	540-44-084-17-40000X	14	56	250	25,4	25,4	35,56	35,56	45,49	4,57

Plating Code
44 = 3,81 µm Sn ◇

Packaging Code
X = 0 Tubes
X = 4 Tape & Reel

*Rectangular