TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC125P,TC74AC125F,TC74AC125FN,TC74AC125FT TC74AC126P,TC74AC126F,TC74AC126FN

TC74AC125P/F/FN/FT Quad Bus Buffer TC74AC126P/F/FN Quad Bus Buffer

The TC74AC125/126 are advanced high speed CMOS QUAD BUS BUFFERs fabricated with silicon gate and double-layer metal wiring $\rm C^2MOS$ technology.

They achieve the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

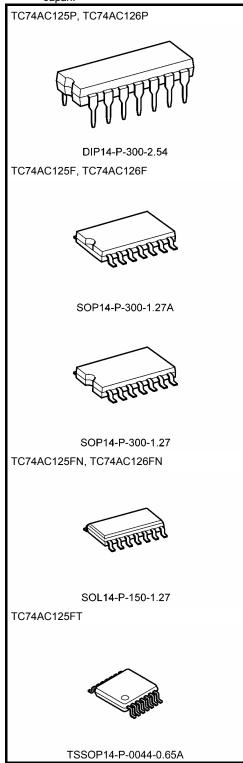
The TC74AC125 requires the 3-state control input \overline{G} to be set high to place the output into the high impedance state, whereas the TC74AC126 requires the control input to be set low to place the output into high impedance.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 4.0$ ns (typ.) at $V_{CC} = 5$ V
- Low power dissipation: $I_{CC} = 8 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 24$ mA (min) Capability of driving 50 Ω transmission lines.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 5.5 V
- Pin and function compatible with 74F125/126

Note: xxxFN (JEDEC SOP) is not available in Japan.

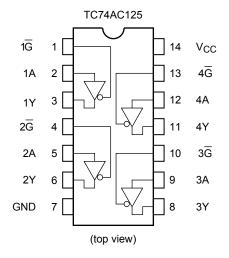


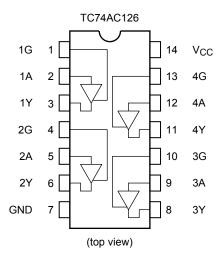
Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) SOP14-P-300-1.27 : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.)

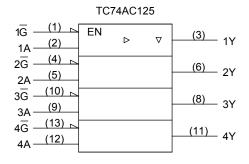


Pin Assignment





IEC Logic Symbol



TC74AC126								
1G—(1) 1A—(2)	EN	D	V	(3) 1Y				
2G — (4) 2A — (5)				(6) 2Y				
3G (10) 3A (9)				(8) 3Y				
4G (13) 4A (12)				(11) 4Y				

Truth Table

TC74AC125

Inputs		Output
Ġ	Α	Y
Н	Х	Z
L	L	L
L	Н	Н

X: Don't care

Z: High impedance

TC74AC126

Inputs		Output
G	Α	Υ
L	Χ	Z
Н	L	L
Н	Н	Н

X: Don't care

2

Z: High impedance



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	−0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±100	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0 to 5.5	V	
Input voltage	V _{IN}	0 to V _{CC}	٧	
Output voltage	V _{OUT}	0 to V _{CC}	V	
Operating temperature	T _{opr}	−40 to 85	°C	
Input rise and fall time	dt/dV	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V	
input rise and rail time	a.uv	0 to 20 ($V_{CC} = 5 \pm 0.5 \text{ V}$)		

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.



Electrical Characteristics

DC Characteristics

Characteristics	Test Condition		_	Ta = 25°C		Ta = -40 to 85°C		Unit			
Characteristics	Cymbol				V _{CC} (V)	Min	Тур.	Max	Min	Max	Omit
					2.0	1.50	_	_	1.50	_	
High-level input voltage	V_{IH}		_		3.0	2.10	_	_	2.10	_	V
G					5.5	3.85	_	_	3.85	_	
					2.0	_	_	0.50	_	0.50	
Low-level input voltage	V_{IL}		_		3.0	_	_	0.90	_	0.90	V
G					5.5	-	_	1.65	_	1.65	
					2.0	1.9	2.0	_	1.9	_	
			I _{OH} = -50 μA		3.0	2.9	3.0	_	2.9	_	
High-level output	V _{OH}	V _{IH} or			4.5	4.4	4.5	_	4.4	_	V
voltage	VOH	V _{IL}	$I_{OH} = -4 \text{ mA}$		3.0	2.58	_	_	2.48	_	v
			I _{OH} = −24 mA		4.5	3.94	_	_	3.80	_	
			I _{OH} = -75 mA	(Note)	5.5	_	_	_	3.85	_	
					2.0	_	0.0	0.1	_	0.1	V
		V _{IH} or V _{IL}	I _{OL} = 50 μA		3.0	_	0.0	0.1	_	0.1	
Low-level output	V _{OL}				4.5	_	0.0	0.1	_	0.1	
voltage	VOL		I _{OL} = 12 mA		3.0	_	_	0.36	_	0.44	
			I _{OL} = 24 mA		4.5	_	_	0.36	_	0.44	
			I _{OL} = 75 mA	(Note)	5.5	_	_	_	_	1.65	
3-state output off-state current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		5.5	_	_	±0.5	_	±5.0	μΑ	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		5.5	_	_	±0.1	_	±1.0	μΑ	
Quiescent supply current	Icc	V _{IN} = V _C	CC or GND		5.5	_	_	8.0	_	80.0	μΑ

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Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.



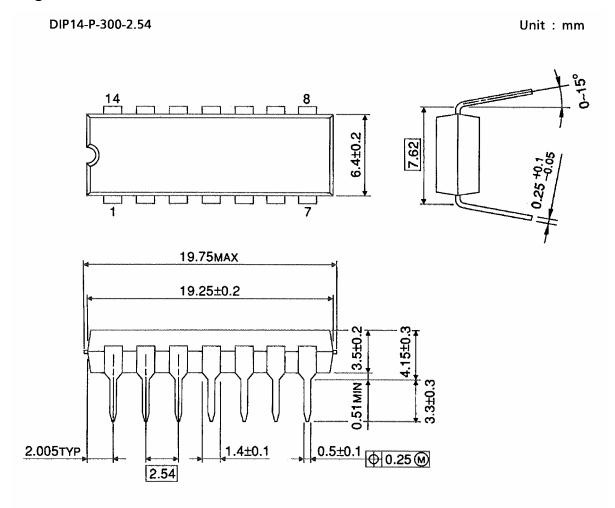
AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

Characteristics	Symbol	Test Condition	Test Condition		Ta = 25°C			Ta = -40 to 85°C	
	,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay	t _{pLH}		3.3 ± 0.3	_	6.4	10.5	1.0	12.0	no
time	t_{pHL}	_	5.0 ± 0.5	_	4.7	7.0	1.0	8.0	ns
Output anable time	t _{pZL}		3.3 ± 0.3	_	7.1	12.3	1.0	14.0	ns
Output enable time	t_{pZH}	_	5.0 ± 0.5	_	5.0	7.9	1.0	9.0	
Output disable time	t _{pLZ}		3.3 ± 0.3	_	5.1	8.8	1.0	10.0	20
Output disable time t _{ph}	t_{pHZ}	_	5.0 ± 0.5	_	4.6	6.6	1.0	7.5	ns
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Output capacitance	C _{OUT}	_		_	10	_	_	_	pF
Power dissipation capacitance	C _{PD}		(Note)	_	24	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 (per gate)$

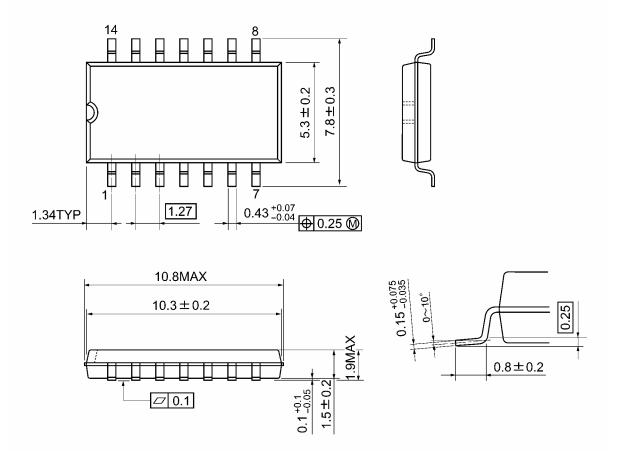


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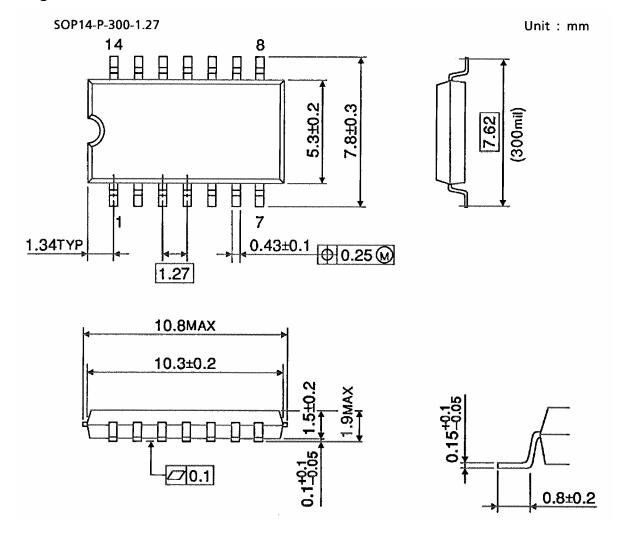
Weight: 0.96 g (typ.)



SOP14-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

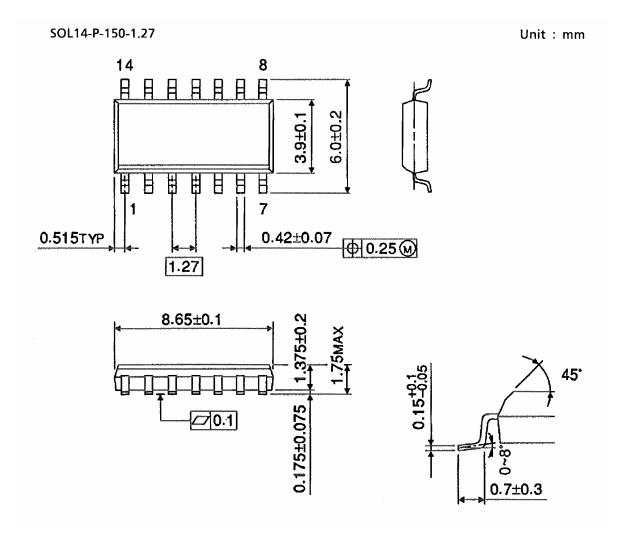


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Weight: 0.18 g (typ.)



Package Dimensions (Note)

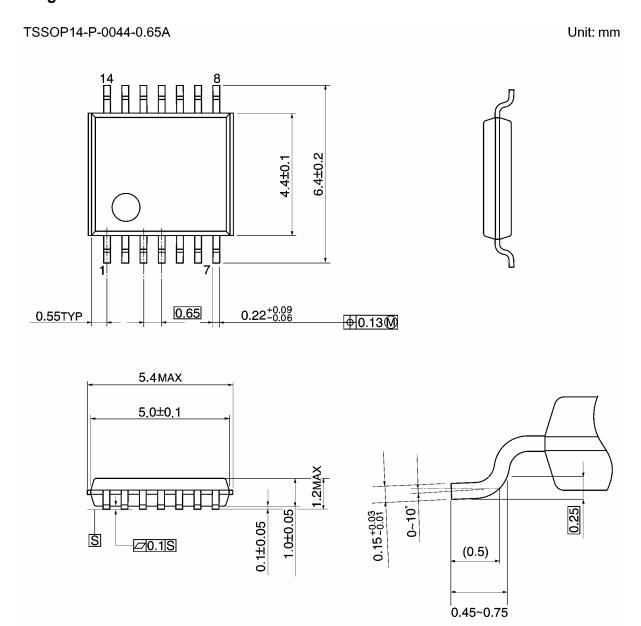


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Note: This package is not available in Japan.

Weight: 0.12 g (typ.)





Weight: 0.06 g (typ.)

Note: Lead (Pb)-Free Packages

DIP14-P-300-2.54 SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A

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