TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK3309

Switching Regulator Applications

- Low drain-source ON resistance: RDS (ON) = 0.48Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 4.3 \text{ S (typ.)}$
- Low leakage current: $IDSS = 100 \mu A (max) (VDS = 450 V)$
- Enhancement-mode: $V_{th} = 3.0 \sim 5.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit	
Drain-source voltage			V_{DSS}	450	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	450	٧	
Gate-source voltage			V _{GSS}	±30	V	
Drain current	DC	(Note 1)	ID	10	А	
	Pulse	(Note 1)	I _{DP}	40		
Drain power dissipation (Tc = 25°C)			PD	65	W	
Single pulse avalanche energy (Note 2)			E _{AS}	222	mJ	
Avalanche current			I _{AR}	10	Α	
Repetitive avalanche energy (Note 3)			E _{AR}	6.5	mJ	
Channel temperature			T _{ch}	150	°C	
Storage temperature range			T _{stg}	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

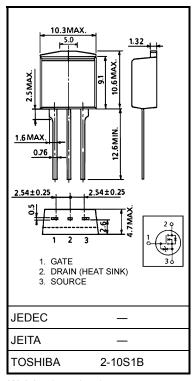
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.92	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

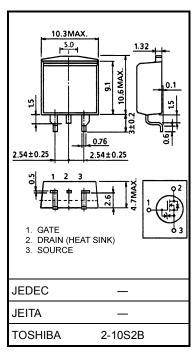
- Note 1: Please use devise on condition that the channel temperature is below 150°C.
- Note 2: $V_{DD}=90$ V, $T_{ch}=25^{\circ}C$ (initial), L = 3.7 mH, $R_{G}=25~\Omega,$ $I_{AR}=10~A$
- Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

Unit: mm



Weight: 1.5 g (typ.)



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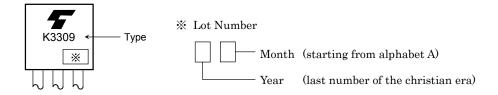
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Gate -source brea	Gate -source breakdown voltage		$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off curre	ent	I _{DSS}	V _{DS} = 450 V, V _{GS} = 0 V	10		100	μА
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	450 550	_	_	V
Gate threshold vo	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	3.0	_	5.0	V
Drain-source ON	resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 5 A	_	0.48	0.65	Ω
Forward transfer admittance		Y _{fs}	$V_{DS} = 10 \text{ V}, I_D = 5 \text{ A}$	1.5	4.3	_	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	920	_	pF
Reverse transfer capacitance		C _{rss}		_	12	_	
Output capacitance		Coss		_	140	_	
Switching time	Rise time	t _r	10 V		25		ns
	Turn-on time	t _{on}	0 V		35		
	Fall time	t _f			10	_	115
	Turn-off time	t _{off}	Duty \leq 1%, $t_W = 10 \ \mu s$ $V_{DD} \simeq 200 \ V$	_	60		
Total gate charge		Qg		_	23	_	
Gate-source charge		Q _{gs}	$V_{DD} \simeq 360 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		9		nC
Gate-drain charge		Q _{gd}		_	14	_	

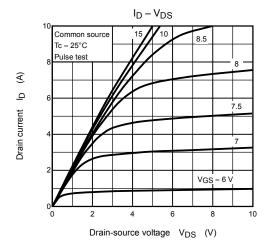
Source-Drain Ratings and Characteristics (Ta = 25°C)

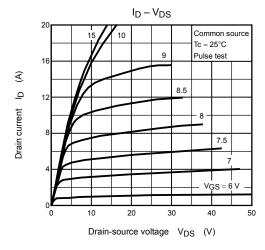
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	40	Α
Forward voltage (diode)	V_{DSF}	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V},$	_	280	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs	_	2.7	_	μС

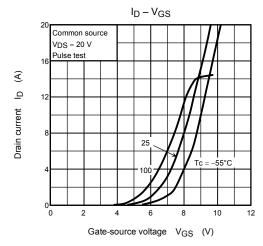
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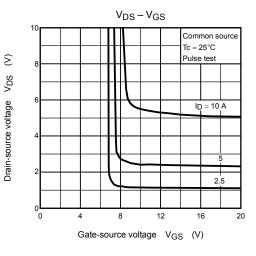


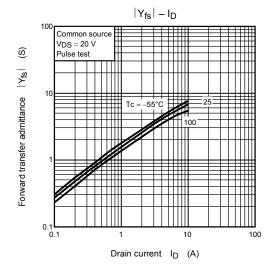
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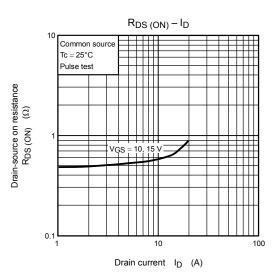


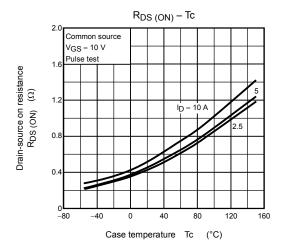


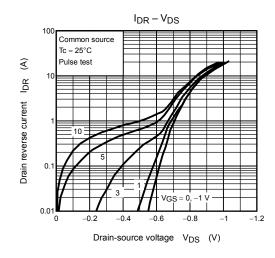


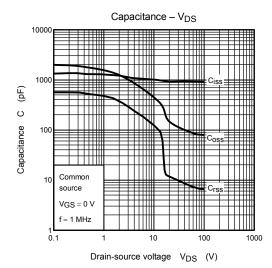


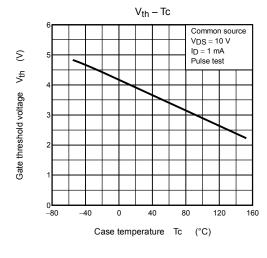


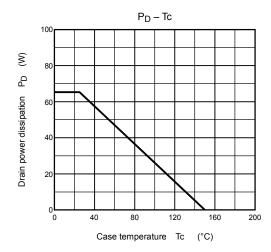


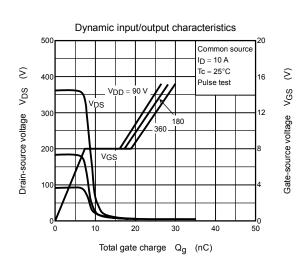












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€

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Drain current

0.1

0.01

* Single nonrepetitive pulse

V_{DSS} max

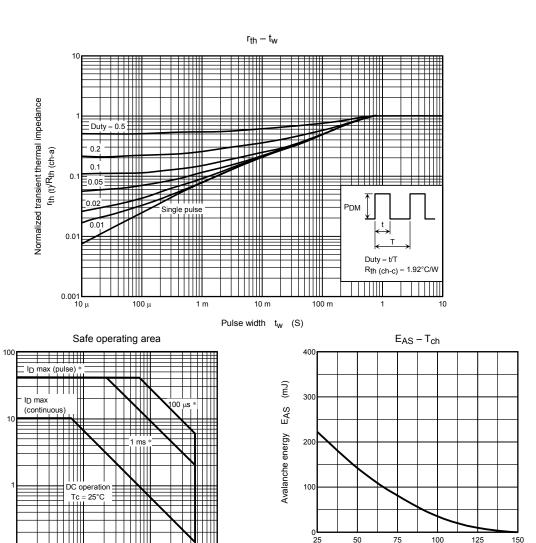
1000

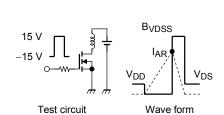
100

Drain-source voltage V_{DS} (V)

 $Tc = 25^{\circ}C$ Curves must be derated linearly with increase in

temperature.





Channel temperature (initial) T_{ch} (°C)

$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 3.7~mH \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot l^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right) \end{aligned}$$

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