TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

## 2SK3068

# Chopper Regulator DC-DC Converter, and Motor Drive Applications

• Low drain—source ON resistance : RDS (ON) =  $0.4 \Omega$  (typ.) • High forward transfer admittance :  $|Y_{fs}| = 9.0 S$  (typ.) • Low leakage current : IDSS =  $100 \mu A$  (max) (VDS = 500 V) • Enhancement mode :  $V_{th} = 2.0 \sim 4.0 V$  (VDS = 10 V, ID = 1 mA)

### Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	500	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	500	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	12	Α	
	Pulse (Note 1)	I <sub>DP</sub>	48	Α	
Drain power dissipation	n (Tc = 25°C)	P <sub>D</sub>	100	W	
Single pulse avalanche energy (Note 2)		EAS	364	mJ	
Avalanche current		I <sub>AR</sub>	12	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	10	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.25	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

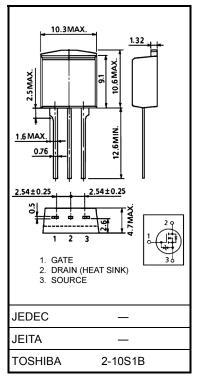
Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 4.3 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 12 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

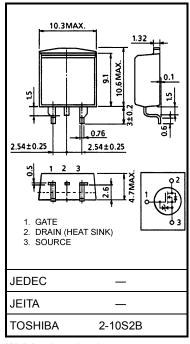
This transistor is an electrostatic-sensitive device.

Please handle with caution.

Unit: mm



Weight: 1.5 g (typ.)



Weight: 1.5 g (typ.)

## Electrical Characteristics (Ta = 25°C)

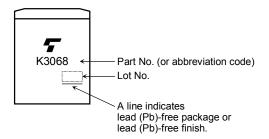
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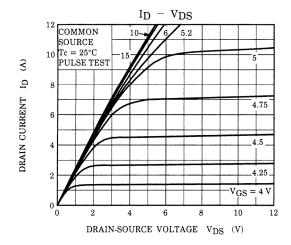
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	_	_	V
Gate threshold v	oltage/	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6 A	_	0.4	0.52	Ω
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6 A	4.0	9.0	_	S
Input capacitano	e	C <sub>iss</sub>			2040	_	
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		200	_	pF
Output capacitance		C <sub>oss</sub>			640	_	
Switching time	Rise time	tr	$V_{GS}$ $V_{OV}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{DD}$ $= 200V$	_	22	_	
	Turn-on time	t <sub>on</sub>		_	58	_	nc
	Fall time	t <sub>f</sub>		l	36	l	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\rm w} = 10 \mu \rm s$	_	180	_	
Total gate charge (Gate-source plus gate-drain)		Qg			45		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		25	_	nC
Gate-drain ("miller") charge		$Q_{gd}$			20	_	

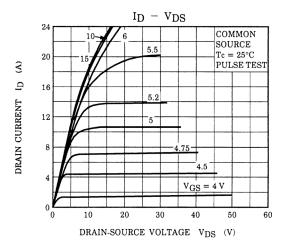
### Source-Drain Ratings and Characteristics (Ta = 25°C)

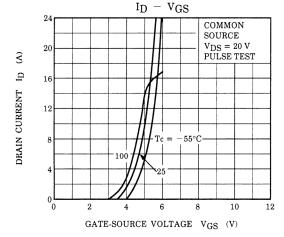
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	12	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	48	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V	_	370		ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 A / μs	1	3.5	1	μC

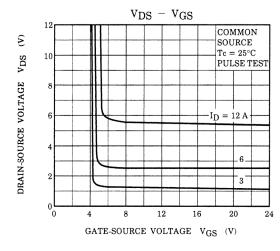
### Marking

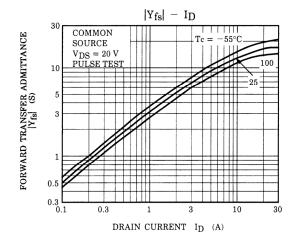


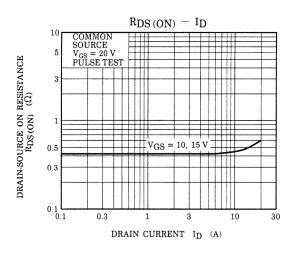


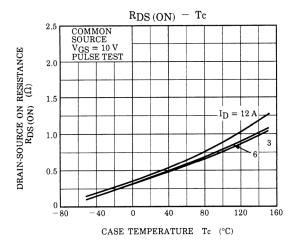


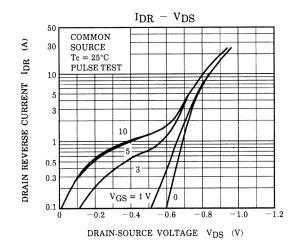


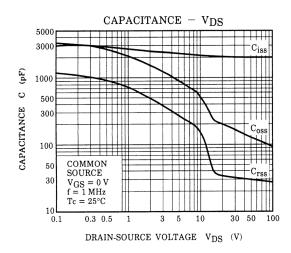


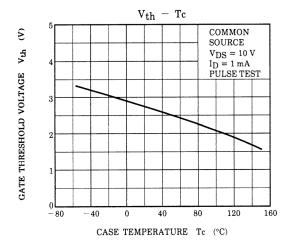


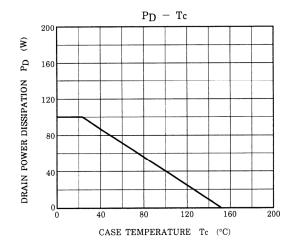


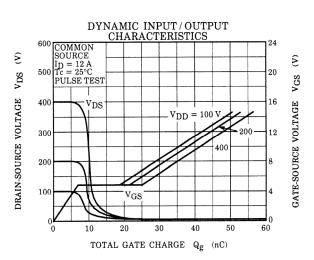




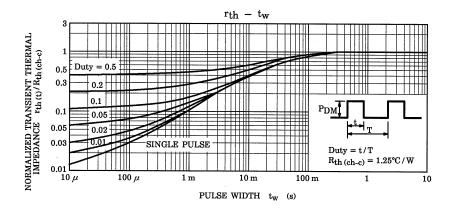


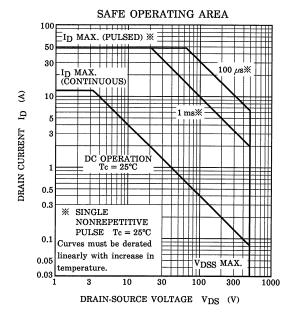


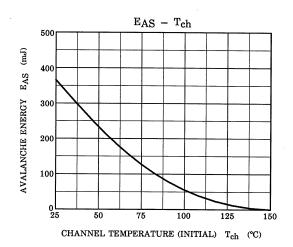


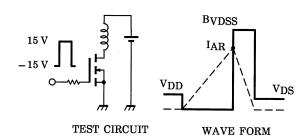


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$$R_G = 25 \Omega$$
  
 $V_{DD} = 90 \text{ V}, L = 4.3 \text{ mH}$ 

$$EAS = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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