Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSIII)

# 2SK3017

# DC-DC Converter, Relay Drive and Motor Drive Applications

• Low drain–source ON resistance : RDS (ON) = 1.05  $\Omega$  (typ.)

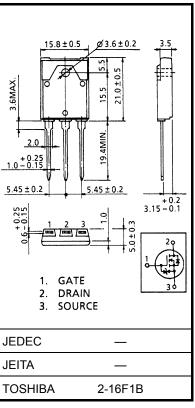
• High forward transfer admittance :  $|Y_{fs}| = 7.0 \text{ S (typ.)}$ 

• Low leakage current :  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 720 \text{ V)}$ 

• Enhancement mode :  $V_{th} = 2.0 \sim 4.0 \text{ V (Vps} = 10 \text{ V, Ip} = 1 \text{ mA})$ 

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	900	V	
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	$V_{DGR}$	900	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	ID	8.5	Α	
	Pulse (Note 1)	I <sub>DP</sub>	25.5	Α	
Drain power dissipation	n (Tc = 25°C)	P <sub>D</sub>	90	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	966	mJ	
Avalanche current		I <sub>AR</sub>	8.5	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	9	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C	



Weight: 5.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.39	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	41.6	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 24.5 mH,  $R_{G}$  = 25  $\Omega$ ,  $I_{AR}$  = 8.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



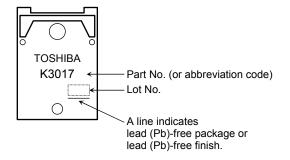
## **Electrical Characteristics (Ta = 25°C)**

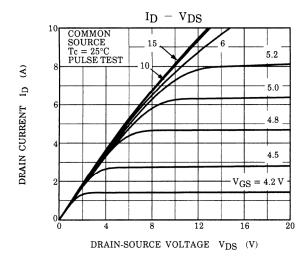
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V <sub>(BR)</sub> GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	900	_	_	V
Gate threshold v	oltage/	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4 A	_	1.05	1.25	Ω
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 4 A	3.5	7.0	_	S
Input capacitano	e	C <sub>iss</sub>		_	2150	_	
Reverse transfe	nsfer capacitance $C_{rss}$ $V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		_	35	_	pF	
Output capacitance		C <sub>oss</sub>			220		_
Switching time	Rise time	t <sub>r</sub>	$V_{GS} \stackrel{10 \text{ V}}{\circ} V \stackrel{\text{I}_{D} = 4 \text{ A}}{\circ} V_{OUT}$ $R_{L} = 100 \Omega$ $V_{DD} = 400 \text{ V}$	_	25	_	- ns
	Turn-on time	t <sub>on</sub>		_	60	_	
	Fall time	t <sub>f</sub>		_	25	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\rm W} = 10 \mu \rm s$	_	120	_	
Total gate charge (Gate-source plus gate-drain)		Qg			70		
Gate-source charge		$Q_{gs}$	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 8 \text{ A}$		37	_	nC
Gate-drain ("miller") charge		$Q_{gd}$			33	_	

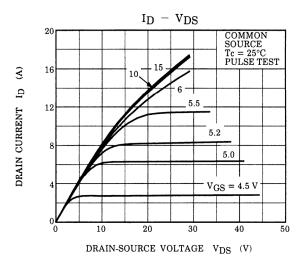
### **Source-Drain Ratings and Characteristics (Ta = 25°C)**

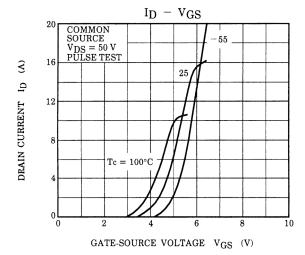
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	8.5	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	25.5	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 8.5 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 8.5 A, V <sub>GS</sub> = 0 V	1	1300	1	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 Å / μs	_	14.5	_	μC

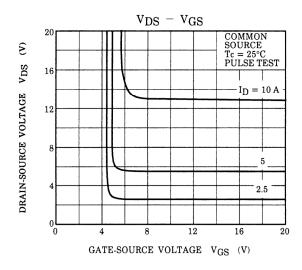
### Marking

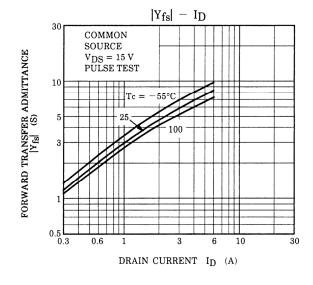


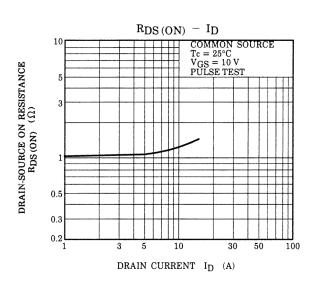


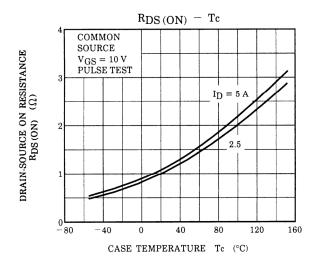


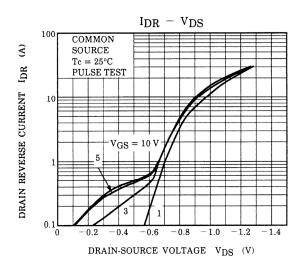


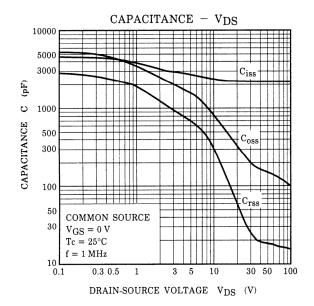


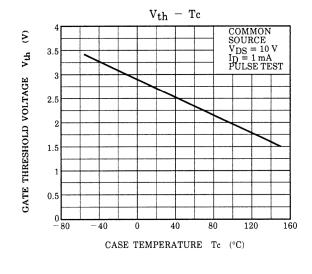


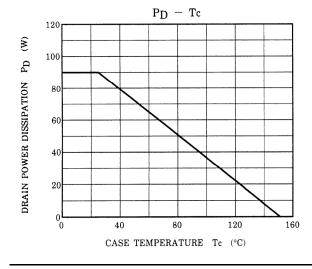


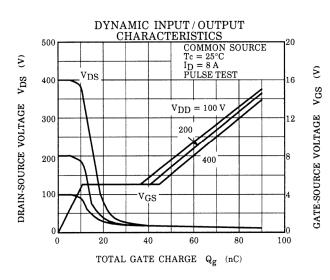


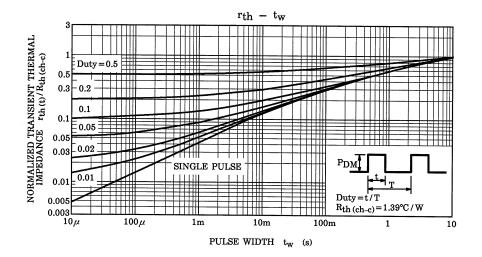


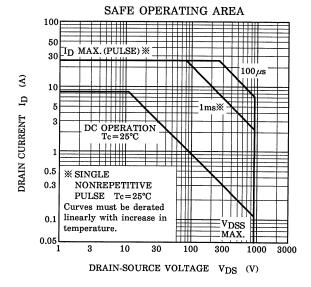


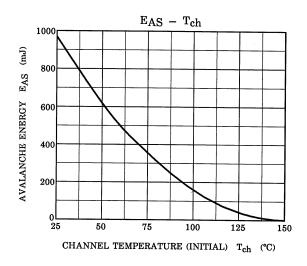


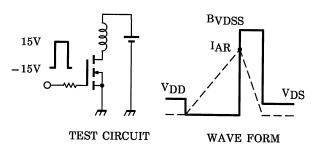












$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 24.5~mH \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

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