TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2995

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain–source ON resistance : RDS (ON) = 48 m Ω (typ.)

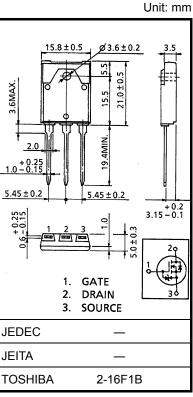
• High forward transfer admittance : $|Y_{fs}| = 30 \text{ S (typ.)}$

• Low leakage current : $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 250 \text{ V)}$

• Enhancement mode : $V_{th} = 1.5 \sim 3.5 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	250	V	
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	250	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	ΙD	30	Α	
	Pulse (Note 1)	I _{DP}	120	Α	
Drain power dissipation	n (Tc = 25°C)	P _D	90	W	
Single pulse avalanche energy (Note 2)		E _{AS}	925	mJ	
Avalanche current		I _{AR}	30	Α	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	9	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.39	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	41.6	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 1.74 mH, I_{AR} = 30 A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device.

Please handle with caution.



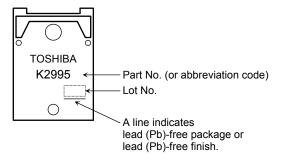
Electrical Characteristics (Ta = 25°C)

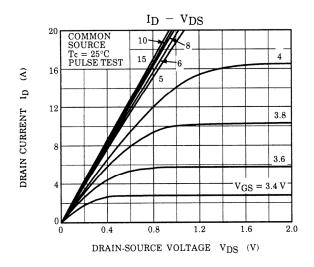
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cur	rent	I _{DSS}	V _{DS} = 250 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	250	_	_	V
Gate threshold v	roltage	V _{th}	V _{DS} = 10 V, I _D = 1m A	1.5	_	3.5	V
Drain-source Ol	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 15 A	_	48	68	mΩ
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 15 A	15	30	_	S
Input capacitano	e	C _{iss}		_	5400	_	
Reverse transfer	capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	580	_	pF
Output capacitance C _{oss}			_	1900	_		
Switching time	Rise time	t _r	V_{GS} V_{OUT} V_{OUT} V_{OUT} V_{OUT} V_{OUT} V_{OUT} V_{OUT} V_{OUT} V_{OUT}	_	20	_	ns
	Turn-on time	t _{on}		_	50	_	
	Fall time	t _f		_	35	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm W} = 10 \mu \rm s$	_	200	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	132	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 200 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$		80	_	nC
Gate-drain ("miller") charge		Q_{gd}			52	_	

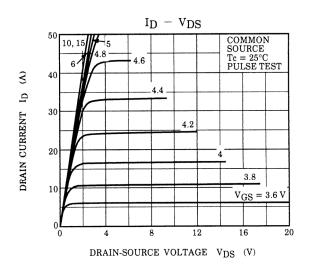
Source-Drain Ratings and Characteristics (Ta = 25°C)

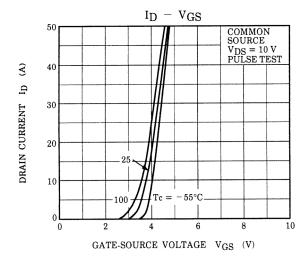
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	30	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	120	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 30 A, V _{GS} = 0 V	_	_	-2.0	V
Reverse recovery time	t _{rr}	I _{DR} = 30 A, V _{GS} = 0 V	1	270	1	ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 100 A / μs	_	3.0	_	μC

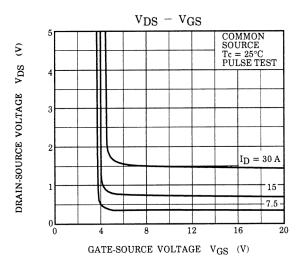
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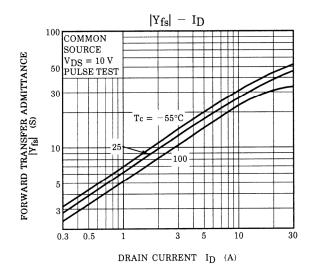


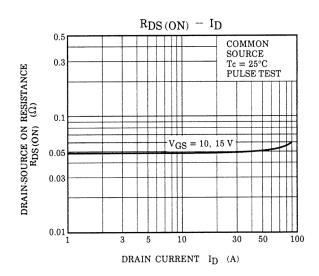




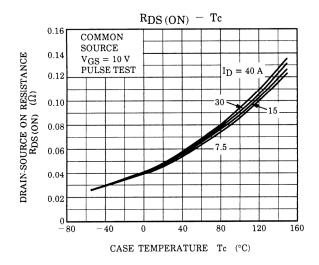


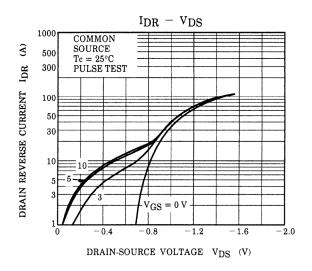


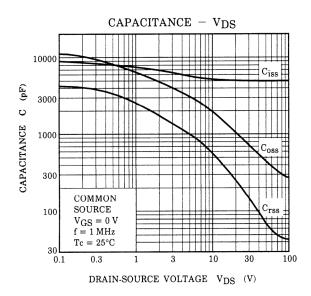


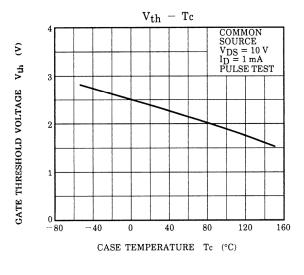


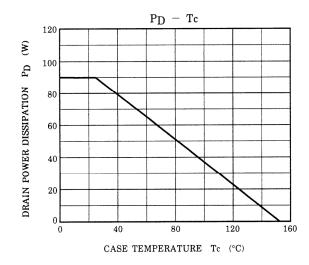
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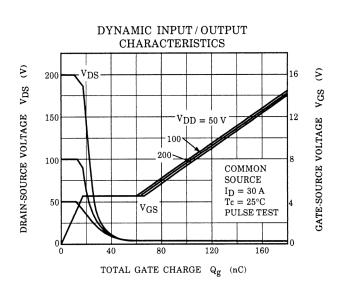


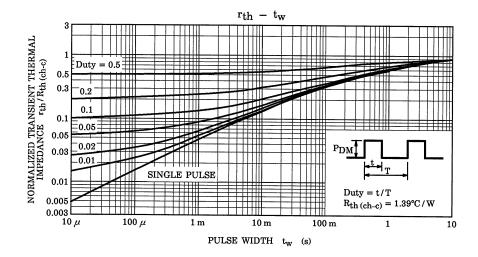


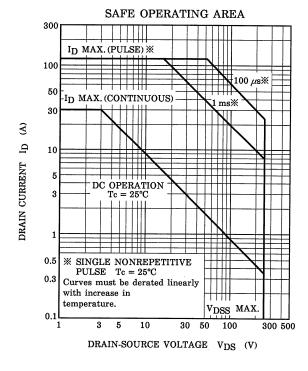


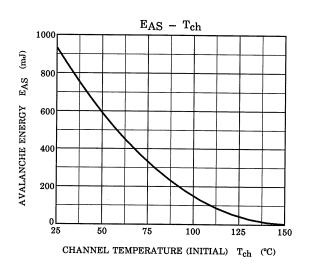


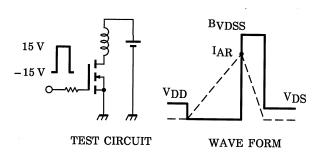












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 50~V,~L = 1.74~mH \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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