Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

2SK2968

DC-DC Converter, Relay Drive and Motor Drive **Applications**

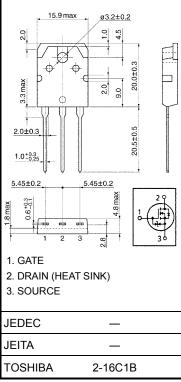
• Low drain-source ON resistance : RDS (ON) = 1.05Ω (typ.) • High forward transfer admittance : $|Y_{fs}| = 7.6 \text{ S (typ.)}$

• Low leakage current : IDSS = 100 μA (max) (VDS = 720 V)

Enhancement mode : $V_{th} = 2.0 \sim 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	900	٧	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	900	٧	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	10	Α	
	Pulse (Note 1)	I _{DP}	30	Α	
Drain power dissipation	n (Tc = 25°C)	P _D	150	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	810	mJ	
Avalanche current		I _{AR}	10	А	
Repetitive avalanche energy (Note 3)		E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 14.9 mH, R_{G} = 25 Ω , I_{AR} = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



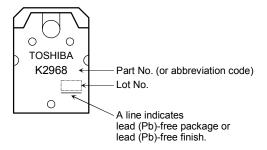
Electrical Characteristics (Ta = 25°C)

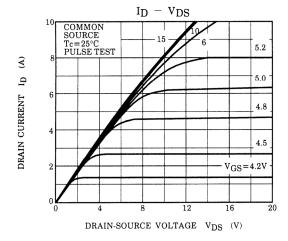
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±30 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bro	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	900	_	_	V
Gate threshold v	/oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 4 A	_	1.05	1.25	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 15 V, I _D = 4 A	3.5	7.6	_	S
Input capacitano	e	C _{iss}		_	2150	_	
Reverse transfe	r capacitance	C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		35	_	pF
Output capacitance		Coss			220	_	
Switching time	Rise time	t _r	$V_{\rm GS}$ $V_{\rm OV}$ $V_{\rm DD}$ $V_{\rm OUT}$ $V_{\rm DD}$	_	25	_	ns
	Turn-on time	t _{on}		_	60	_	
	Fall time	t _f		_	25	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm W} = 10 \mu \rm s$	_	120	_	
Total gate charge (gate-source plus gate-drain)		Qg			70		
Gate-source charge		Qgs	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		37	_	nC
Gate-drain ("miller") Charge		Q_{gd}		_	33	_	

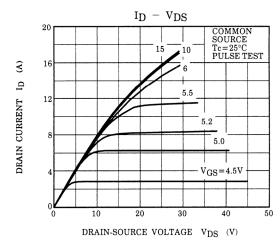
Source-Drain Ratings and Characteristics (Ta = 25°C)

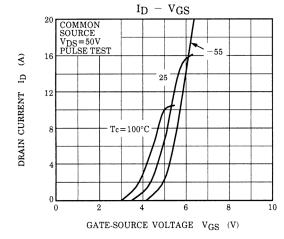
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	-	_	_	10	Α
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	30	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 10 A, V _{GS} = 0 V	_	_	-1.9	V
Reverse recovery time	t _{rr}	I _{DR} = 10 A, V _{GS} = 0 V	_	1300	_	ns
Reverse recovery charge	Qrr	dI _{DR} / dt = 100 A / μs	_	14.5	_	μC

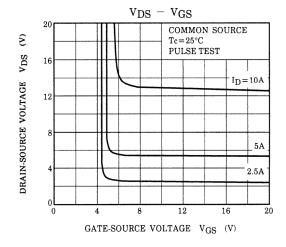
Marking

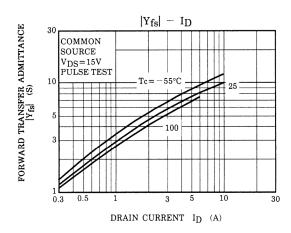


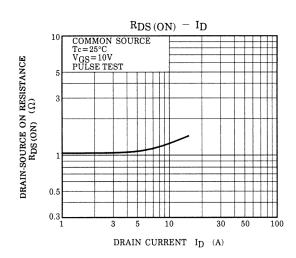


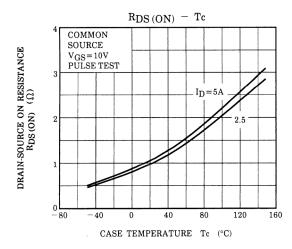


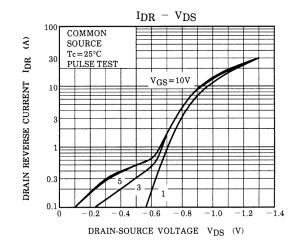


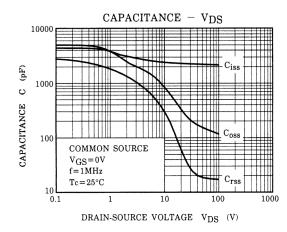


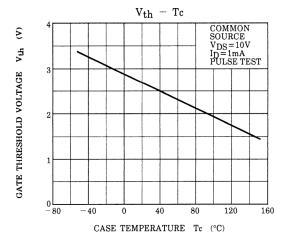


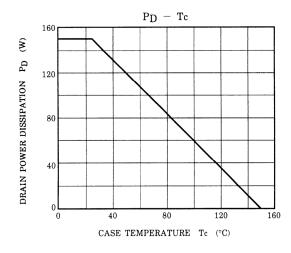


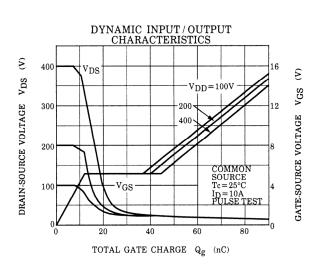




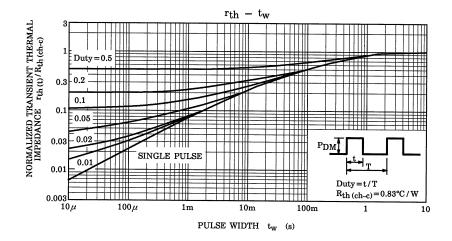


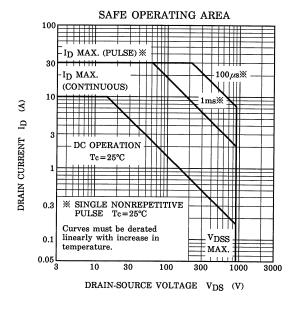


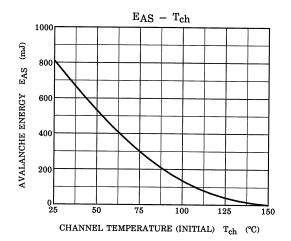


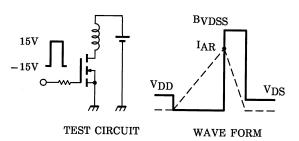


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$$RG = 25 \Omega$$

 $VDD = 90 V, L = 14.9 mH$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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