Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

# 2SK2698

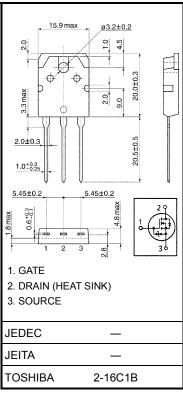
# DC-DC Converter, Relay Drive and Motor Drive Applications

 $\begin{array}{ll} \bullet & \text{Low drain-source ON resistance} & : \text{RDS (ON)} = 0.35 \ \Omega \ \text{(typ.)} \\ \bullet & \text{High forward transfer admittance} & : | Y_{fs}| = 11 \ S \ \text{(typ.)} \\ \end{array}$ 

• Low leakage current :  $IDSS = 100 \mu A (max) (VDS = 500 V)$ • Enhancement mode :  $V_{th} = 2.0 \sim 4.0 V (VDS = 10 V, ID = 1 mA)$ 

### Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	500	V
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	$V_{DGR}$	500	V
Gate-source voltage		V <sub>GSS</sub>	±30	V
Drain current	DC (Note 1)	I <sub>D</sub>	15	Α
	Pulse (Note 1)	I <sub>DP</sub>	60	Α
Drain power dissipation	n (Tc = 25°C)	P <sub>D</sub>	150	W
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	630	mJ
Avalanche current		I <sub>AR</sub>	15	Α
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	15	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	0.833	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 4.76 mH,  $R_{G}$  = 25  $\Omega$ ,  $I_{AR}$  = 15 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



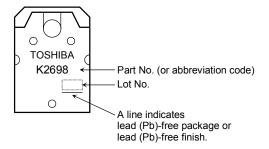
# **Electrical Characteristics (Ta = 25°C)**

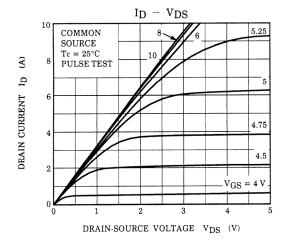
Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V	_	_	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cur	rent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	_	_	V
Gate threshold v	roltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source OI	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.0 A	_	0.35	0.4	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 7.0 A	6	11	_	S
Input capacitano	е	C <sub>iss</sub>		-	2600	-	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	280	-	pF
Output capacitance		C <sub>oss</sub>			880	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 10V$ $V_{GS} = 10V$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$	_	50	_	
	Turn-on time	t <sub>on</sub>		_	85	_	nc
	Fall time	t <sub>f</sub>		_	65	_	- ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{W}} = 10 \mu s$	_	260	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	58		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$		36	_	nC
Gate-drain ("miller") Charge		$Q_{gd}$		_	22	_	

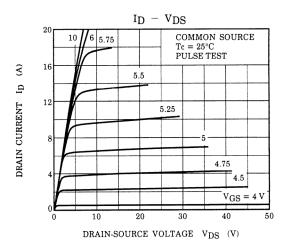
## Source-Drain Ratings and Characteristics (Ta = 25°C)

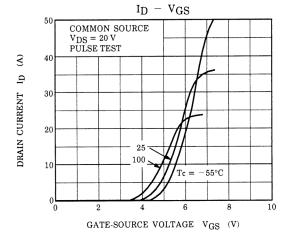
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	15	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>		_	_	60	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V dI <sub>DR</sub> / dt = 100 A / μs	1	400		ns
Reverse recovery charge	Qrr	dI <sub>DR</sub> / dt = 100 A / μs	- 1	4.3	_	μC

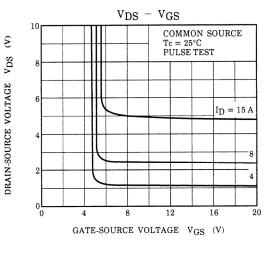
### Marking

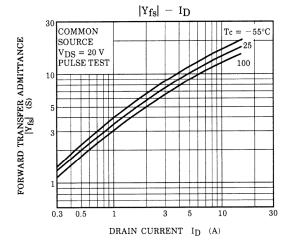


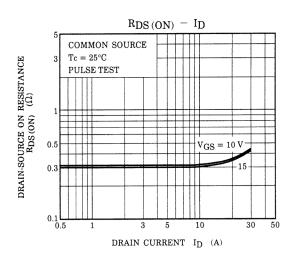


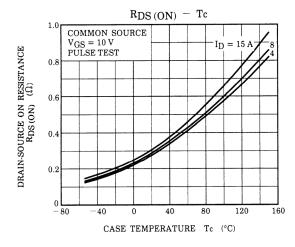


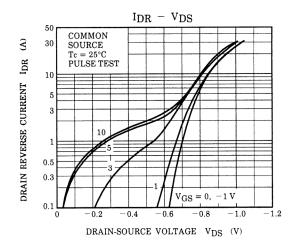


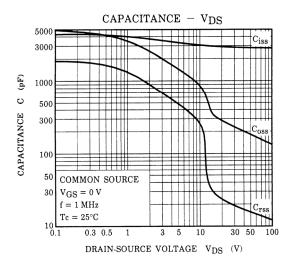


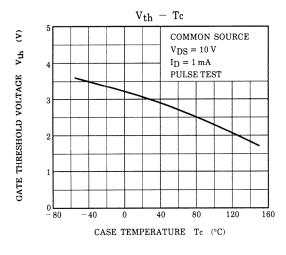


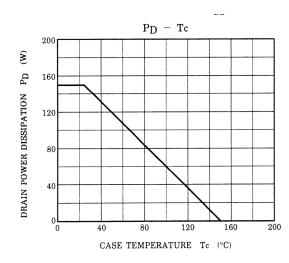


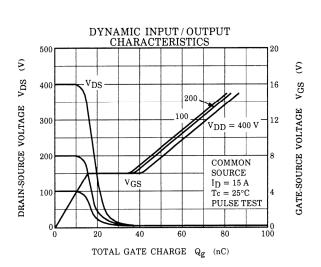




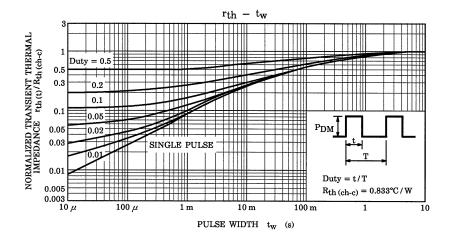


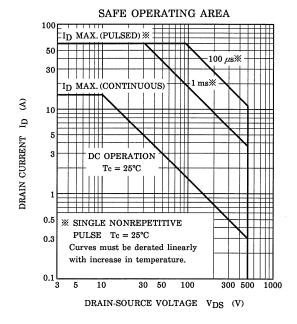


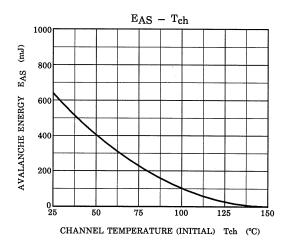


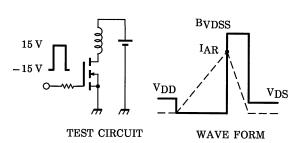


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$$R_G$$
 = 25  $\Omega$   
 $V_{DD}$  = 90 V, L = 4.76 mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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