TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSIII)

# 2SK2607

# Chopper Regulator, DC-DC Converter and Moter Drive Applications

• Low drain-source ON resistance : RDS (ON) = 1.0  $\Omega$  (typ.)

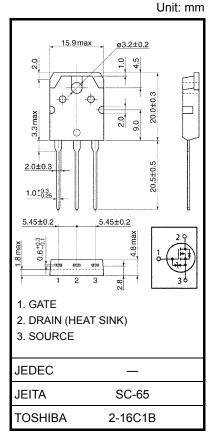
• High forward transfer admittance :  $|Y_{fs}| = 7.0 \text{ S (typ.)}$ 

• Low leakage current  $: I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 640 \text{ V)}$ 

• Enhancement mode :  $V_{th} = 2.0 \sim 4.0 \text{ V (Vps} = 10 \text{ V, Ip} = 1 \text{ mA})$ 

### Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	800	V	
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	$V_{DGR}$	800	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	ΙD	9	Α	
	Pulse (Note 1)	I <sub>DP</sub>	27		
Drain power dissipation	n (Tc = 25°C)	$P_{D}$	150	W	
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	778	mJ	
Avalanche current		I <sub>AR</sub>	9	Α	
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	15	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	0.883	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 17.4 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 9 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



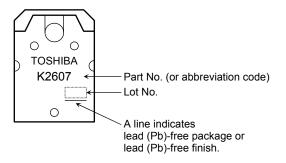
## **Electrical Characteristics (Ta = 25°C)**

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cui	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 640 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V <sub>(BR) DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	800	_	_	V
Gate threshold v	oltage/	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source Ol	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4 A,	_	1.0	1.2	Ω
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 4 A	3.0	7.0	_	S
Input capacitano	e	C <sub>iss</sub>		_	2160	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	45	_	pF
Output capacitance		Coss		_	200	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} \stackrel{10 \text{ V}}{\text{O} \text{ V}} \stackrel{I_{D} = 4 \text{ A}}{\text{N}} V_{out}$ $V_{DD} \stackrel{:}{=} 400 \text{ V}$	_	25	_	
	Turn-on time	t <sub>on</sub>		-	60	ı	ns
	Fall time	t <sub>f</sub>		_	25	_	113
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, $t_{\rm w} = 10  \mu \rm s$	ı	110	I	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 9 \text{ A}$	_	68	_	nC
Gate-source charge		$Q_{gs}$		_	38	_	
Gate-drain ("miller") Charge		$Q_{gd}$		_	30	_	

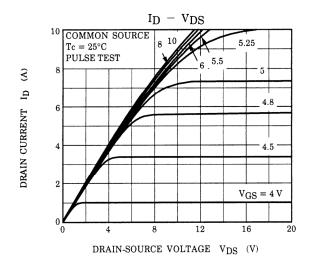
## Source-Drain Ratings and Characteristics (Ta = 25°C)

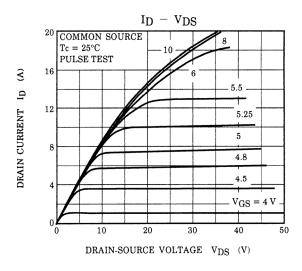
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	9	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	-	_	_	27	Α
Forward voltage (diode)	$V_{DSF}$	I <sub>DR</sub> = 9 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	IDR = 9 A, V <sub>GS</sub> = 0 V, dI <sub>DR</sub> / dt = 100 A / µs	ı	1000	1	ns
Reverse recovery charge	Qrr	IDR = 9 A, VGS = 0 V, diDR / dt = 100 A / μs		12	_	μC

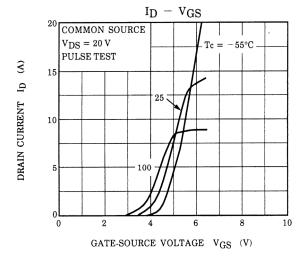
### Marking

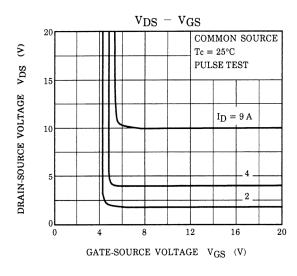


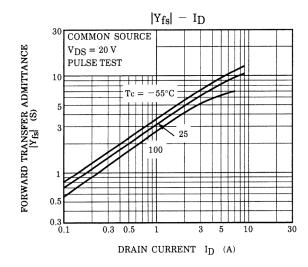
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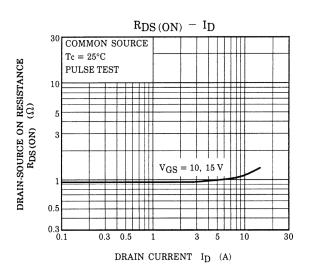




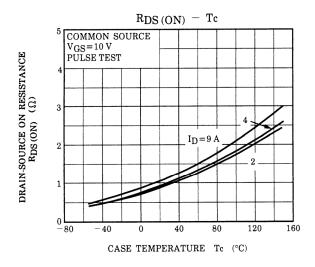


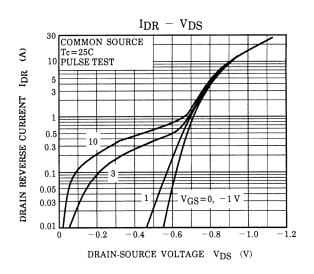


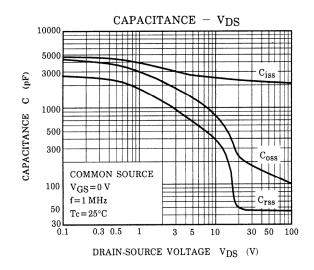


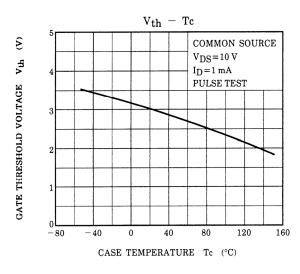


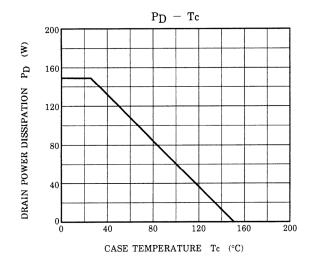
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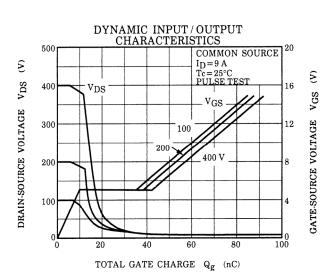


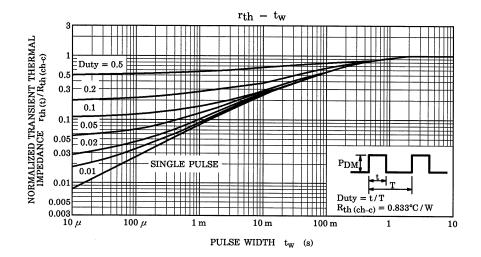


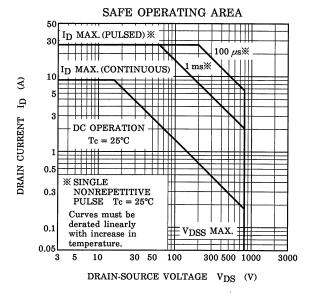


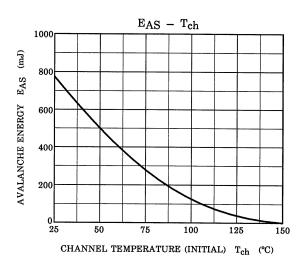


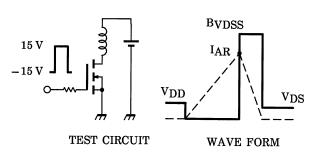












$$\begin{aligned} R_G &= 25 \ \Omega \\ V_{DD} &= 90 \ \text{V}, \ L = 17.4 \ \text{mH} \end{aligned} \quad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B \text{VDSS}}{B \text{VDSS} - \text{VDD}} \right)$$

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