## Product Preview

# **Small Signal MOSFET**

20 V, 350 mA, Single P-Channel, Gate Zener, SC-75 & SC-89

#### **Features**

- Low R<sub>DS(on)</sub> for Higher Efficiency and Longer Battery Life
- Small Outline Package (1.6 x 1.6 mm)
- Low Threshold
- ESD Protected Gate
- Pb-Free Package for Green Manufacturing (G Suffix)

#### **Applications**

- Battery Operated Systems
- Load Switch for Cell Phones, PDAs, Digital Cameras, etc.
- Power Supply Converter Circuits

#### **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Value	Units	
Drain-to-Source Voltage	Drain-to-Source Voltage		-20	V
Gate-to-Source Voltage		V <sub>GS</sub>	±6.0	V
Continuous Drain Current (Note 1) Steady State		Ι <sub>D</sub>	-350	mA
Power Dissipation (Note 1)	Steady State	P <sub>D</sub>	150	mW
Pulsed Drain Current tp =10 μs		I <sub>DM</sub>	±1000	mA
Operating Junction and Storage	T <sub>J</sub> , T <sub>STG</sub>	–55 to 150	°C	
Continuous Source Current (Body Diode)		I <sub>S</sub>	-250	mA
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)		T <sub>L</sub>	260	°C

#### THERMAL RESISTANCE RATINGS

Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	TBD	°C/W
Junction–to–Ambient – t ≤ 10 s (Note 1)	$R_{\theta JA}$	TBD	

1. Surface mounted on FR4 board using 1 in sq. pad size (Cu area = 1.127 in sq. [1 oz] including traces).

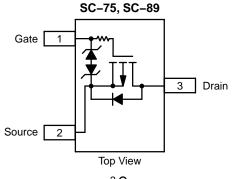
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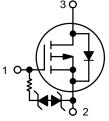


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V <sub>(BR)DSS</sub>	(BR)DSS R <sub>DS(on)</sub> TYP	
–20 V	0.55 Ω @ -4.5 V	
	0.75 Ω @ -2.5 V	-350 mA
	1.2 Ω @ –1.8 V	





P-Channel MOSFET

#### 3 2 2 SC-75 / SOT-416 CASE 463 STYLE 5

SC-89 CASE 463C

# MARKING DIAGRAM 3 XX D

xx = Specific Device Code
D = Date Code



#### **ORDERING INFORMATION**

Device	Package	Shipping
NTx4151PT1	TBD	TBD
NTx4151PT1G	TBD (Pb-Free)	TBD

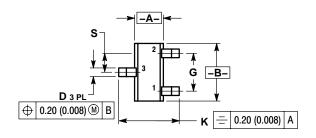
## **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise stated)

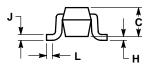
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-20			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = -16 V			-100	nA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			±2.0	μА
ON CHARACTERISTICS (Note 2)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{DS} = 3.0 \text{ V}, I_D = 250 \mu\text{A}$	-0.45		1.0	V
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -350 \text{ mA}$		0.55	0.8	Ω
		$V_{GS} = -2.5 \text{ V}, I_D = -300 \text{ mA}$		0.75	1.2	
		$V_{GS} = -1.8 \text{ V}, I_D = -150 \text{ mA}$		1.2	1.8	
Forward Transconductance	9 <sub>FS</sub>	$V_{DS} = 10 \text{ V}, I_{D} = -250 \text{ mA}$		0.4		S
CHARGES AND CAPACITANCES						
Input Capacitance	C <sub>ISS</sub>	$V_{GS} = 0 \text{ V, } f = 1.0 \text{ MHz,}$		TBD		pF
Output Capacitance	C <sub>OSS</sub>	$V_{DS} = 5.0 \text{ V}$		TBD		1
Reverse Transfer Capacitance	C <sub>RSS</sub>			TBD		1
SWITCHING CHARACTERISTICS (No	te 3)					
Turn-On Delay Time	td <sub>(ON)</sub>	V <sub>GS</sub> = -4.5 V, V <sub>DD</sub> = -10 V,		TBD		ns
Rise Time	tr	$I_D = 10 \text{ mA}, R_G = 10 \Omega$		TBD		
Turn-Off Delay Time	td <sub>(OFF)</sub>			TBD		1
Fall Time	tf			TBD		
DRAIN-SOURCE DIODE CHARACTE	RISTICS					
Forward Diode Voltage	V <sub>SD</sub>	$V_{GS} = 0 \text{ V, } I_S = -150 \text{ mA}$		TBD	-1.2	V

Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

#### **PACKAGE DIMENSIONS**

SC-75 / SOT-416 CASE 463-01 ISSUE C



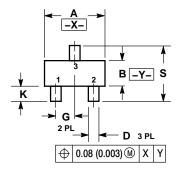


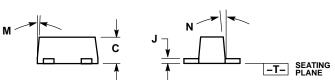
- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	0.70	0.90	0.028	0.035	
В	1.40	1.80	0.055	0.071	
C	0.60	0.90	0.024	0.035	
D	0.15	0.30	0.006	0.012	
G	1.00 BSC		0.039 BSC		
Η		0.10		0.004	
J	0.10	0.25	0.004	0.010	
K	1.45	1.75	0.057	0.069	
L	0.10	0.20	0.004	0.008	
S	0.50 BSC		0.020 BSC		

STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN

SC-89 CASE 463C-03 ISSUE C





- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETERS
  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
  4. 463C-01 OBSOLETE, NEW STANDARD 463C-02.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	1.50	1.60	1.70	0.059	0.063	0.067	
В	0.75	0.85	0.95	0.030	0.034	0.040	
С	0.60	0.70	0.80	0.024	0.028	0.031	
D	0.23	0.28	0.33	0.009	0.011	0.013	
G	0.50 BSC			0.020 BSC			
Н	0.53 REF			0.021 REF			
J	0.10	0.15	0.20	0.004	0.006	0.008	
K	0.30	0.40	0.50	0.012	0.016	0.020	
L	1	1.10 REF			0.043 REF		
M			10			10	
N			10			10	
S	1.50	1.60	1.70	0.059	0.063	0.067	

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