Preferred Device

High Voltage Transistor

NPN Silicon

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CEO}	400	Vdc
Collector - Base Voltage	V _{CBO}	500	Vdc
Emitter-Base Voltage	V _{EBO}	6.0	Vdc
Collector Current – Continuous	I _C	300	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

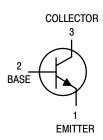
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W	
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W	



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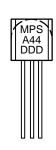
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MARKING DIAGRAM



TO-92 (TO-226AA) CASE 29-11 Style 1



MPSA44= Specific Device Cod DDD = Date Code

ORDERING INFORMATION

Device	Package	Shipping	
MPSA44	TO-92	5000 Units / Bag	
MPSA44RLRA	TO-92	2000 / Tape & Reel	
MPSA44RL1	TO-92	2000 / Tape & Reel	

Preferred devices are recommended choices for future use and best overall value.

$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_A = 25^{\circ}C \ unless \ otherwise \ noted)$

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	·			
Collector – Emitter Breakdown Voltage (Note 1) $(I_C = 1.0 \text{ mAdc}, I_B = 0)$	V _(BR) CEO	400	-	Vdc
Collector – Emitter Breakdown Voltage ($I_C = 100 \mu Adc$, $V_{BE} = 0$)	V _(BR) CES	500	-	Vdc
Collector – Base Breakdown Voltage ($I_C = 100 \mu Adc, I_E = 0$)	V _(BR) CBO	500	-	Vdc
Emitter – Base Breakdown Voltage ($I_E = 10 \mu Adc$, $I_C = 0$)	V _{(BR)EBO}	6.0	-	Vdc
Collector Cutoff Current (V _{CB} = 400 Vdc, I _E = 0)	Ісво	1	0.1	μAdc
Collector Cutoff Current (V _{CE} = 400 Vdc, V _{BE} = 0)	I _{CES}	-	500	nAdc
Emitter Cutoff Current (V _{EB} = 4.0 Vdc, I _C = 0)	I _{EBO}	1	0.1	μAdc
ON CHARACTERISTICS (Note 1)				
DC Current Gain (Note 1) $ \begin{aligned} &(I_C=1.0 \text{ mAdc, } V_{CE}=10 \text{ Vdc}) \\ &(I_C=10 \text{ mAdc, } V_{CE}=10 \text{ Vdc}) \\ &(I_C=50 \text{ mAdc, } V_{CE}=10 \text{ Vdc}) \\ &(I_C=100 \text{ mAdc, } V_{CE}=10 \text{ Vdc}) \end{aligned} $	h _{FE}	40 50 45 40	_ 200 _ _	_
Collector – Emitter Saturation Voltage (Note 1) ($I_C = 1.0 \text{ mAdc}$, $I_B = 0.1 \text{ mAdc}$) ($I_C = 10 \text{ mAdc}$, $I_B = 1.0 \text{ mAdc}$) ($I_C = 50 \text{ mAdc}$, $I_B = 5.0 \text{ mAdc}$)	V _{CE(sat)}	- - -	0.4 0.5 0.75	Vdc
Base – Emitter Saturation Voltage $(I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc})$	V _{BE(sat)}	1	0.75	Vdc
SMALL-SIGNAL CHARACTERISTICS	·			•
Output Capacitance (V _{CB} = 20 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	-	7.0	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)	C _{ibo}	-	130	pF
Small–Signal Current Gain (I _C = 10 mAdc, V _{CE} = 10 Vdc, f = 20 MHz)	h _{fe}	1.0	-	-

^{1.} Pulse Test: Pulse Width $\leq 300~\mu s$, Duty Cycle $\leq 2.0\%$.

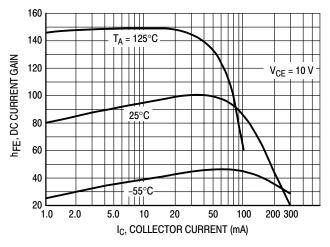


Figure 1. DC Current Gain

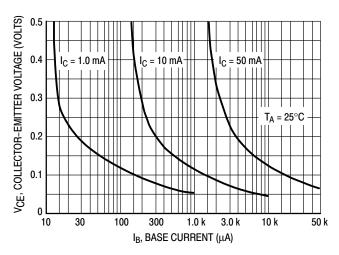


Figure 2. Collector Saturation Region

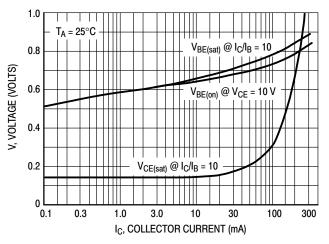


Figure 3. "On" Voltages

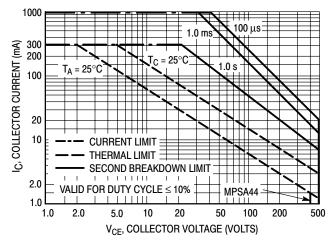


Figure 4. Active Region - Safe Operating Area

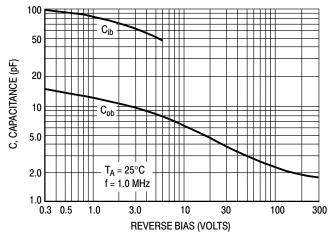


Figure 5. Capacitance

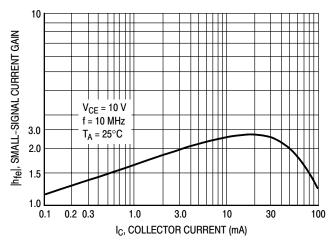


Figure 6. High Frequency Current Gain

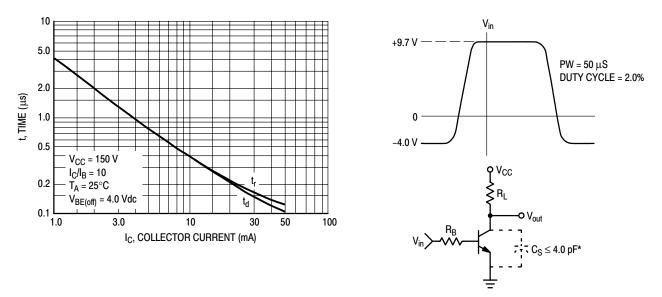


Figure 7. Turn-On Switching Times and Test Circuit

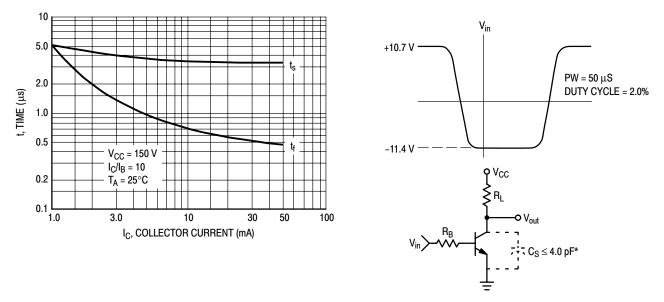
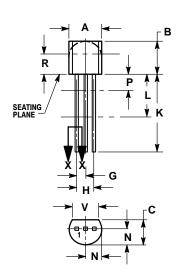


Figure 8. Turn-Off Switching Times and Test Circuit

*Total Shunt Capacitance or Test Jig and Connectors.

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL**





- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

- STYLE 1: PIN 1. EMITTER

 - 2. BASE 3. COLLECTOR

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