MMSZ5221ET1 Series

Zener Voltage Regulators

500 mW SOD−123 Surface Mount

Three complete series of Zener diodes are offered in the convenient, surface mount plastic SOD−123 package. These devices provide a convenient alternative to the leadless 34−package style.

Specification Features:

- 500 mW Rating on FR−4 or FR−5 Board
- Wide Zener Reverse Voltage Range − 2.4 V to 110 V
- Package Designed for Optimal Automated Board Assembly
- Small Package Size for High Density Applications
- General Purpose, Medium Current
- ESD Rating of Class 3 (>16 KV) per Human Body Model
- Peak Power − 225 Watt (8 X 20 μs)

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic case
FINISH: Corrosion resistant finish, easily solderable
MAXIMUM CASE TEMPERATURE FOR SOLDERING PURPOSES: 260°C for 10 Seconds
POLARITY: Cathode indicated by polarity band
FLAMMABILITY RATING: UL94 V−0

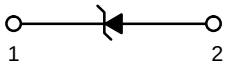
MAXIMUM RATINGS

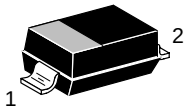
Rating	Symbol	Max	Unit
Peak Power Dissipation @ 20 μs (Note 1) @ $T_L \le 25°C$	P_{pk}	225	Watts
Total Power Dissipation on FR−5 Board, (Note 2) @ $T_L = 75°C$ Derated above 75°C	P_D	500 6.7	mW mW/°C
Thermal Resistance − Junction to Ambient (Note 3)	$R_{\theta JA}$	340	°C/W
Thermal Resistance − Junction to Lead (Note 3)	$R_{\theta JL}$	150	°C/W
Junction and Storage Temperature Range	T_J, T_{stg}	−55 to +150	°C

1. Non−repetitive current pulse per Figure 11
2. FR−5 = 3.5 X 1.5 inches, using the On minimum recommended footprint
3. Thermal Resistance measurement obtained via infrared Scan Method

ON Semiconductor®

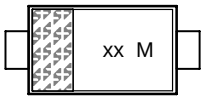
http://onsemi.com





SOD−123
CASE 425
STYLE 1

MARKING DIAGRAM



xx = Specific Device Code
M = Date Code

ORDERING INFORMATION

Device †	Package	Shipping
MMSZ52xxET1	SOD−123	3000/Tape & Reel
MMSZ52xxET3	SOD−123	10,000/Tape & Reel

DEVICE MARKING INFORMATION

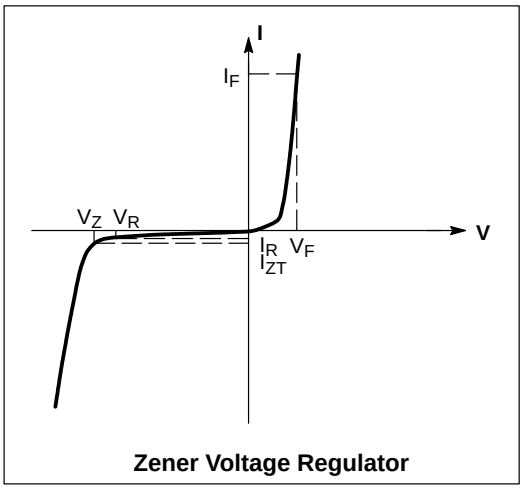
See specific marking information in the device marking column of the Electrical Characteristics table on page 2 of this data sheet.

Devices listed in ***bold, italic*** are ON Semiconductor **Preferred** devices. **Preferred** devices are recommended choices for future use and best overall value.

†The “T1” suffix refers to an 8 mm, 7 inch reel.
The “T3” suffix refers to an 8 mm, 13 inch reel.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted, V_F = 0.95 V Max. @ I_F = 10 mA)

Symbol	Parameter
V_Z	Reverse Zener Voltage @ I_{ZT}
I_{ZT}	Reverse Current
Z_{ZT}	Maximum Zener Impedance @ I_{ZT}
I_{ZK}	Reverse Current
Z_{ZK}	Maximum Zener Impedance @ I_{ZK}
I_R	Reverse Leakage Current @ V_R
V_R	Reverse Voltage
I_F	Forward Current
V_F	Forward Voltage @ I_F



Zener Voltage Regulator

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted, V_F = 0.9 V Max. @ I_F = 10 mA)

Device	Device Marking	Zener Voltage (Notes 4 and 5) V_Z (Volts) Min	Nom	Max	@ I_{ZT} mA	Zener Impedance (Note 6) Z_{ZT} @ I_{ZT} Ω	Z_{ZK} @ I_{ZK} Ω	mA	Leakage Current I_R @ V_R µA	Volts
MMSZ5221ET1	***C1***	***2.28***	***2.4***	***2.52***	***20***	***30***	***1200***	***0.25***	***100***	***1***
MMSZ5226ET1	D1	3.14	3.3	3.47	20	28	1600	0.25	25	1
MMSZ5228ET1	D3	3.71	3.9	4.10	20	23	1900	0.25	10	1
MMSZ5229ET1	***D4***	***4.09***	***4.3***	***4.52***	***20***	***22***	***2000***	***0.25***	***5***	***1***
MMSZ5231ET1	***E1***	***4.85***	***5.1***	***5.36***	***20***	***17***	***1600***	***0.25***	***5***	***2***
MMSZ5232ET1	***E2***	***5.32***	***5.6***	***5.88***	***20***	***11***	***1600***	***0.25***	***5***	***3***
MMSZ5234ET1	***E4***	***5.89***	***6.2***	***6.51***	***20***	***7***	***1000***	***0.25***	***5***	***4***
MMSZ5235ET1	E5	6.46	6.8	7.14	20	5	750	0.25	3	5
MMSZ5236ET1	F1	7.13	7.5	7.88	20	6	500	0.25	3	6
MMSZ5237ET1	F2	7.79	8.2	8.61	20	8	500	0.25	3	6.5
MMSZ5240ET1	***F5***	***9.50***	***10***	***10.50***	***20***	***17***	***600***	***0.25***	***3***	***8***
MMSZ5242ET1	***H2***	***11.40***	***12***	***12.60***	***20***	***30***	***600***	***0.25***	***1***	***9.1***
MMSZ5243ET1	H3	12.35	13	13.65	9.5	13	600	0.25	0.5	9.9
MMSZ5244ET1	H4	13.30	14	14.70	9.0	15	600	0.25	0.1	10
MMSZ5245ET1	***H5***	***14.25***	***15***	***15.75***	***8.5***	***16***	***600***	***0.25***	***0.1***	***11***
MMSZ5246ET1	***J1***	***15.20***	***16***	***16.80***	***7.8***	***17***	***600***	***0.25***	***0.1***	***12***
MMSZ5248ET1	***J3***	***17.10***	***18***	***18.90***	***7.0***	***21***	***600***	***0.25***	***0.1***	***14***
MMSZ5250ET1	J5	19.00	20	21.00	6.2	25	600	0.25	0.1	15
MMSZ5252ET1	***K2***	***22.80***	***24***	***25.20***	***5.2***	***33***	***600***	***0.25***	***0.1***	***18***
MMSZ5255ET1	K5	26.60	28	29.40	4.5	44	600	0.25	0.1	21
MMSZ5257ET1	M2	31.35	33	34.65	3.8	58	700	0.25	0.1	25
MMSZ5263ET1	N3	53.20	56	58.80	2.2	150	1300	0.25	0.1	43

4. The type numbers shown have a standard tolerance of ±5% on the nominal Zener voltage.
5. Nominal Zener voltage is measured with the device junction in thermal equilibrium at T_L = 30°C ±1°C
6. Z_{ZT} and Z_{ZK} are measured by dividing the AC voltage drop across the device by the ac current applied. The specified limits are for $I_{Z(AC)}$ = 0.1 $I_{Z(dc)}$ with the AC frequency = 1 KHz.

TYPICAL CHARACTERISTICS

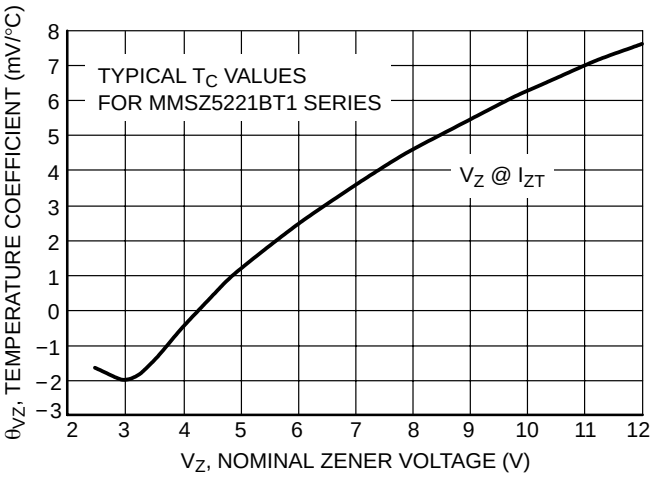


Figure 1. Temperature Coefficients (Temperature Range −55°C to +150°C)

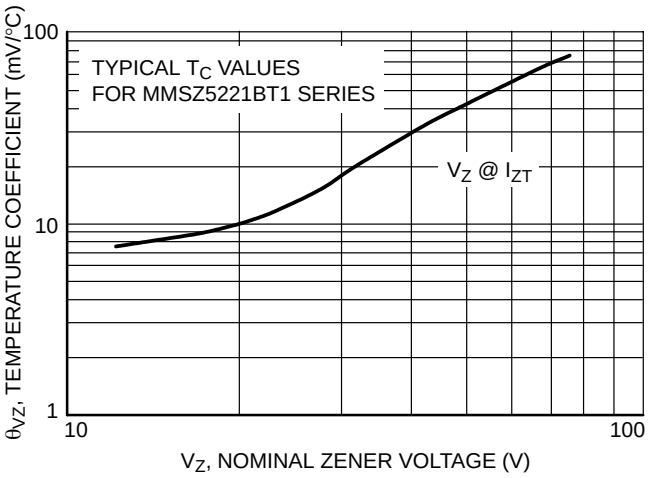


Figure 2. Temperature Coefficients (Temperature Range −55°C to +150°C)

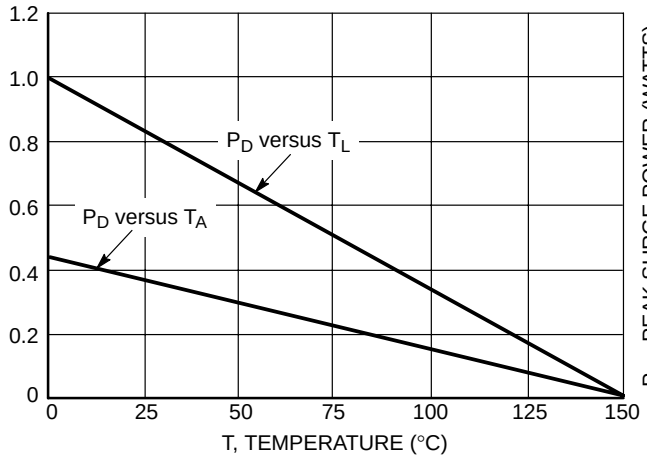


Figure 3. Steady State Power Derating

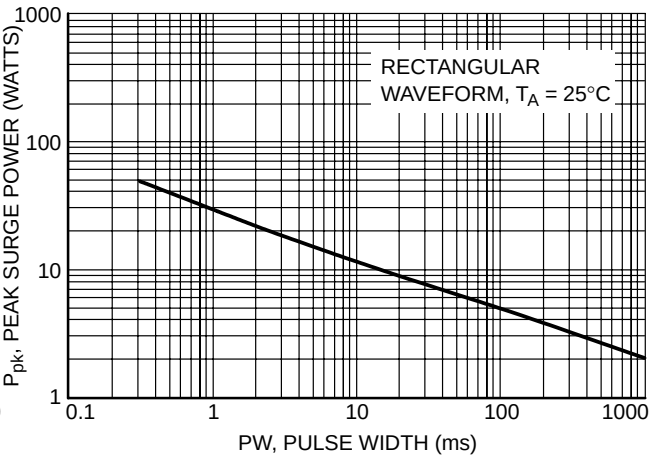


Figure 4. Maximum Nonrepetitive Surge Power

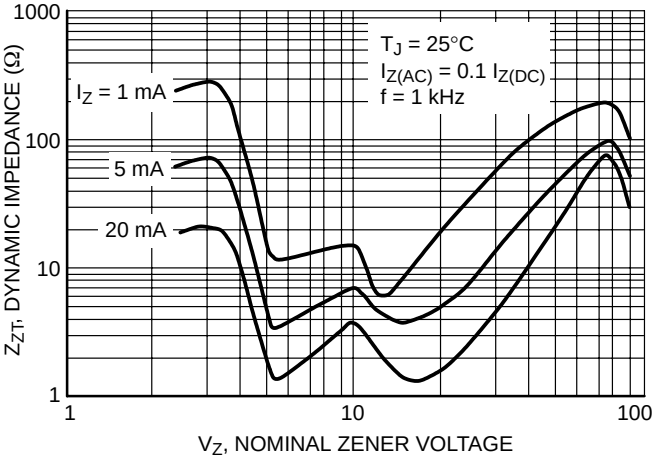


Figure 5. Effect of Zener Voltage on Zener Impedance

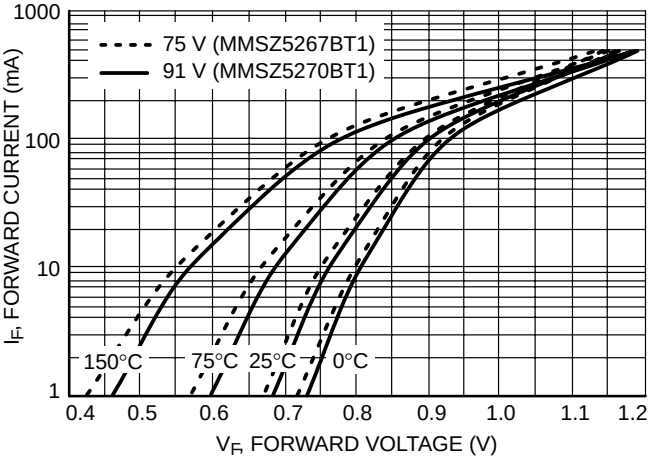


Figure 6. Typical Forward Voltage

TYPICAL CHARACTERISTICS

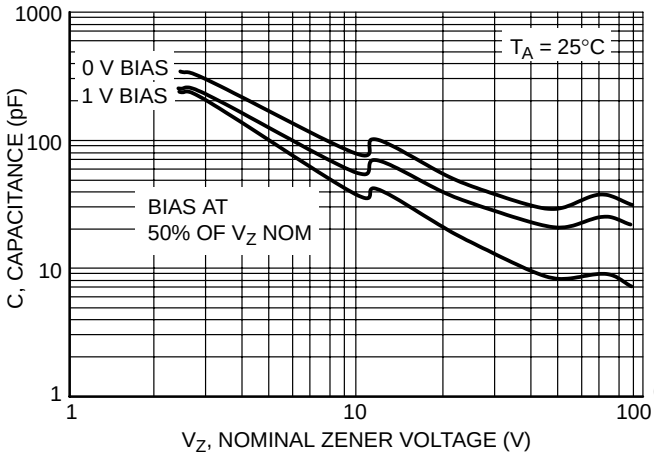


Figure 7. Typical Capacitance

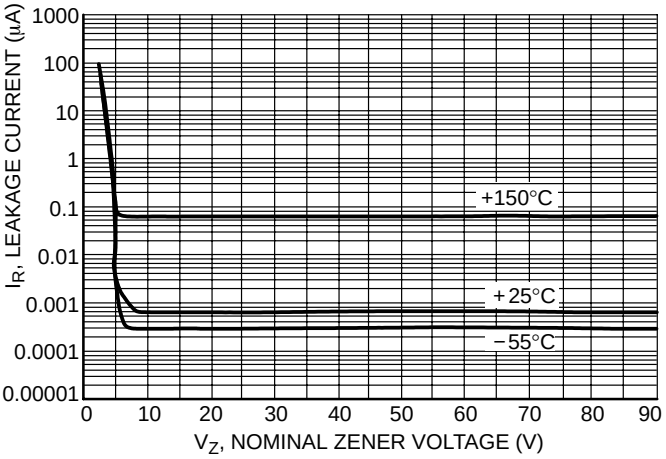


Figure 8. Typical Leakage Current

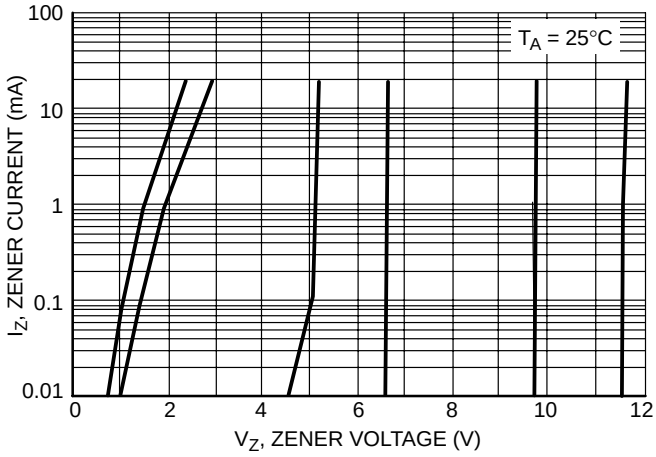


Figure 9. Zener Voltage versus Zener Current (V_Z Up to 12 V)

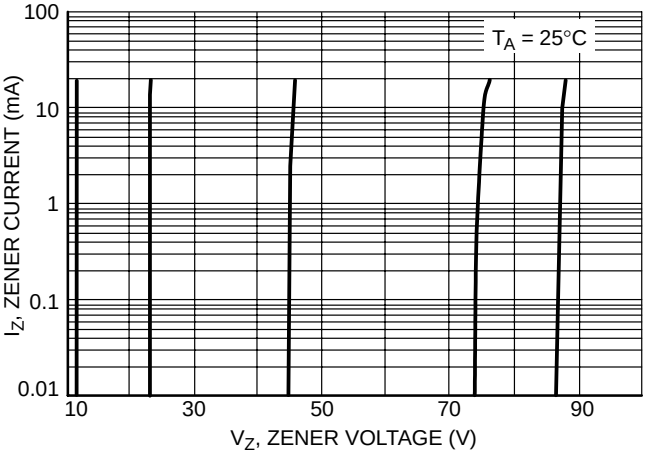


Figure 10. Zener Voltage versus Zener Current (12 V to 91 V)

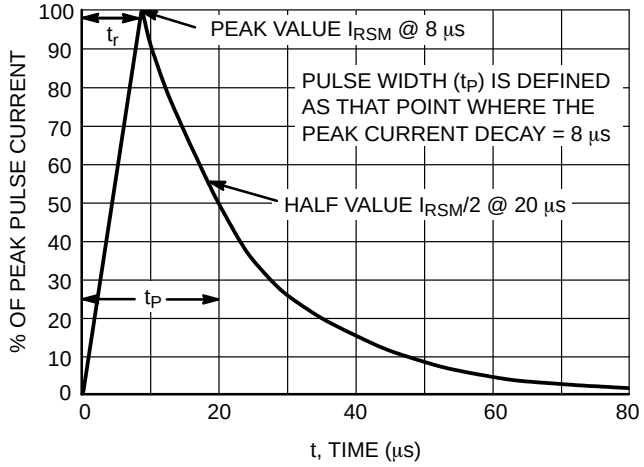


Figure 11. 8 × 20 μs Pulse Waveform

INFORMATION FOR USING THE SOD-123 SURFACE MOUNT PACKAGE

MINIMUM RECOMMENDED FOOTPRINTS FOR SURFACE MOUNT APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to ensure proper solder connection interface between the board and the package.

The minimum recommended footprint for the SOD-123 is shown at the right.

The SOD-123 package can be used on existing surface mount boards which have been designed for the leadless 34 package style. The footprint compatibility makes conversion from leadless 34 to SOD-123 straightforward.

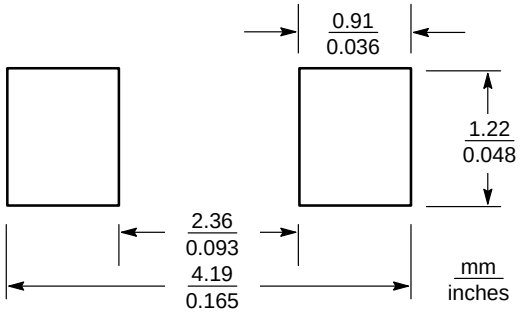


Figure 12. Minimum Recommended Footprint

SOD-123 POWER DISSIPATION

The power dissipation of the SOD-123 is a function of the pad size. This can vary from the minimum pad size for soldering to a pad size given for maximum power dissipation. Power dissipation for a surface mount device is determined by $T_{J(max)}$, the maximum rated junction temperature of the die, $R_{\theta JA}$, the thermal resistance from the device junction to ambient; and the operating temperature, T_A. Using the values provided on the data sheet for the SOD-123 package, P_D can be calculated as follows:

$$P_D = \frac{T_{J(max)} - T_A}{R_{\theta JA}}$$

The values for the equation are found in the maximum ratings table on the data sheet. Substituting these values into the equation for an ambient temperature T_A of 25°C, one can calculate the power dissipation of the device which in this case is 0.37 watts.

$$P_D = \frac{150°C - 25°C}{340°C/W} = 0.37 \text{ watts}$$

The 340°C/W for the SOD-123 package assumes using recommended footprint shown on FR-4 glass epoxy printed circuit board. Another alternative is to use a ceramic substrate or an aluminum core board such as Thermal Clad®. By using an aluminum core board material such as Thermal Clad, the power dissipation can be doubled using the same footprint.

GENERAL SOLDERING PRECAUTIONS

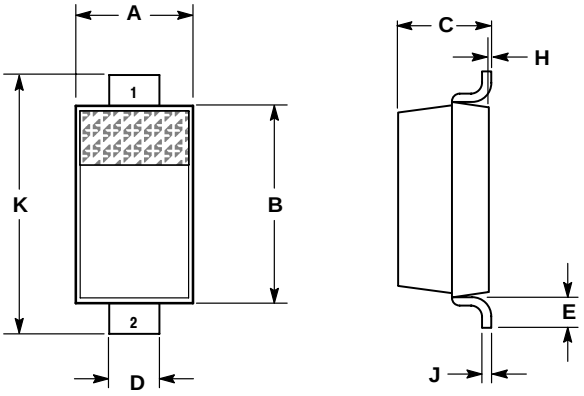
The melting temperature of solder is higher than the rated temperature of the device. When the entire device is heated to a high temperature, failure to complete soldering within a short time could result in device failure. Therefore, the following items should always be observed in order to minimize the thermal stress to which the devices are subjected.

- Always preheat the device.
- The delta temperature between the preheat and soldering should be 100°C or less.*
- When preheating and soldering, the temperature of the leads and the case must not exceed the maximum temperature ratings as shown on the data sheet. When using infrared heating with the reflow soldering method, the difference shall be a maximum of 10°C.
- The soldering temperature and time shall not exceed 260°C for more than 10 seconds.
- When shifting from preheating to soldering, the maximum temperature gradient shall be 5°C or less.
- After soldering has been completed, the device should be allowed to cool naturally for at least three minutes. Gradual cooling should be used as the use of forced cooling will increase the temperature gradient and result in latent failure due to mechanical stress.
- Mechanical stress or shock should not be applied during cooling

* Soldering a device without preheating can cause excessive thermal shock and stress which can result in damage to the device.

PACKAGE DIMENSIONS

SOD−123
CASE 425−04
ISSUE C



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	0.055	0.071	1.40	1.80
B	0.100	0.112	2.55	2.85
C	0.037	0.053	0.95	1.35
D	0.020	0.028	0.50	0.70
E	0.01	−−−	0.25	−−−
H	0.000	0.004	0.00	0.10
J	−−−	0.006	−−−	0.15
K	0.140	0.152	3.55	3.85

STYLE 1:
PIN 1. CATHODE
2. ANODE

Thermal Clad is a registered trademark of the Bergquist Company

PUBLICATION ORDERING INFORMATION

Literature Fulfillment:
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