Registered Hex TTL to ECL Translator

The MC10H/100H604 is a 6-bit, registered, dual supply TTL to ECL translator. The device features differential ECL outputs as well as a choice between either a differential ECL clock input or a TTL clock input. The asynchronous master reset control is an ECL level input..

With its differential ECL outputs and TTL inputs the H604 device is ideally suited for the transmit function of a HPPI bus type board-to-board interface application. The on chip registers simplify the task of synchronizing the data between the two boards.

The device is available in either ECL standard: the 10H device is compatible with MECL 10 kH logic levels while the 100H device is compatible with 100 k logic levels.

- Differential 50 Ω ECL Outputs
- Choice Between Differential ECL or TTL Clock Input
- Dual Power Supply
- Multiple Power and Ground Pins to Minimize Noise
- Specified Within-Device Skew

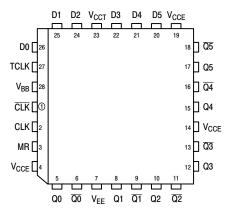


Figure 1. PLCC-28 Pinout (Top View)

PIN NAMES

PIN	FUNCTION
D0-D5 CLK, CLK TCLK MR Q0-Q5 Q0-Q5 VCCE VCCT VEE	TTL Data Inputs Differential ECL Clock Input TTL Clock Input ECL Master Reset Input True ECL Outputs Inverted ECL Outputs ECL V _{CC} (0V) TTL V _{CC} (+5.0 V) ECL V _{EE} (-5.2 V)



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PLCC-28 FN SUFFIX CASE 776

MARKING DIAGRAM

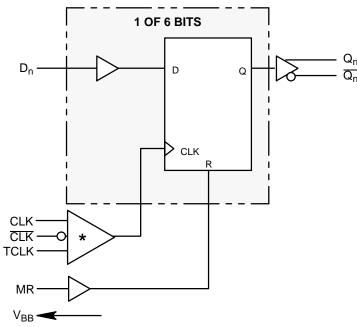


A = Assembly Location

VL = Wafer Lot 'Y = Year VW = Work Week

ORDERING INFORMATION

Device	Package	Shipping
MC10H604FN	PLCC-28	37 Units/Rail
MC100H604FN	PLCC-28	37 Units/Rail



- When using MECL inputs, TCLK must be tied to ground (0 V).
 When using only one MECL input, the unused MECL input must be tied to V_{BB}, and TCLK must be tied to ground (0 V).
 When using TCLK, both MECL inputs must be tied to V_{EE} (-5.2 V).

Figure 2. Logic Symbol

TRUTH TABLE

D _n	MR	TCLK/CLK	Q _n +1
L	L	Z	L
Н	L	Z	Н
Х	Н	X	L

Z = LOW to HIGH Transition

DC CHARACTERISTICS: V_{EE} = V_{EE}(Min) to V_{EE}(Max); V_{CCE} = GND; V_{CCT} = 5.0 V +10%

		0°C		25°C		85°C		
Symbol	Parameter	Min	Max	Min	Max	Min	Max	Unit
I _{EE}	ECL Power Supply Current 10H 100H		130 130		130 140		130 150	mA
I _{CCH}	TTL Power Supply Current		35 45		35 45		35 45	mA

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

10H ECL DC CHARACTERISTICS: V_{CCT} = +5.0 V ± 10%; V_{EE} = -5.20 V ±5%

			0°C		25°C		85°C		
Symbol	Parameter	Condition	Min	Max	Min	Max	Min	Max	Unit
I _{INH} I _{INL}	Input HIGH Current Input LOW Current		0.5	255	0.5	175	0.5	175	μ Α μ Α
V _{IH} V _{IL}	Input HIGH Voltage Input LOW Voltage		-1170 -1950	-840 -1480	-1130 -1950	-810 -1480	-1060 -1950	-720 -1480	mV
V_{BB}	Output Bias Voltage		-1400	-1290	-1370	-1270	-1330	-1210	mV
V _{OH} V _{OL}	Output HIGH Voltage Output LOW Voltage	50 Ω to –2.0 V	-1020 -1950	-840 -1630	-980 -1950	-810 -1630	-910 -1950	-720 -1595	mV

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

100H ECL DC CHARACTERISTICS: V_{CCT} = 5.0 V \pm 10%; V_{EE} = -4.2 V to -5.5 V

			0°C		25°C		85		
Symbol	Parameter	Condition	Min	Max	Min	Max	Min	Max	Unit
I _{INH} I _{INL}	Input HIGH Current Input LOW Current		0.5	255	0.5	175	0.5	175	μΑ μΑ
V _{IH} V _{IL}	Input HIGH Voltage Input LOW Voltage		-1165 -1810	-880 -1475	-1165 -1810	-880 -1475	-1165 -1810	-880 -1475	mV
V_{BB}	Output Bias Voltage		-1400	-1280	-1400	-1280	-1400	-1280	mV
V _{OH} V _{OL}	Output HIGH Voltage Output LOW Voltage	50 Ω to –2.0 V	-1025 -1810	-880 -1620	-1025 -1810	-880 -1620	-1025 -1810	-880 -1620	mV

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

TTL DC CHARACTERISTICS: $V_{CCT} = 5.0 \text{ V} \pm 10\%$; $V_{EE} = -5.2 \text{ V} \pm 5\%$ (10H); $V_{EE} = -4.2 \text{ V}$ to -5.5 V (100H)

			0°C		25°C		85°C		
Symbol	Parameter	Condition	Min	Max	Min	Max	Min	Max	Unit
V _{IH} V _{IL}	Input HIGH Voltage Input LOW Voltage		2.0	0.8	2.0	0.8	2.0	0.8	V V
I _{IH}	Input HIGH Current	V _{IN} = 2.7 V V _{IN} = 7.0 V		20 100		20 100		20 100	μΑ
I _{IL}	Input LOW Current	V _{IN} = 0.5 V		-0.6		-0.6		-0.6	mA
V_{IK}	Input Clamp Voltage	$I_{IN} = -18 \text{ mA}$		-1.2		-1.2		-1.2	V

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

AC CHARACTERISTICS: $V_{CCT} = 5.0 \text{ V} \pm 10\%$; $V_{EE} = -5.2 \text{ V} \pm 5\%$ (10H); $V_{EE} = -4.2 \text{ V}$ to -5.5 V (100H)

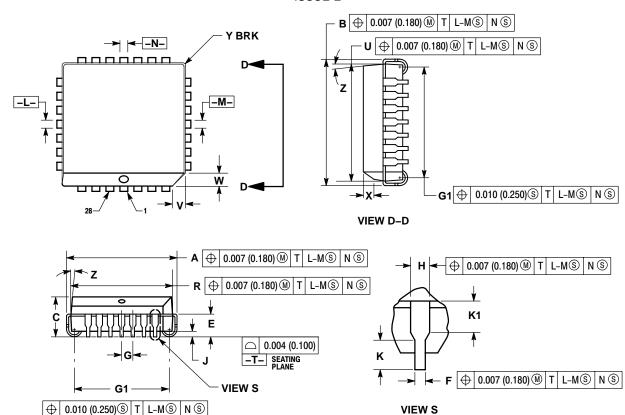
				0°C		25°C			85°C			
Symbol	Parameter	Condition	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
t _{PLH} t _{PHL}	Propagation Delay CLK to Q to Output TCLK to Q MR to Q	50 Ω to –2.0 V	1.5 2.0 1.5		3.5 4.0 4.0	1.5 2.0 1.5		3.5 4.0 4.0	1.5 2.0 1.5		3.5 4.0 4.0	ns
t _s	Setup Time	50 Ω to -2.0 V	1.5	0.5		1.5	0.5		1.5	0.5		ns
t _H	Hold Time	50 Ω to -2.0 V	1.5	0.5		1.5	0.5		1.5	0.5		ns
t _{PW}	Minimum Pulse Width CLK, MR	50 Ω to –2.0 V		1.0			1.0			1.0		ns
V_{PP}	Minimum Input Swing						150					mV
t _r	Rise/Fall Times	20% – 80%	0.3	1.0	2.0	0.3	1.0	2.0	0.3	1.0	2.0	ns

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

PACKAGE DIMENSIONS

PLCC-28 **FN SUFFIX**

PLASTIC PLCC PACKAGE CASE 776-02 **ISSUE E**



NOTES:

- DATUMS -L-, -M-, AND -N- DETERMINED
 WHERE TOP OF LEAD SHOULDER EXITS
 PLASTIC BODY AT MOLD PARTING LINE.
- DIMENSION G1, TRUE POSITION TO BE MEASURED AT DATUM -1-, SEATING PLANE.
 DIMENSIONS R AND U DO NOT INCLUDE MOLD FLASH. ALLOWABLE MOLD FLASH IS
- 0.010 (0.250) PER SIDE.
 4. DIMENSIONING AND TOLERANCING PER

- 4. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 5. CONTROLLING DIMENSION: INCH.
 6. THE PACKAGE TOP MAY BE SMALLER THAN THE PACKAGE BOTTOM BY UP TO 0.012 (0.300). DIMENSIONS R AND U ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY STANDING OF THE PLASTIC BODY EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS, GATE BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY MISMATCH BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY.

 7. DIMENSION H DOES NOT INCLUDE DAMBAR
- PROTRUSION OR INTRUSION. THE DAMBAR PROTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE GREATER THAN 0.037 (0.940). THE DAMBAR INTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE SMALLER THAN 0.025 (0.635).

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.485	0.495	12.32	12.57
В	0.485	0.495	12.32	12.57
С	0.165	0.180	4.20	4.57
Е	0.090	0.110	2.29	2.79
F	0.013	0.019	0.33	0.48
G	0.050	BSC	1.27	BSC
Н	0.026	0.032	0.66	0.81
7	0.020		0.51	
K	0.025		0.64	
R	0.450	0.456	11.43	11.58
U	0.450	0.456	11.43	11.58
٧	0.042	0.048	1.07	1.21
W	0.042	0.048	1.07	1.21
Х	0.042	0.056	1.07	1.42
Υ		0.020		0.50
Z	2 °	10°	2°	10°
G1	0.410	0.430	10.42	10.92
K1	0.040		1.02	

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