

UTCM54123L LINEAR INTEGRATED CIRCUIT

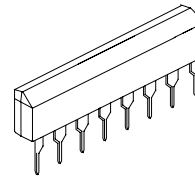
EARTH LEAKAGE CURRENT DETECTOR

DESCRIPTION

The UTC M54123L is a semiconductor integrated circuit with amplifier for a high-speed earth leakage circuit breaker.

FEATURES

- *Suitable for JIS C 8371
- *Good temperature characteristics of input sensitivity current
- *High input sensitivity ($V_T=6.1\text{mV Typ.}$)
- *Low external component count
- *High noise and surge-proof
- *Low power dissipation ($P_d=5\text{mW Typ.}$) and may be used both as 100V and 200V.
- *Wide temperature range ($T_a=-20\sim+80^\circ\text{C}$)



SIP-8

APPLICATION

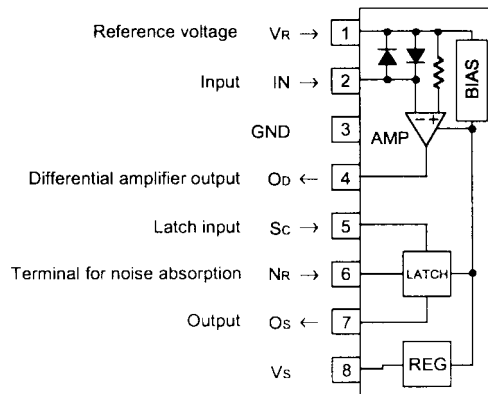
- *High speed earth leakage circuit breaker.

FUNCTION

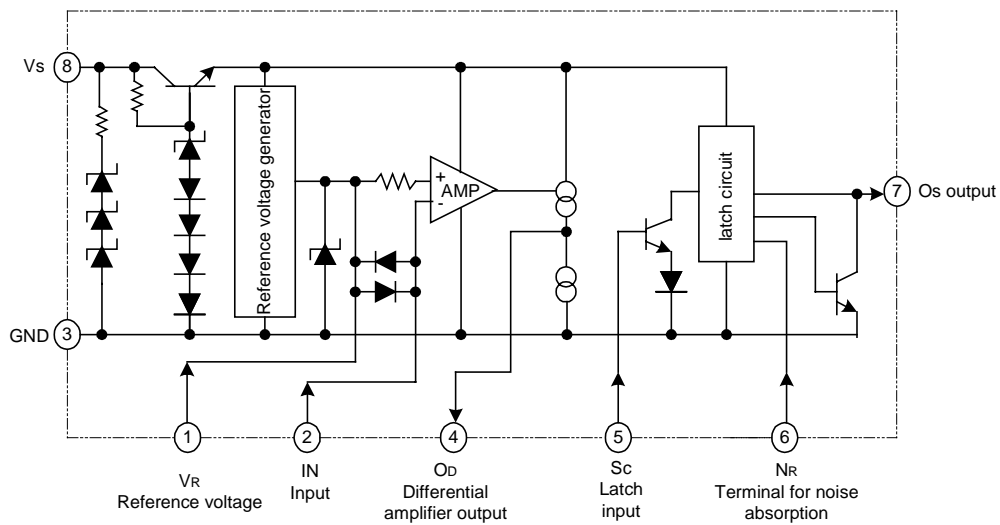
The UTC M54123L circuit for the amplifying parts of earth leakage circuit breaker consists of differential amplifier, latch circuit and voltage regulator. It is connected to the secondary side of the zero current transformer (ZCT) which detects leakage current in the both input of the differential amplifier. Signals amplified by differential amplifier are integrated by an external capacitor, and connects to the input terminal of latch circuit with output suitable for the characteristics of high- speed earth leakage circuit breaker. Latch circuit keeps low in the output till the input voltage reaches the fixed level, and output becomes high when the leakage current more than fixed flows. It drives a thyristor connected to the output terminal of latch circuit.

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PIN CONFIGURATIONS (TOP VIEW)



BLOCK DIAGRAM



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ABSOLUTE MAXIMUM RATINGS (Ta=-20~80°C, unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	RATINGS	UNIT
Supply current	Is		8	mA
VR pin current	IVR	Between VR-IN (Note 1)	250	mA
		Between VR-GND	30	
		Between IN-VR(Note 1)	-250	
IN terminal current	IIN	Between IN-VR(Note 1)	250	mA
		Between IN-GND	30	
		Between VR-IN (Note 1)	-250	
Sc terminal current	Isc		5	mA
Power dissipation	Pd		200	mW
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-55 to +125	°C

Note 1: Current value between VR and IN, and between IN and VR is less than 1ms in the pulse width, and duty cycle is less than 12%, In applying AC current continuously, it is 100 mA in the off-state.

Remarks: GND terminal (pin 3 of the circuit is a basis of all the voltage except differential input clamp voltage of DC electrical characteristics, and direction of current is plus(no signal) in flowing into the circuit and is minus(-signal)in flowing out of it. Maximum value and minimum one are shown as absolute value. Please do not apply voltage whose standard is GND terminal in VR and IN pin.

RECOMMENDED OPERATING CONDITIONS (Ta=-20~+80°C, unless otherwise noted)

PARAMETER	SYMBOL	LIMITS			UNIT
		MIN	TYP	MAX	
Supply Voltage when latch circuit is off-state	Is	12			V
External capacitor between Vs and GND	Cvs	1			μF
External capacitor between Os and GND	Cos			1	μF

ELECTRICAL CHARACTERISTICS (Ta=-20~+80°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		LIMITS			UNIT
				MIN	TYP*	MAX	
Supply current	Is1	Vs=12V, VR-V1=30mV	TEMPERATURE (°C)				μA
			-20	1		580	
			25	1	400	530	
Trip voltage	VT	Vs=16V, VR-V1(Note2)	-20~+80	2	4	6.1	9
Timed current1	ITD1	Vs=16V, VR-V1=30mV VOD=1.2V	25	3	-12		-30
Timed current2	ITD2	Vs=16V, short circuit between VR and V1, VOD=0.8V	25	4	17		37
Output current	Io	VSC=1.4V, VOS=0.8V,	IS1=580μA	-20	5	-200	μA
			IS1=530μA	25	5	-100	
			IS1=480μA	80	5	-75	
Sc "ON" voltage (Note3)	VSC"ON"	Vs=16V	25	6	0.7		1.4
Sc input current	ISC"ON"	Vs=12V	25	7			5

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PARAMETER	SYMBOL	TEST CONDITIONS			LIMITS			UNIT
			TEMPERA -TURE (°C)	TEST CIRCUIT	MIN	TYP*	MAX	
Output low-level current	I _{OL}	V _S =12V, V _{OL} =0.2V	-20~+80	8	200			μA
Input clamp voltage	V _{IC}	V _S =12V, I _{IC} =20mA	-20~+80	9	4.3		6.7	V
Differential input clamp voltage	V _{IDC}	I _{IDC} =100mA	-20~+80	10	0.4		2	V
Maximum current voltage	V _{SM}	I _{SM} =7mA	25	11	20		28	V
Supply current 2(Note 4)	I _{S2}	V _R -V _I , V _{OS} =0.6V (Note 5)	-20~+80	12			900	μA
Latch circuit is off-state supply voltage (Note6)	V _S "OFF"		25	13	0.5			V
Operating time (Note 7)	T _{ON}	V _S =16V, V _R -V _I =0.3V	25	14	2		4	ms

*: Typical values are at Ta=25°C.

Note 2: When standard value of voltage (60Hz) between V_R and V_I is minimum, and output O_S is low-level, or when standard value of voltage(60Hz)between V_R and V_I is maximum, and output O_S is high-level, it is considered as a good one.

Note 3: When standard value of voltage V_{SC}"ON" is minimum, and output O_S is low-level, or when standard value of voltage V_{SC}"ON" is maximum, and output O_S is high-level, it is considered as a good one.

Note 4: Supply current 2 is necessary to keep high in output O_S.

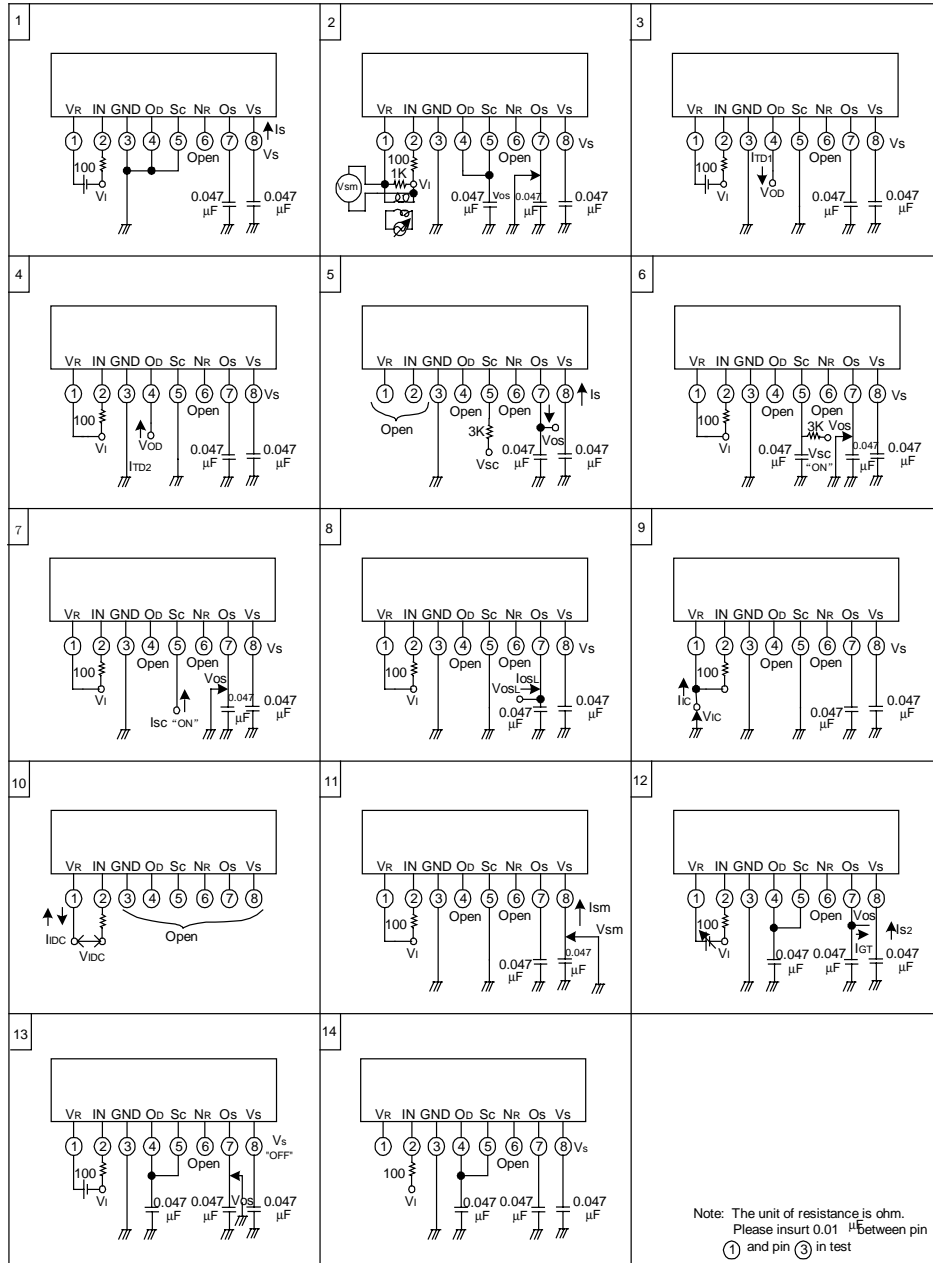
Note 5: After applying 30mV between V_R and V_I and shorting between them,it is considered as a good one if standard value of I_{GT} flows out of output O_S

Note 6: After supply voltage applies 12V and output O_S is high-level,it is considered as a good one in the standard value of supply voltage and in the low-level of output O_S.

Note 7: Operating time is a time from applying fixed input till operating latch circuit in 0.047μF between O_D and GND.

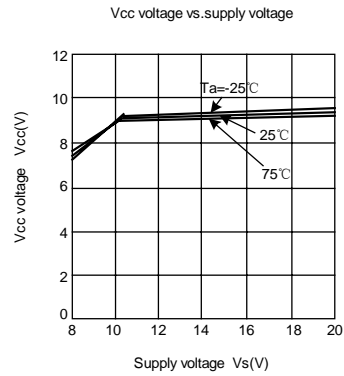
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TEST CIRCUIT

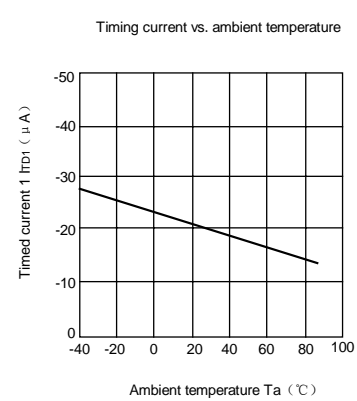
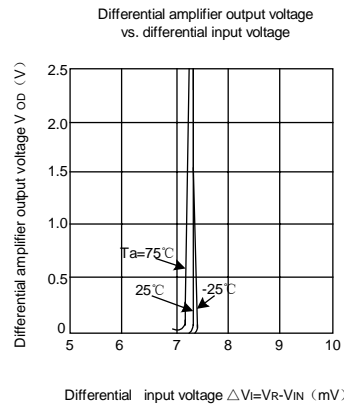
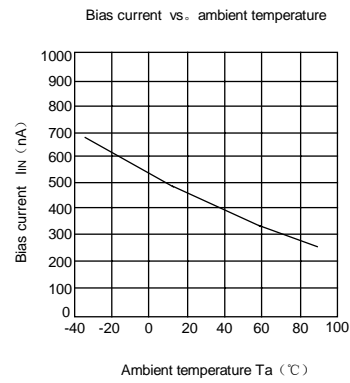
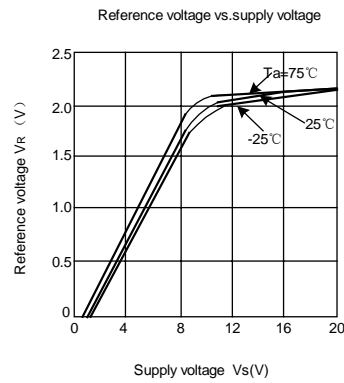
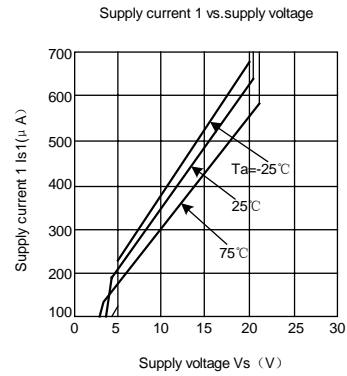


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TYPICAL CHARACTERISTICS

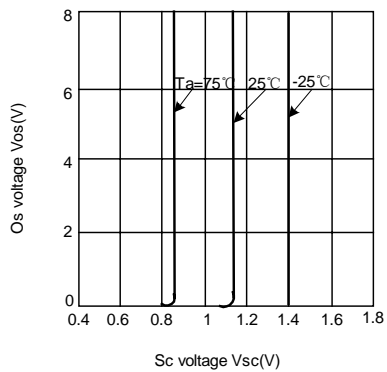


Vcc voltage generates by the constant voltage circuit in IC.
This is measured not by M54123L but by a special element.

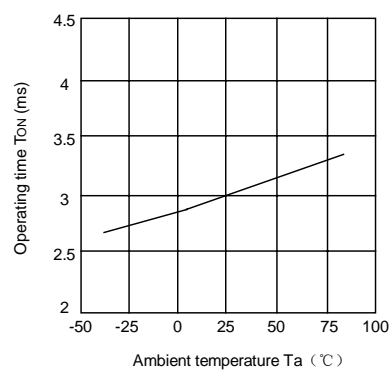


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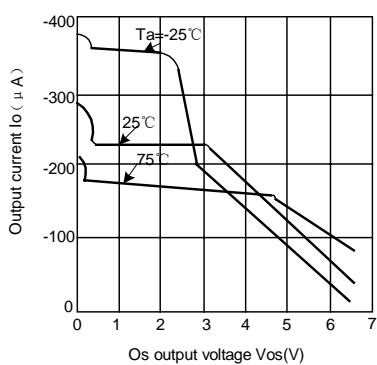
Os voltage vs.Sc voltage



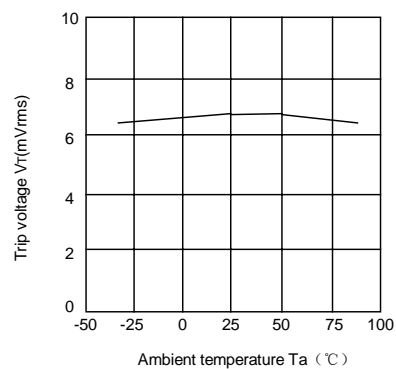
Operating time vs. ambient temperature



Output current vs. Os output voltage



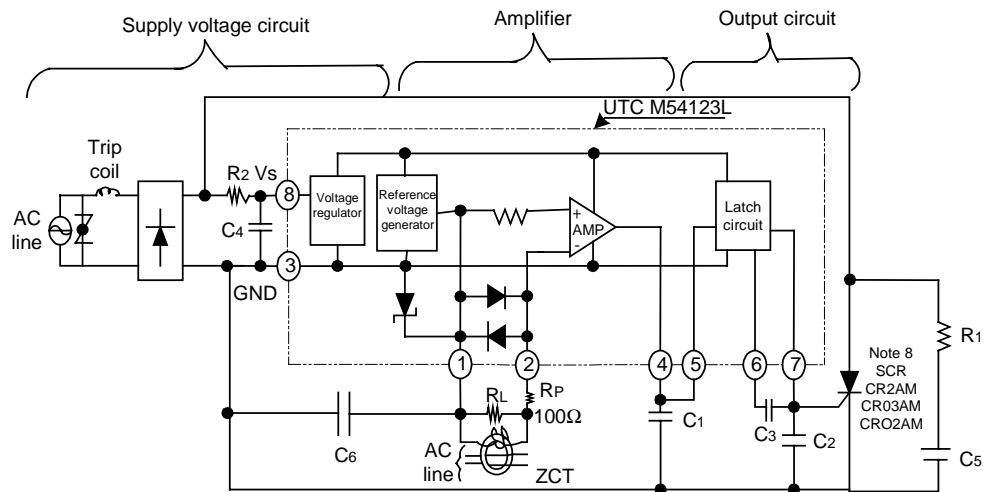
Trip voltage vs. ambient temperature



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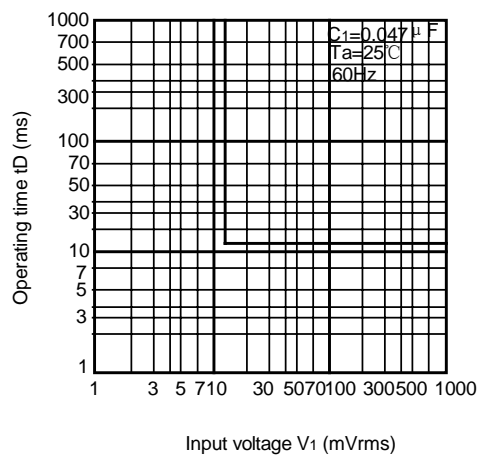
APPLICATION CIRCUIT

HIGH-SPEED LEAKAGE CIRCUIT BREAKER WITH UTC M54123L



Note 8: Gate current must be selected.
Please select voltage resistance by AC supply voltage

Operating time vs. input voltage



UTCM54123L LINEAR INTEGRATED CIRCUIT

Supply voltage circuit is connected as a previous diagram. Please decide constants R1, R2, C4, and C5 of a filter in order to keep at least 12V in Vs, when normal, supply current flows.

In this case, please connect C4 (more than 1 μ F) and C2 (less than 1 μ F). ZCT and load resistance R_L of ZCT are connected between input pin 1 and 2. In this case protective resistance (Rp=100 Ω) must be insured. Sensitivity current is regulated by R_L, and output of amplifier shows in pin 4. External capacitor C1 between pin 4 and GND is used for noise removal.

When large current is grounded in the primary side (AC line) of ZCT, the wave form in the secondary side of ZCT is distorted and some signals does not appear in the output of amplifier. So please connect a varistor or a diode (2 pcs) to ZCT in parallel.

Latch circuit is used to inspect the output level of amplifier and to supply gate current on the external SCR. When input pin becomes more than 1.1V (Typ.), latch circuit operate and supply gate current in the gate of SCR connected to the output pin 7. Pin 6 can be used in the open state, but please connect capacitor (about 0.047 μ F) between pin 6 and pin 7.

Capacitor C6 between pin 1 and GND is used to remove noise and is about 0.047 μ F.