3 V SILICON RFIC FREQUENCY UPCONVERTER

FEATURES

- RECOMMENDED OPERATING FREQUENCY: fRFout = 0.4 GHz to 2.0 GHz fIFin = 100 MHz to 400 MHz
- **SUPPLY VOLTAGE:** Vcc = 2.7 to 5.5 V
- HIGH DENSITY SURFACE MOUNTING: 6 pin super mini mold package
- LOW CARRIER LEAKAGE: Due to double balanced mixer
- BUILT-IN POWER SAVE FUNCTION

DESCRIPTION

NEC's UPC8106TB is a silicon RFIC designed as a frequency upconverter for cellular/cordless telephone transmitter stages and features improved intermodulation. This device is housed in a 6 pin super mini mold or SOT-363 package making it ideal for reducing system size. The UPC8106TB is manufactured using NEC's 20 GHz fr NESAT[™]III silicon bipolar process.

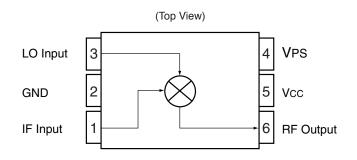
NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

ELECTRICAL CHARACTERISTICS

(TA = 25°C, Vcc = VRFout = 3 V, fIFin = 240 MHz, PLOin = -5 dBm, VPS ≥ 2.7 V unless otherwise specified)

	PART NUMBER PACKAGE OUTLINE	UPC8106TB S06			
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	ТҮР	МАХ
Icc	Circuit Current at VPs ≥ 2.7 V VPs = 0 V	mA μA	4.5	9	13.5 10
CG	Conversion Gain at fRFout = 0.9 GHz, PIFin = -30 dBm fRFout = 1.9 GHz, PIFin = -30 dBm	dB dB	6 4	9 7	12 10
Psat	Saturated Output Power at fRFout = 0.9 GHz, PIFin = 0 dBm fRFout = 1.9 GHz, PIFin = 0 dBm	dBm dBm	-4 -6.5	-2 -4	
OIP3	Output Third-Order Intercept Point at fIFin1 = 240.0 MHz fIFin2 = 240.4 MHz PIFin = -20 dBm fRFout = 1.9 GHz	dBm dBm		+5.5 +2.0	
ΙМз	Third-Order Intermodulation Level at fIFin1 = 240 MHz fIFin2 = 240.4 MHz PIFin = -20 dBm fRFout = 0.9 GHz fRFout = 1.9 GHz	dBc dBc		-31 -30	
NF	SSB Noise Figure, fRFout = 0.9 GHz	dB		8.5	
TPS(RISE)	Power Save Rise Time at VPs: GND→Vcc	μS		2.0	
TPS(FALL)	Power Save Fall Time at VPs: Vcc →GND	μS		2.0	

INTERNAL BLOCK DIAGRAM



UPC8106TB

APPLICATION

CELLULAR/CORDLESS TELEPHONE

ABSOLUTE MAXIMUM RATINGS¹ (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage Pins 5 & 6	V	6.0
VPS	Power Save Voltage	V	6.0
Рт	Total Power Dissipation ²	mW	200
Тор	Operating Temperature	°C	-40 to +85
Tstg	Storage Temperature	°C	-55 to +150
Pin	Input Power	dBm	+10

Notes:

1. Operation in excess of any one of these parameters may result in permanent damage.

2. Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB (TA = $+85^{\circ}$ C).

RECOMMENDED OPERATING CONDITIONS

SYMBOLS	PARAMETERS	UNITS	MIN	ТҮР	MAX
Vcc	Supply Voltage ¹	V	2.7	3.0	5.5
Тор	Operating Temperature	°C	-40	+25	+85
Plo	LO Input Level ²	dBm	-10	-5	0
fRFout	RF Output Frequency ³	GHz	0.4		2.5
fIFin	IF Input Frequency	MHz	100		400

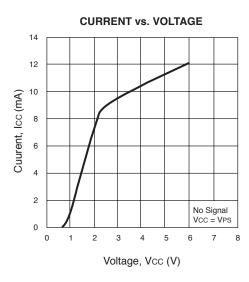
Notes:

1. The same voltage should be supplied to pin 5 and 6.

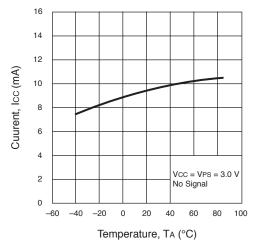
2. $Zs = 50 \Omega$ (without matching).

3. With external matching circuit.

TYPICAL PERFORMANCE CURVES (TA = +25°C, VCC = VRFout)

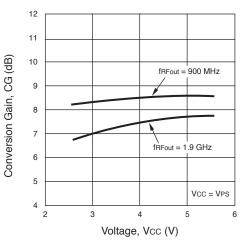




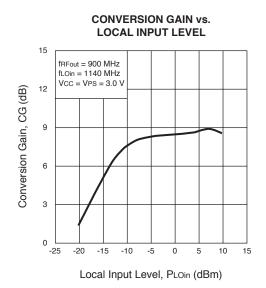


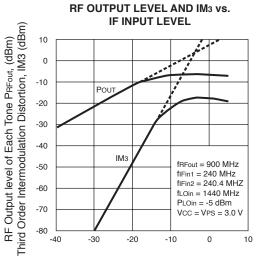
CURRENT vs. POWER SAVE VOLTAGE 12 VCC = 5.5 V 10 Cuurent, Icc (mA) 8 VCC = 3.0 V 6 4 2 0 0 2 3 4 5 6 Power Save Voltage, VPS (V)

CONVERSION GAIN vs. VOLTAGE



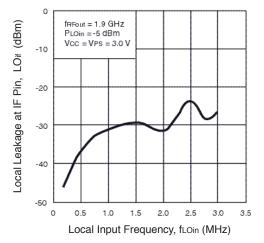
TYPICAL PERFORMANCE CURVES (TA = +25°C, VCC = VRFout)

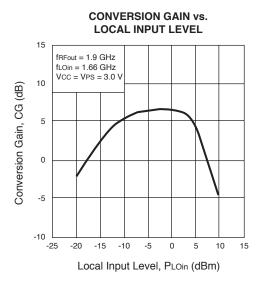




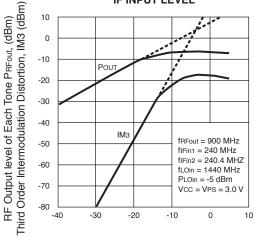
IF Input Level, PIFin (dBm)

LOCAL LEAKAGE AT IF PIN vs. LOCAL INPUT FREQUENCY



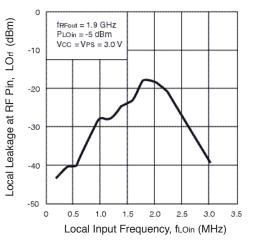


RF OUTPUT LEVEL AND IM₃ vs. IF INPUT LEVEL

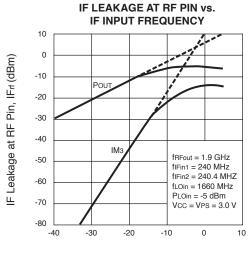


IF Input Level, PIFin (dBm)

LOCAL LEAKAGE AT RF PIN vs. LOCAL INPUT FREQUENCY

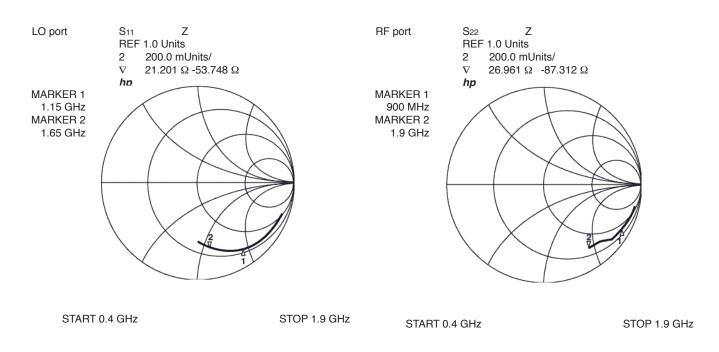


TYPICAL PERFORMANCE CURVES (TA = +25°C, VCC = VRFout)

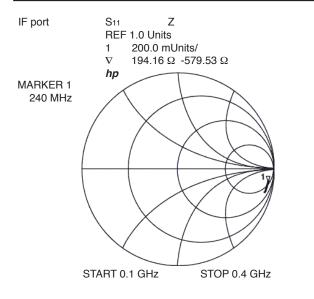


IF Input Frequency, fIFin (MHz)

S-PARAMETERS FOR EACH PORT (VCC = VPS = VRFout = 3.0 V)

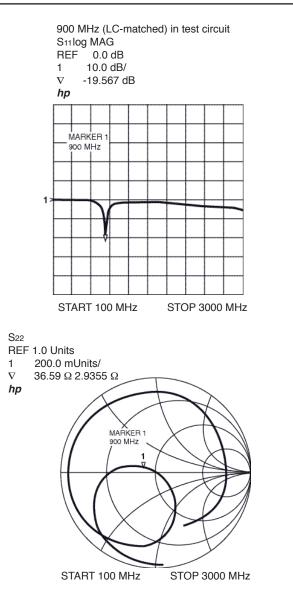


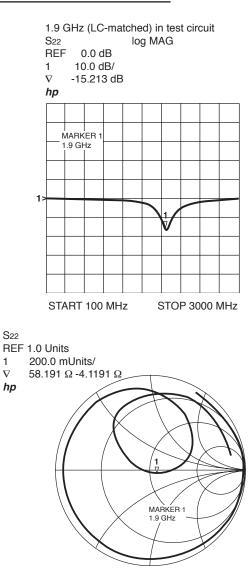
S-PARAMETERS FOR EACH PORT (VCC = VPS = VRFout = 3.0 V)



S-PARAMETERS FOR MATCHED RF OUTPUT

(Vcc = VPS = VRFout = 3.0 V) - with TEST CIRCUITS 1 and 2 - (S22 data is monitored at RF connector on board.)





START 100 MHz

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STOP 3000 MHz

PIN FUNCTIONS

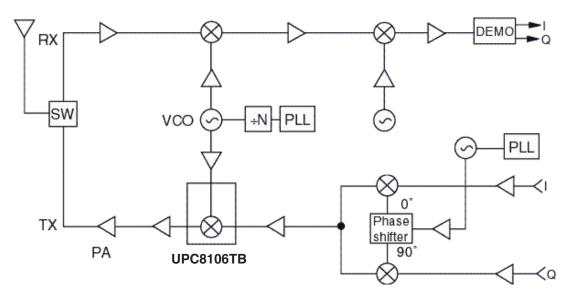
Pin No.	Symbol	Supply Voltage (V)	Pin ¹ Voltage (V)	Description	Equivalent Circuit
1	IF Input	-	1.3	This pin is the IF input to the double bal- anced mixer. The input is a high imped- ance.	
2	GND	0	_	GND pin. Ground pattern on the board should be as wide as possible. Trace length should be kept as short as possible to minimize ground impedance.	
3	LOIN	-	2.4	LO input pin. Recommended input level is -10 to 0 dBm.	
5	Vcc	2.7 to 5.5	-	Supply voltage pin.	
6	RF Output	2.7 to 3.6	_	This pin is the RF output. This pin is designed as an open collector. Due to the high impedance output, this pin requires an external LC matching circuit.	
4	VPS	Vcc/GND	-	Power save control pin. Bias controls operation as follows: Pin Bias Control Vcc ON GND Power Save	Vcc 5 w 4 GND 2

Note:

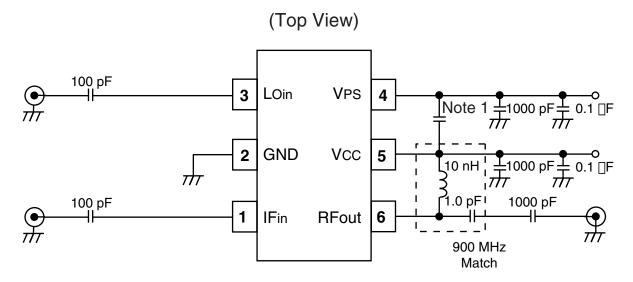
1. Each pin voltage is measured with Vcc = VPs = VRFout = 3.0 V

SYSTEM APPLICATION EXAMPLE

EXAMPLE OF DECT 900 MHz Cordless Phone



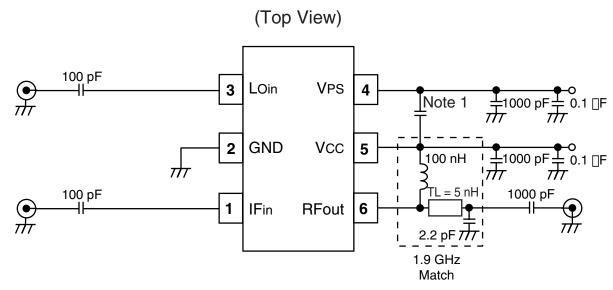
TEST CIRCUIT 1 (RFout = 900 MHz)



Note:

1. In case of unstable operation, connect 100 pF capacitor between pins 4 and 5.

TEST CIRCUIT 2 (RFOUT = 1.9 GHz)

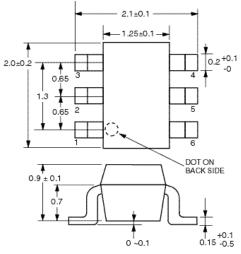


Note:

1. In case of unstable operation, connect 100 pF capacitor between pins 4 and 5.

OUTLINE DIMENSIONS (Units in mm)

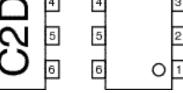
PACKAGE OUTLINE S06



LEAD CONNECTIONS

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(Top View) (Bottom View)



- 1. IF INPUT
- 2. GND
- 3. LO INPUT

4. POWER SAVE

5. Vcc

6. RF OUTPUT

Note:

All dimensions are typical unless otherwise specified.

ORDERING INFORMATION

PART NUMBER	QTY
UPC8106TB-E3-A	3K/Reel

Note:

Embossed Tape, 8 mm wide, Pins 1, 2, and 3 face tape perforation side.

Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

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CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices	
Lead (Pb)	< 1000 PPM	-A Not Detected	-AZ (*)
Mercury	< 1000 PPM	Not Detected	
Cadmium	< 100 PPM	Not Detected	
Hexavalent Chromium	< 1000 PPM	Not Detected	
РВВ	< 1000 PPM	Not Detected	
PBDE	< 1000 PPM	Not Detected	

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