

TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (MACH II π -MOS V)

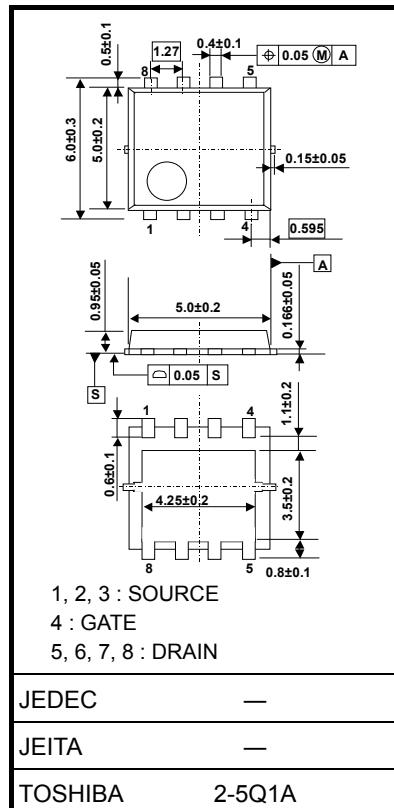
TPCA8008-H

High Speed Switching Applications

Switching Regulator Applications

DC/DC Converter Applications

Unit: mm



Weight: 0.069 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

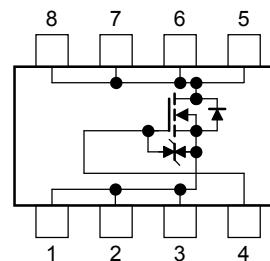
Characteristic	Symbol	Rating	Unit
Drain-source voltage	V _{DSS}	250	V
Drain-gate voltage (R _{GS} = 20 k Ω)	V _{DGR}	250	V
Gate-source voltage	V _{GSS}	± 20	V
Drain current	DC (Note 1)	I _D	A
	Pulsed (Note 1)	I _{DP}	
Drain power dissipation (T _c =25°C)	P _D	45	W
Drain power dissipation (t = 10 s) (Note 2a)	P _D	2.8	W
Drain power dissipation (t = 10 s) (Note 2b)	P _D	1.6	W
Single-pulse avalanche energy (Note 3)	E _{AS}	11	mJ
Avalanche current	I _{AR}	4	A
Repetitive avalanche energy (T _c =25°C) (Note 4)	E _{AR}	4.5	mJ
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

This transistor is an electrostatic-sensitive device. Handle with care.

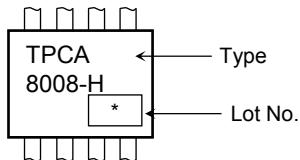
Circuit Configuration



Thermal Characteristics

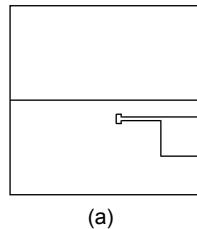
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case ($T_c=25^\circ\text{C}$)	R_{th} (ch-c)	2.78	$^\circ\text{C}/\text{W}$
Thermal resistance, channel to ambient ($t = 10$ s) (Note 2a)	R_{th} (ch-a)	44.6	$^\circ\text{C}/\text{W}$
Thermal resistance, channel to ambient ($t = 10$ s) (Note 2b)	R_{th} (ch-a)	78.1	$^\circ\text{C}/\text{W}$

Marking (Note 5)



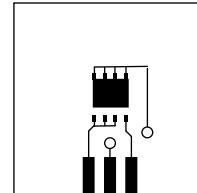
Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)



FR-4
25.4 × 25.4 × 0.8
(Unit: mm)

(a)



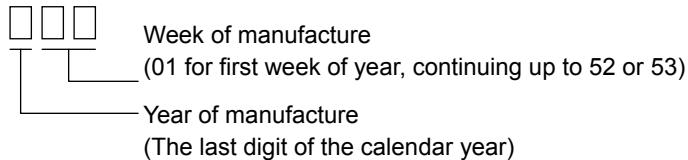
FR-4
25.4 × 25.4 × 0.8
(Unit: mm)

(b)

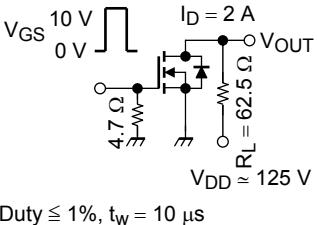
Note 3: $V_{DD} = 50$ V, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 1\text{mH}$, $R_G = 25 \Omega$, $I_{AR} = 4$ A

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: * Weekly code: (Three digits)

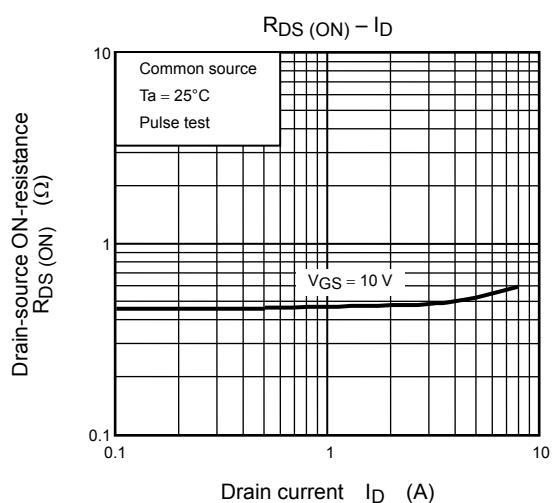
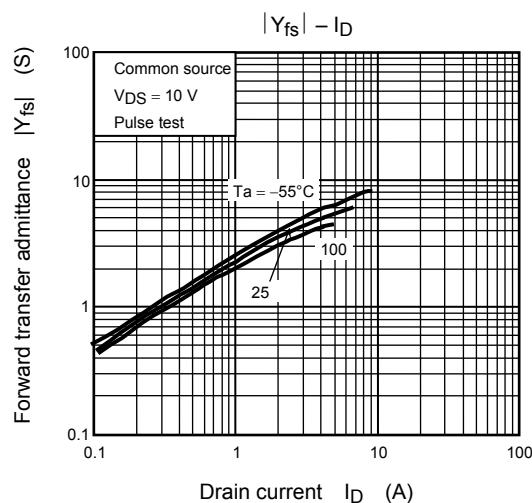
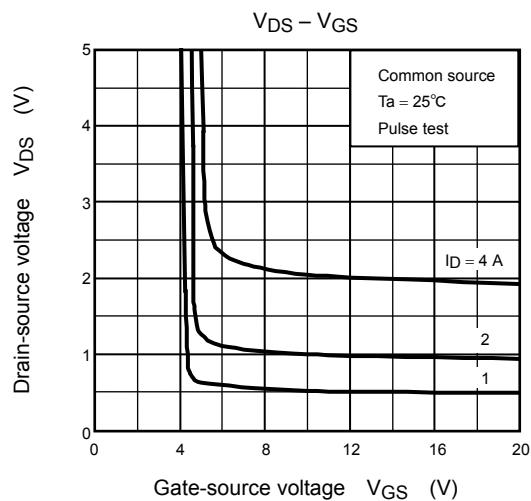
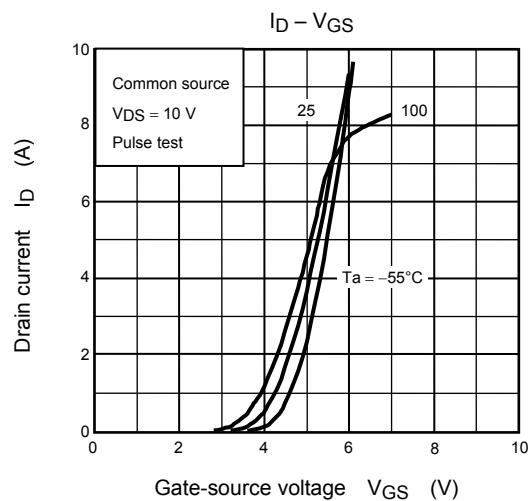
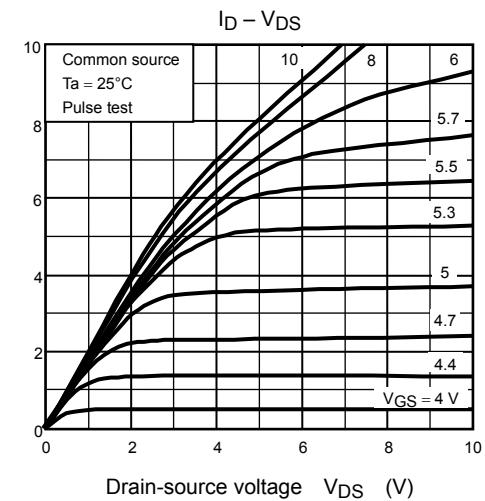
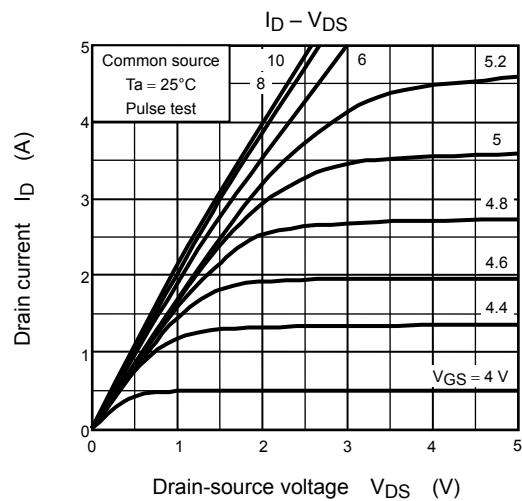


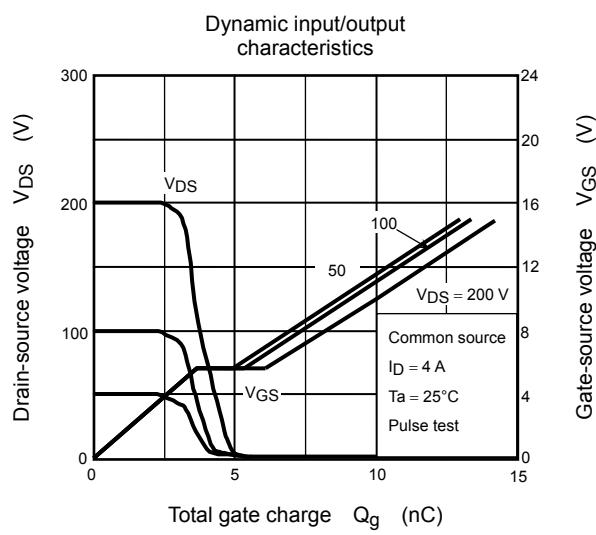
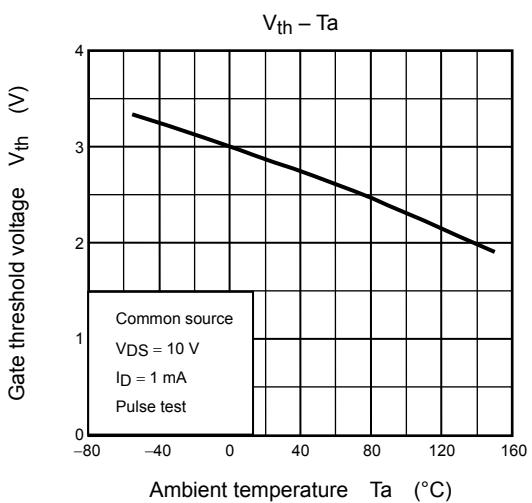
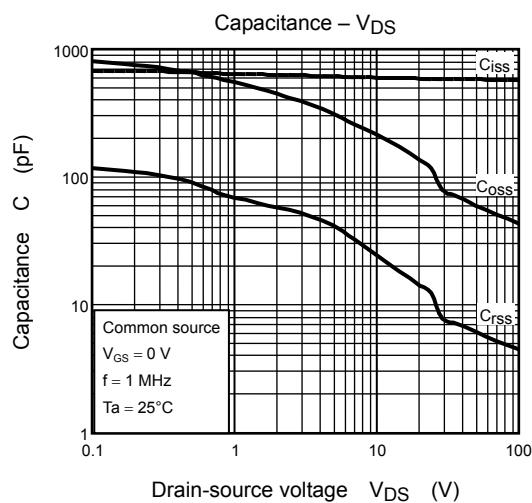
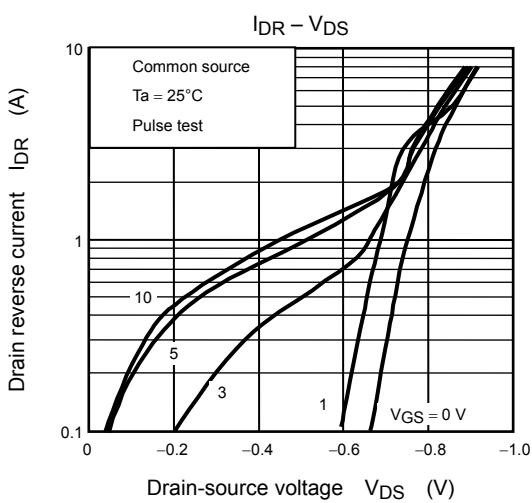
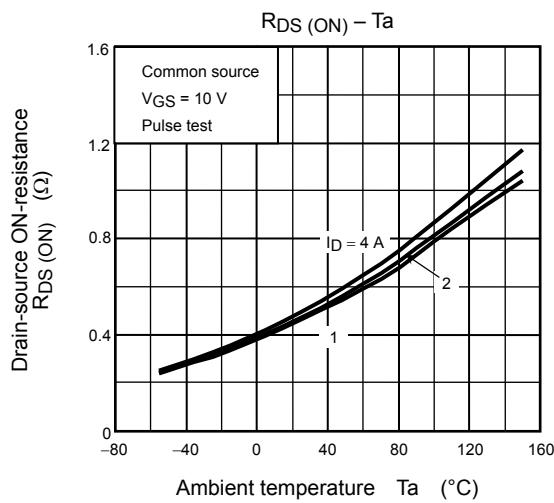
Electrical Characteristics ($T_a = 25^\circ\text{C}$)

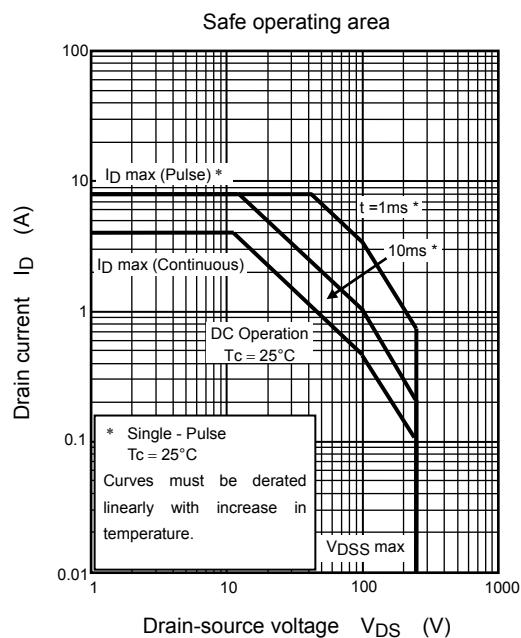
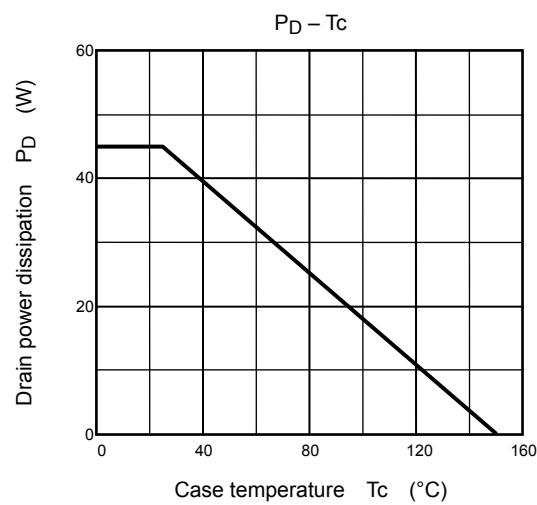
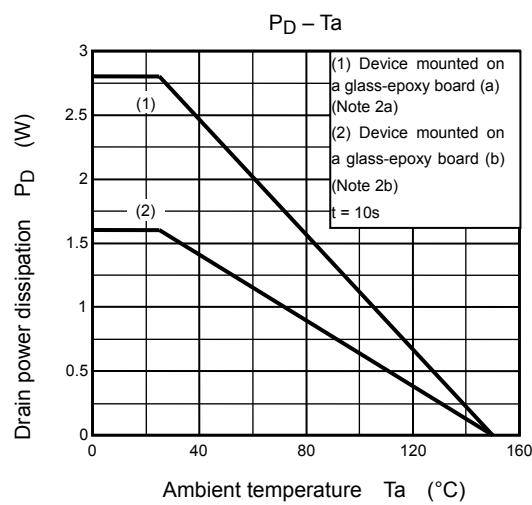
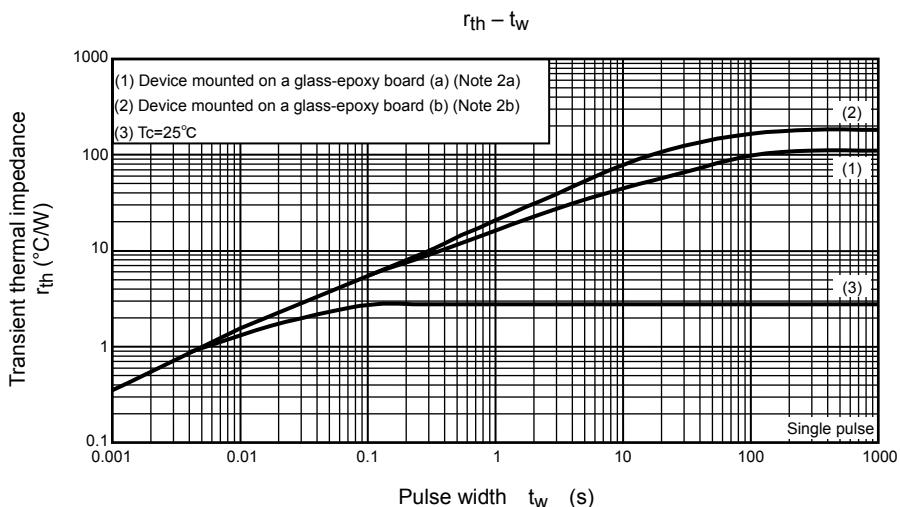
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	I_{GSS}	$V_{GS} = \pm 16\text{ V}$, $V_{DS} = 0\text{ V}$	—	—	± 10	μA
Drain cutoff current	I_{DSS}	$V_{DS} = 250\text{ V}$, $V_{GS} = 0\text{ V}$	—	—	100	μA
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$I_D = 10\text{ mA}$, $V_{GS} = 0\text{ V}$	250	—	—	V
	$V_{(\text{BR})\text{DSX}}$	$I_D = 10\text{ mA}$, $V_{GS} = -5\text{ V}$	250	—	—	
		$I_D = 10\text{ mA}$, $V_{GS} = -20\text{ V}$	200	—	—	
Gate threshold voltage	V_{th}	$V_{DS} = 10\text{ V}$, $I_D = 1\text{ mA}$	2.0	—	4.0	V
Drain-source ON-resistance	$R_{DS\text{ (ON)}}$	$V_{GS} = 10\text{ V}$, $I_D = 2\text{ A}$	—	0.47	0.58	Ω
Forward transfer admittance	$ Y_{fs} $	$V_{DS} = 10\text{ V}$, $I_D = 2\text{ A}$	1.5	3.3	—	S
Input capacitance	C_{iss}	$V_{DS} = 10\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$	—	600	—	pF
Reverse transfer capacitance	C_{rss}		—	20	—	
Output capacitance	C_{oss}		—	220	—	
Switching time	Rise time	t_r	 V_{GS} 10 V 0 V $I_D = 2\text{ A}$ $V_{DD} \approx 125\text{ V}$ Duty $\leq 1\%$, $t_w = 10\text{ }\mu\text{s}$	—	8	ns
	Turn-on time	t_{on}		—	17	
	Fall time	t_f		—	13	
	Turn-off time	t_{off}		—	70	
Total gate charge (gate-source plus gate-drain)	Q_g	$V_{DD} \approx 200\text{ V}$, $V_{GS} = 10\text{ V}$, $I_D = 4\text{ A}$	—	10	—	nC
Gate-source charge	Q_{gs}		—	7.6	—	
Gate-drain ("Miller") charge	Q_{gd}		—	2.4	—	
Gate switch charge	Q_{sw}		—	3.7	—	

Source-Drain Ratings and Characteristics ($T_a = 25^\circ\text{C}$)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	I_{DRP}	Pulse (Note 1)	—	—	8	A
Forward voltage (diode)	V_{DSF}		$I_{DR} = 4\text{ A}$, $V_{GS} = 0\text{ V}$	—	—	V







RESTRICTIONS ON PRODUCT USE

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