TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7W14F, TC7W14FU, TC7W14FK

#### SCHMITT INVERTER

The TC7W14 is high speed C<sup>2</sup>MOS SCHMITT INVERTER fabricated with silicon gate C<sup>2</sup>MOS technology. It achieves the high speed operation similar to equivalent LSTTL while maintaining the C<sup>2</sup>MOS low power dissipation.

Pin configuration and function are the same as the TC7WU04 but the inputs have 25% V<sub>CC</sub> hysteresis and with its schmitt trigger function, the TC7W14 can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **FEATURES**

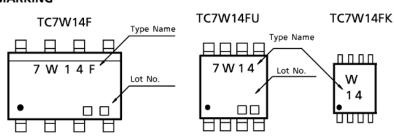
•	High Speed		$t_{pd} = 11ns$ (Typ.) at
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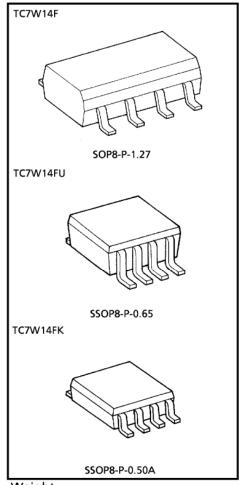
• Low Power Dissipation .......... 
$$I_{CC} = 1\mu A$$
 (Max.) at  $Ta = 25^{\circ}C$ 

$$V_{CC} = 5V$$

Wide Operating Voltage Range ... V<sub>CC</sub> (opr) = 2∼6V

#### MARKING





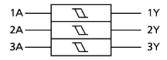
Weight

SOP8-P-1.27 : 0.05g (Typ.) SSOP8-P-0.65 : 0.02g (Typ.) SSOP8-P-0.50A : 0.01g (Typ.)

## MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	Vcc	-0.5~7	V
DC Input Voltage	VIN	-0.5~V <sub>CC</sub> +0.5	V
DC Output Voltage	Vout	−0.5~V <sub>CC</sub> +0.5	V
Input Diode Current	ΙK	± 20	mA
Output Diode Current	loк	± 20	mA
DC Output Current	IOUT	± 25	mA
DC V <sub>CC</sub> /Ground Current	Icc	± 25	mA
Baucan Dissipation	D-	300 (FM8, SM8)	\^/
Power Dissipation	PD	200 (US8)	mW
Storage Temperature	T <sub>stg</sub>	- 65~150	°C
Lead Temperature (10s)	TL	260	°C

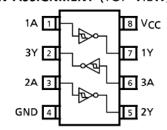
#### LOGIC DIAGRAM



#### TRUTH TABLE

Α	Υ
L	Н
Н	L

# PIN ASSIGNMENT (TOP VIEW)



# RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	2~6	V
Input Voltage	VIN	0~V <sub>CC</sub>	V
Output Voltage	Vout	0~V <sub>CC</sub>	V
Operating Temperature	T <sub>opr</sub>	-40~85	°C

## DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	TEST CONDITION			Ta = 25°C			Ta = -4	UNIT	
CHARACTERISTIC	STIVIBUL			Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	ONIT
Positive Threshold				2.0	1.0	1.25	1.5	1.0	1.5	
Voltage	V <sub>P</sub>		_	4.5	2.3	2.7	3.15	2.3	3.15	V
voitage				6.0	3.0	3.5	4.2	3.0	4.2	
Nogativa				2.0	0.3	0.65	0.9	0.3	0.9	
Negative	V <sub>N</sub>		_	4.5	1.13	1.6	2.0	1.13	2.0	V
Threshold Voltage				6.0	1.5	2.3	2.6	1.5	2.6	
				2.0	0.3	0.6	1.0	0.3	1.0	
Hysteresis Voltage	VH		_	4.5	0.6	1.1	1.4	0.6	1.4	V
				6.0	0.8	1.2	1.7	0.8	1.7	
		V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -20μA	2.0	1.9	2.0		1.9	_	
Ulark Lavel	V <sub>ОН</sub>			4.5	4.4	4.5	—	4.4	—	
High-Level				6.0	5.9	6.0	l —	5.9	—	l v l
Output Voltage			$I_{OH} = -4mA$	4.5	4.18	4.31		4.13	_	
			$I_{OH} = -5.2 \text{mA}$	6.0	5.68	5.80	—	5.63	_	
	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \mu A$	4.5	l —	0.0	0.1	<b>—</b>	0.1	
Low-Level				6.0	<b>—</b>	0.0	0.1	<b>—</b>	0.1	l v l
Output Voltage			I <sub>OL</sub> = 4mA	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 5.2 mA$	6.0	_	0.18	0.26	_	0.33	
Input Leakage Current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	± 0.1	_	± 1.0	
Quiescent Supply Current	Icc			6.0	_	_	1.0	_	10.0	μΑ

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# AC ELECTRICAL CHARACTERISTICS ( $C_L = 15pF$ , $V_{CC} = 5V$ , Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	T	UNIT		
CHARACTERISTIC	3 TIVIBOL	TEST CONDITION	MIN.	TYP.	MAX.	OIVII
Output Transition	<sup>t</sup> TLH	_		1	8	ns
Time	<sup>t</sup> THL	_			"	113
Propagation Delay	t <sub>pLH</sub>			11	21	ns
Time	t <sub>pHL</sub>	_	_	''	'	115

## AC ELECTRICAL CHARACTERISTICS ( $C_L = 50pF$ , Input $t_r = t_f = 6ns$ )

CHARACTERISTIC	SYMBOL	. TEST CONDITION		Ta = 25°C			Ta = -4	UNIT	
CHARACTERISTIC	STIVIBOL			MIN.	TYP.	MAX.	MIN.	MAX.	O NI I
Output Transition	t		2.0		30	75		95	
Time	1	_	4.5	l —	8	15	—	19	ns
Time	<sup>t</sup> THL	HL	6.0	—	7	13	—	16	
Propagation Delay	.'	_	2.0	_	42	125	_	155	
Time			4.5	l —	14	25	—	31	ns
Time	t <sub>pHL</sub>		6.0	—	12	21	—	26	
Input Capacitance	CIN	_		_	5	10	_	10	
Power Dissipation Capacitance	C <sub>PD</sub>	(Note 1)		_	28	_	_	_	pF

(Note 1): CpD is defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to Test Circuit).

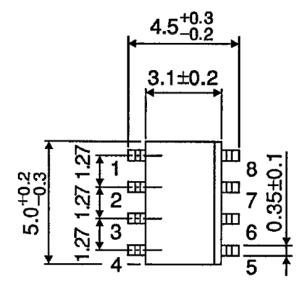
Average operating current can be obtained by the equation hereunder.  $I_{CC}(opr) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 2$  (per gate)

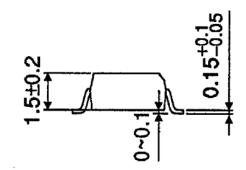
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## PACKAGE DIMENSIONS

SOP8-P-1.27

Unit: mm



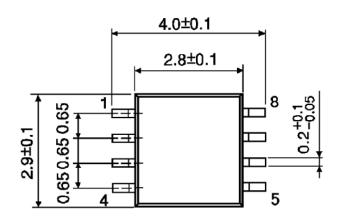


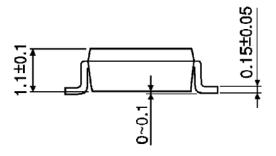
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Weight: 0.05g (Typ.)

## PACKAGE DIMENSIONS SSOP8-P-0.65

Unit: mm





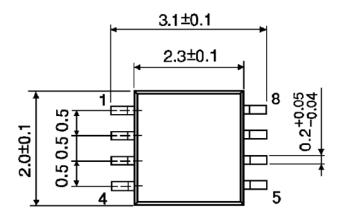
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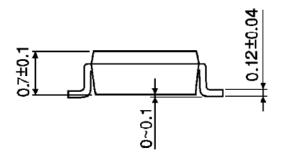
Weight: 0.02g (Typ.)

## PACKAGE DIMENSIONS

SSOP8-P-0.50A

Unit: mm





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Weight: 0.01g (Typ.)

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20070701-EN GENERAL

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