TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX32F,TC74LCX32FN,TC74LCX32FT,TC74LCX32FK

Low-Voltage Quad 2-Input OR Gate with 5-V Tolerant Inputs and Outputs

The TC74LCX32F/FN/FT/FK is a high-performance CMOS 2-input OR gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low-power dissipation.

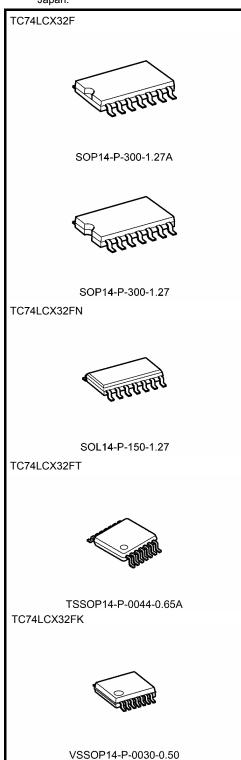
The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: VCC = 2.0 to 3.6 V
- High-speed operation: $t_{pd} = 5.5 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: ±500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- Power-down protection is provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 32 type

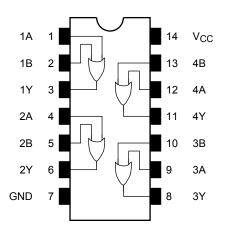
Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

SOP14-P-300-1.27A : 0.18 g (typ.) SOP14-P-300-1.27 : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Pin Assignment (top view)



IEC Logic Symbol

1A — 1 1B — 2	≥ 1	3 1Y
2A — 4 2B — 5		62Y
3A 9 3B 10		8 3Y
4A 12 4B 13		<u>11</u> 4Y

Truth Table

Inputs		Outputs
Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	−0.5 to 7.0	V	
DC input voltage	V _{IN}	−0.5 to 7.0	V	
		-0.5 to 7.0 (Note 2)	V	
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5 (Note 3)		
Input diode current	I _{IK}	-50	mA	
Output diode current	I _{OK}	±50 (Note 4)	mA	
DC output current	lout	±50	mA	
Power dissipation	P _D	180	mW	
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA	
Storage temperature	T _{stg}	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

2

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$



Recommended Operating Conditions (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	2.0 to 3.6	V	
rower supply voltage	v CC	1.5 to 3.6 (Note 2)	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	Vout	0 to 5.5 (Note 3)	V	
		0 to V _{CC} (Note 4)	V	
Output current	I _{OH} /I _{OL}	±24 (Note 5)	mA	
Output current	iOH/iOL	±12 (Note 6)	ША	
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

Note 1: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

Note 3: $V_{CC} = 0 V$

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 6: $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$

Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristi	cs	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	H-level	V _{IH}		_		2.0	_	V
Input voltage	L-level	V _{IL}		_	2.7 to 3.6	_	0.8	v
				I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_	
	H-level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -12 mA	2.7	2.2	_	
				I _{OH} = -18 mA	3.0	2.4	_	V
Output voltage				I _{OH} = -24 mA	3.0	2.2	_	
		V _{OL}	V _{IN} = V _{IL}	$I_{OL} = 100 \mu A$	2.7 to 3.6	_	0.2	
	L-level			$I_{OL} = 12 \text{ mA}$	2.7	_	0.4	
L-ievei	L-level			$I_{OL} = 16 \text{ mA}$	3.0	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current		I _{IN}	V _{IN} = 0 to 5.5 V		2.7 to 3.6	_	±5.0	μΑ
Power-off leakage curr	ent	loff	$V_{IN}/V_{OUT} = 5.5 V$		0		10.0	μΑ
Quiescent supply current		Icc	V _{IN} = V _{CC} or GND		2.7 to 3.6		10.0	
			V _{IN} = 3.6 to 5.5 V		2.7 to 3.6		±10.0	μΑ
Increase in Icc per inpu	ıt	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V		2.7 to 3.6	-	500	

3



AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7	_	6.2	ns
	t_{pHL}		3.3 ± 0.3	1.5	5.5	
Output to output skew	t _{osLH}	(NI ₂ 42)	2.7		_	no
	t _{osHL}	(Note)	3.3 ± 0.3		1.0	ns

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_	3.3	7	pF
Output capacitance	C _{OUT}	_	0	8	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz (Note)	3.3	25	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$

AC Test Circuit

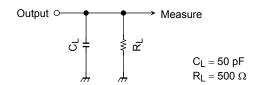


Figure 1

AC Waveform

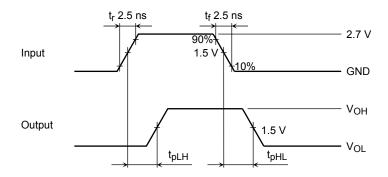
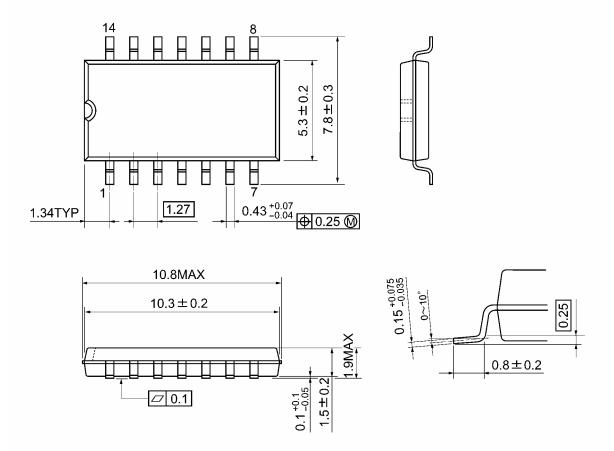


Figure 2 t_{pLH}, t_{pHL}

5

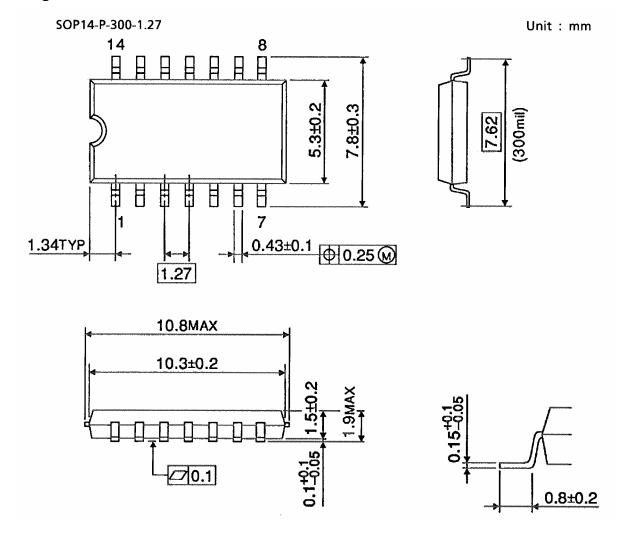
SOP14-P-300-1.27A Unit: mm



6

Weight: 0.18 g (typ.)

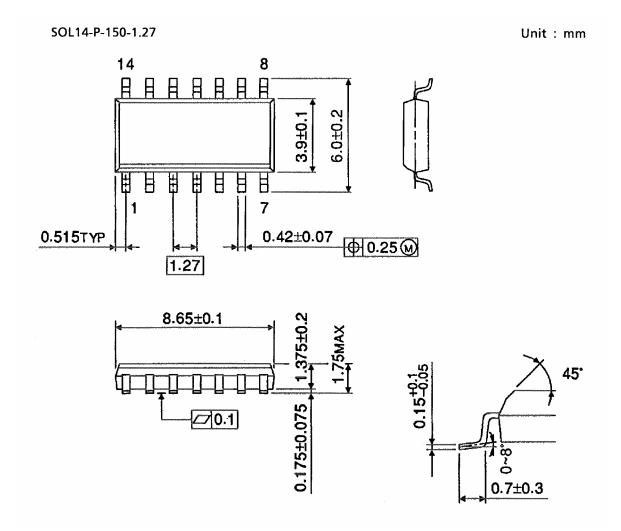
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Weight: 0.18 g (typ.)



Package Dimensions (Note)



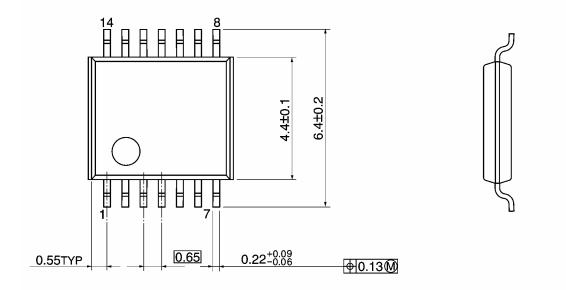
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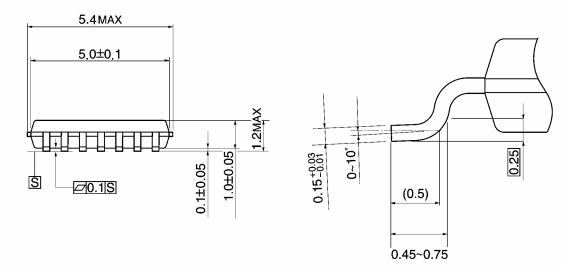
Note: This package is not available in japan.

Weight: 0.12 g (typ.)



TSSOP14-P-0044-0.65A Unit: mm



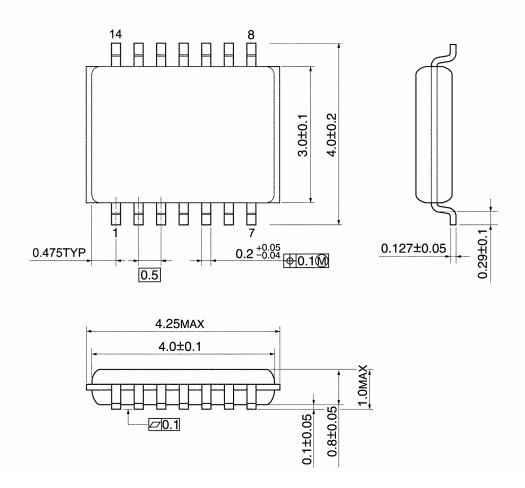


Weight: 0.06 g (typ.)

9

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VSSOP14-P-0030-0.50 Unit: mm



10

Weight: 0.02 g (typ.)

Note: Lead (Pb)-Free Packages

SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A VSSOP14-P-0030-0.50

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