Note: xxxFN (JEDEC SOP) is not available in

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74LCX257F,TC74LCX257FN,TC74LCX257FT

Low-Voltage Quad 2-Channel Multiplexer (3-state) with 5-V Tolerant Inputs and Outputs

The TC74LCX257F/FN/FT is a high-performance CMOS multiplexer. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low-power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

It is composed of four independent 2-channel multiplexers with common select and  $\ \overline{OE}\ .$ 

If  $\overline{OE}$  is set low, the outputs are held in a high-impedance state. When SELECT is set low, "A" data inputs are enabled. Conversely, when SELECT is high, "B" data inputs are enabled.

All inputs are equipped with protection circuits against static discharge.

#### **Features**

- Low-voltage operation: VCC = 2.0 to 3.6 V
- High-speed operation:  $t_{pd} = 6.0 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Output current:  $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: ±500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 257 type

Japan. TC74LCX257F SOP16-P-300-1.27A SOP16-P-300-1 27 TC74LCX257FN SOL16-P-150-1.27 TC74LCX257FT

Weight

 SOP16-P-300-1.27A
 : 0.18 g (typ.)

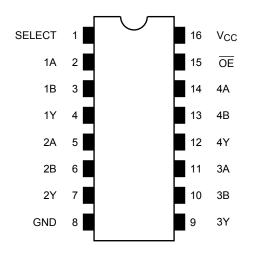
 SOP16-P-300-1.27
 : 0.18 g (typ.)

 SOL16-P-150-1.27
 : 0.12 g (typ.)

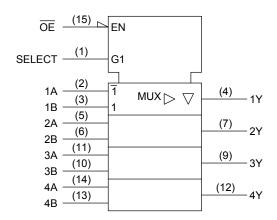
 TSSOP16-P-0044-0.65A
 : 0.06 g (typ.)

TSSOP16-P-0044-0.65A

# Pin Assignment (top view)



# **IEC Logic Symbol**



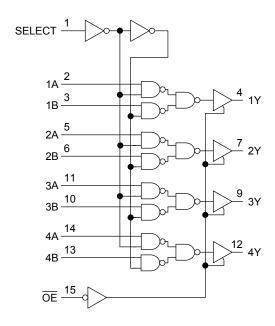
#### **Truth Table**

·	Inputs				
ŌĒ	SELECT	Α	В	Υ	
Н	Х	X	Х	Z	
L	L	L	Х	L	
L	L	Н	X	Н	
L	Н	X	L	L	
L	Н	Х	Н	Н	

X: Don't care

Z: High impedance

### **System Diagram**



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# **TOSHIBA**

### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	−0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	−0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
		(Note 3)	
Input diode current	I <sub>IK</sub>	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	T <sub>stg</sub>	-65 to 150	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: Output in OFF state

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND, V_{OUT} > V_{CC}$ 

### **Recommended Operating Conditions (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	2.0 to 3.6	V	
Tower supply voltage	VCC	1.5 to 3.6 (Note 2)	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V	0 to 5.5 (Note 3)	٧	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub> (Note 4)		
Output current	la/la.	±24 (Note 5)	mA	
Output current	I <sub>OH</sub> /I <sub>OL</sub>	±12 (Note 6)	IIIA	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

Note 1: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

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Note 2: Data retention only

Note 3: Output in OFF state

Note 4: High or low state

Note 5:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 6:  $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$ 

Note 7:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V



### **Electrical Characteristics**

#### DC Characteristics ( $Ta = -40 \text{ to } 85^{\circ}\text{C}$ )

Characteristics		Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
	H-level	Maria				2.0		
Input voltage		V <sub>IH</sub>	-	_	2.7 to 3.6	2.0		V
	L-level	V <sub>IL</sub>	-	_	2.7 to 3.6	_	8.0	
				$I_{OH} = -100 \mu A$	2.7 to 3.6	V <sub>CC</sub> -0.2	_	
	H-level	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_	
				$I_{OH} = -18 \text{ mA}$	3.0	2.4	_	
Output voltage				I <sub>OH</sub> = -24 mA	3.0	2.2	_	V
				I <sub>OL</sub> = 100 μA	2.7 to 3.6	_	0.2	
	L-level	.,	\ \ \\ -=\\	I <sub>OL</sub> = 12 mA	2.7	_	0.4	
L-ievei	L-ievei	vel V <sub>OL</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 16 mA	3.0	_	0.4	
			I <sub>OL</sub> = 24 mA	3.0	_	0.55		
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		2.7 to 3.6	_	±5.0	μА
3-state output OFF state current			V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		0.74-0.0		0	
		loz	V <sub>OUT</sub> = 0 to 5.5 V		2.7 to 3.6	_	±5.0	μΑ
Power-off leakage cu	Power-off leakage current I <sub>OFF</sub> V <sub>IN</sub> /V <sub>OUT</sub> = 5.5 V		0	_	10.0	μА		
Quiescent supply current		1	V <sub>IN</sub> = V <sub>CC</sub> or GND		2.7 to 3.6	_	10.0	
		ICC	V <sub>IN</sub> /V <sub>OUT</sub> = 3.6 to 5.5 V		2.7 to 3.6	_	±10.0	μА
Increase in Icc per input		Δlcc	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6	_	500	

### AC Characteristics ( $Ta = -40 \text{ to } 85^{\circ}\text{C}$ )

Characteristics	Symbol	Test Condition	V 00	Min	Max	Unit
			V <sub>CC</sub> (V)			
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.7	_	6.5	ns
(A, B-Y)	t <sub>pHL</sub>	rigure 1, rigure 2	$3.3 \pm 0.3$	1.5	6.0	115
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.7		8.5	ns
(SELECT-Y)	t <sub>pHL</sub>	rigure 1, rigure 2	$3.3 \pm 0.3$	1.5	7.0	115
Output enable time	t <sub>pZL</sub>	Figure 1, Figure 3	2.7	_	8.5	ns
Output enable time	t <sub>pZH</sub>	rigure 1, rigure o	$3.3 \pm 0.3$	1.5	7.0	113
Output disable time	t <sub>pLZ</sub>	Figure 1, Figure 3	2.7		6.0	ns
Output disable time	t <sub>pHZ</sub>	rigure 1, rigure 3	$3.3 \pm 0.3$	1.5	5.5	115
Output to output skew	t <sub>osLH</sub>	(Alata)	2.7	_	_	ns
Output to output skew	t <sub>osHL</sub>	(Note)	$3.3 \pm 0.3$	_	1.0	113

Note: Parameter guaranteed by design.

 $(t_{\text{OSLH}} = |t_{\text{pLHm}} - t_{\text{pLHn}}|, \, t_{\text{OSHL}} = |t_{\text{pHLm}} - t_{\text{pHLn}}|)$ 

#### Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500 \Omega$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic V <sub>OL</sub>	V <sub>OLP</sub>	V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V	3.3	8.0	V
Quiet output minimum dynamic V <sub>OL</sub>	V <sub>OLV</sub>	V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V	3.3	8.0	V

#### **Capacitive Characteristics (Ta = 25°C)**

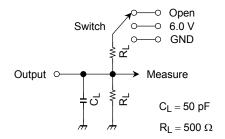
Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	_	3.3	7	pF
Output capacitance	C <sub>OUT</sub>	_	3.3	8	pF
Power dissipation capacitance	C <sub>PD</sub>	f <sub>IN</sub> = 10 MHz (Note	3.3	25	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

#### **AC Test Circuit**



Parameter	Switch
t <sub>pLH</sub> , t <sub>pHL</sub>	Open
t <sub>pLZ</sub> , t <sub>pZL</sub>	6.0 V
t <sub>pHZ</sub> , t <sub>pZH</sub>	GND

Figure 1

#### **AC Waveform**

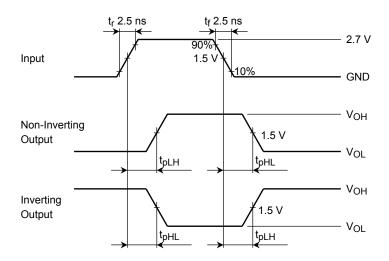


Figure 2 t<sub>pLH</sub>, t<sub>pHL</sub>

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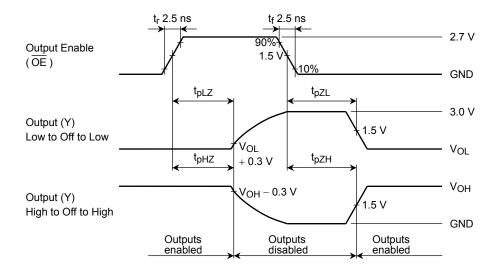


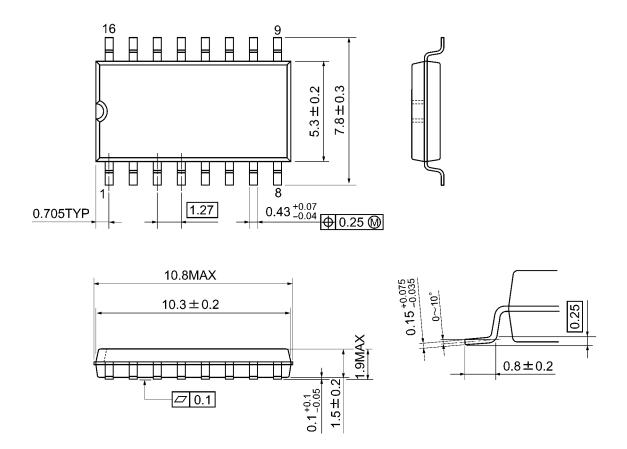
Figure 3  $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$ 

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# **Package Dimensions**

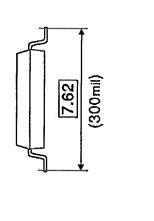
SOP16-P-300-1.27A Unit: mm

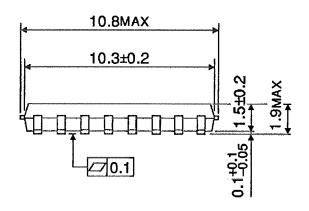


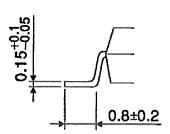
Weight: 0.18 g (typ.)

Unit: mm

### **Package Dimensions**





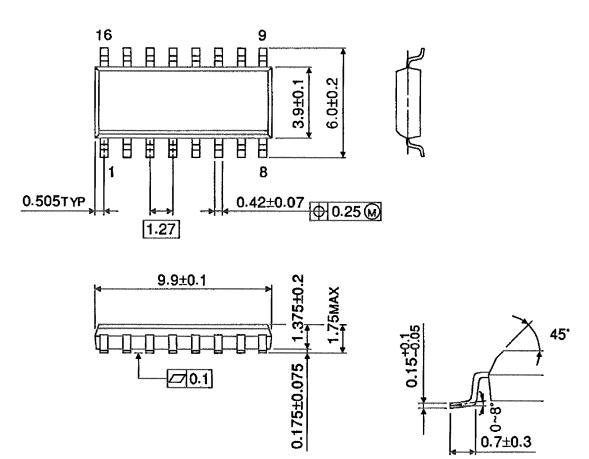


Weight: 0.18 g (typ.)

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# **Package Dimensions (Note)**

SOL16-P-150-1.27 Unit: mm



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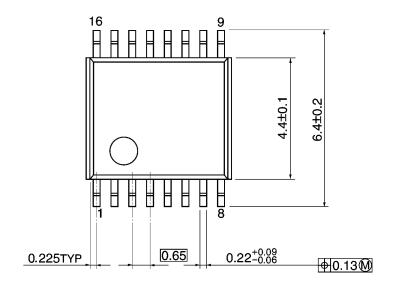
Note: This package is not available in Japan.

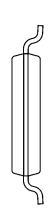
Weight: 0.12 g (typ.)

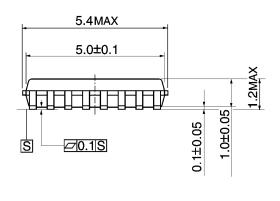
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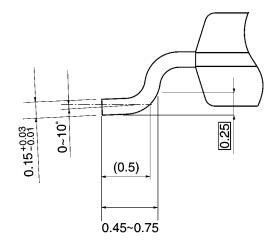
TSSOP16-P-0044-0.65A

Unit: mm









Weight: 0.06 g (typ.)

Note: Lead (Pb)-Free Packages

SOP16-P-300-1.27A SOL16-P-150-1.27 TSSOP16-P-0044-0.65A

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20070701-EN

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