TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC139AP,TC74HC139AF,TC74HC139AFN

Dual 2-to-4 Line Decoder

The TC74HC139A is a high speed CMOS 2-to-4 LINE DECODER/DEMULTIPLEXER fabricated with silicon gate C^2MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The active low enable input can be used for gating or it can be used as a data input for demultiplexing applications.

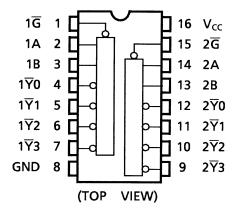
When the enable input is held "H", all four outputs are fixed at a high logic level independent of the other inputs.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

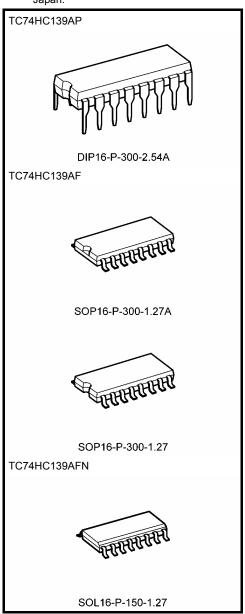
Features

- High speed: $t_{pd} = 16 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu A$ (max) at $T_a = 25$ °C
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2~6 V
- Pin and function compatible with 74LS139

Pin Assignment



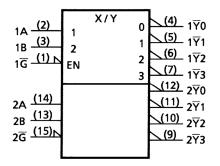
Note: xxxFN (JEDEC SOP) is not available in Japan.

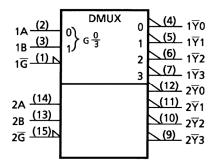


Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOP16-P-300-1.27 : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)

IEC Logic Symbol





Truth Table

Inputs				Out				
Enable	Select		<u></u>			- 73	Selected Output	
G	В	Α	10	Y1 Y2		13		
Н	Х	Х	Н	Н	Н	Н	None	
L	L	L	L	Н	Н	Н	Y 0	
L	L	Н	Н	L	Н	Н	Y 1	
L	Н	L	Н	Н	L	Н	₹2	
L	Н	Н	Н	Н	Н	L	7 3	

X: Don't care

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7	V
DC input voltage	V _{IN}	-0.5~V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65~150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: 500 mW in the range of $Ta = -40^{\circ}C \sim 65^{\circ}C$. From $Ta = 65^{\circ}C$ to $85^{\circ}C$ a derating factor of -10 mW/°C shall be applied until 300 mW.



Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2~6	V
Input voltage	V _{IN}	0~V _{CC}	V
Output voltage	V _{OUT}	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
		0~1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0~500 (V _{CC} = 4.5 V)	ns
		0~400 (V _{CC} = 6.0 V)	

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

		Test Condition		Ta = 25°C			Ta = -40~85°C			
Characteristics Symbol					Min	Тур.	Max	Min	Max	Unit
				2.0	1.50	_	_	1.50	_	
High-level input voltage	V_{IH}		_	4.5	3.15	_	_	3.15	_	V
-				6.0	4.20	_	_	4.20	_	
				2.0	_	_	0.50	_	0.50	
Low-level input voltage	V_{IL}	_		4.5	_	_	1.35	_	1.35	V
Ŭ				6.0	_		1.80	_	1.80	
	V _{ОН}	VIN = VIH or VIL		2.0	1.9	2.0	_	1.9		
			I _{OH} = -20 μA	4.5	4.4	4.5	_	4.4	_	
High-level output voltage				6.0	5.9	6.0	_	5.9	_	V
			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13		
			I _{OH} = -5.2 mA	6.0	5.68	5.80	_	5.63	_	
	V _{OL}	V _{IN} = V _{IH} or V _{IL}		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \mu A$	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage				6.0	_	0.0	0.1	_	0.1	V
			I _{OL} = 4 mA	4.5	_	0.17	0.26	_	0.33	
			I _{OL} = 5.2 mA	6.0	_	0.18	0.26	_	0.33	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	_		±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		6.0	_	_	4.0	_	40.0	μА



AC Characteristics (CL = 15 pF, V_{CC} = 5 V, Ta = 25°C, input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH}			4	8	ns
Output transition time	t_{THL}	_				
Propagation delay time	t _{pLH}			12	22	20
$(A, B-\overline{Y})$	t_{pHL}	_	_	12	22	ns
Propagation delay time	t _{pLH}			40	40	
$(\overline{G} - \overline{Y})$	t _{pHL}	_	_	10	18	ns

AC Characteristics ($C_L = 50 \text{ pF}$, input: $t_r = t_f = 6 \text{ ns}$)

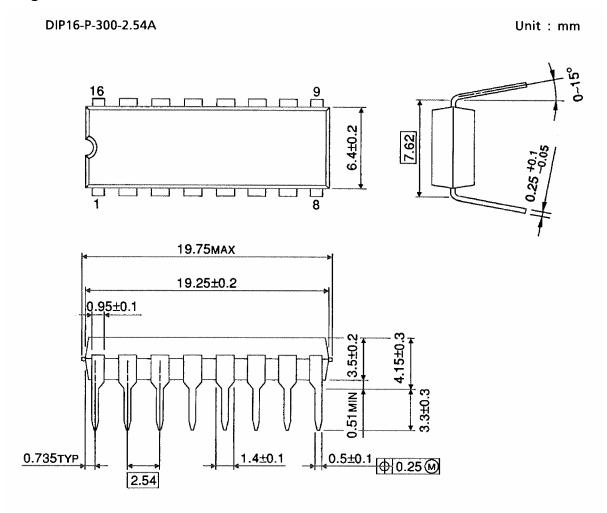
		Test Condition		-	Ta = 25°C		Ta = -4		
Characteristics	Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
	4 —		2.0	_	30	75	_	95	
Output transition time	t _{TLH}	_	4.5	_	8	15	_	19	ns
	t _{THL}		6.0	_	7	13	_	16	
Propagation delay	4		2.0	_	45	130		165	
time	t _{pLH}	_	4.5	_	15	26	_	33	ns
(A, B- \overline{Y})	t _{pHL}		6.0	_	13	22	_	28	
Propagation delay	4		2.0	_	39	110	_	140	
time	t _{pLH}	_	4.5	_	13	22	_	28	ns
(G-Y)	t _{pHL}		6.0	_	11	19	_	24	
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation	C _{PD}	_			46				pF
capacitance	(Note)				70				РΙ

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ (per decoder)

Package Dimensions

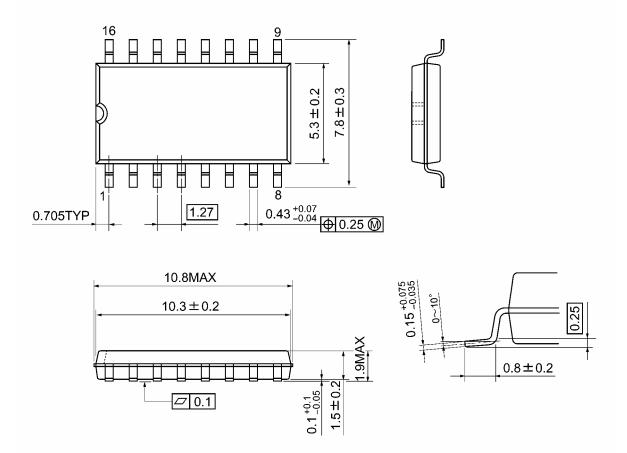


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Weight: 1.00 g (typ.)

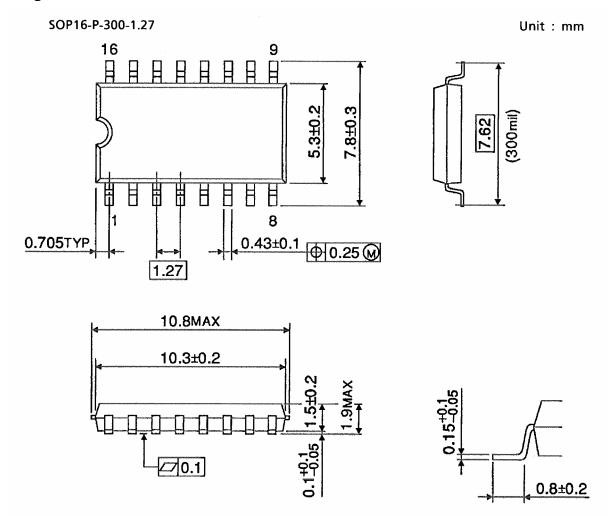
Package Dimensions

SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

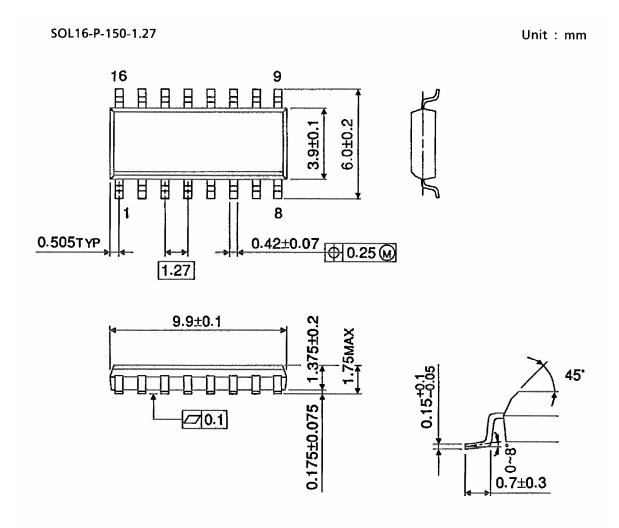
Package Dimensions



Weight: 0.18 g (typ.)



Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

Note: Lead (Pb)-Free Packages

DIP16-P-300-2.54A SOP16-P-300-1.27A SOL16-P-150-1.27

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