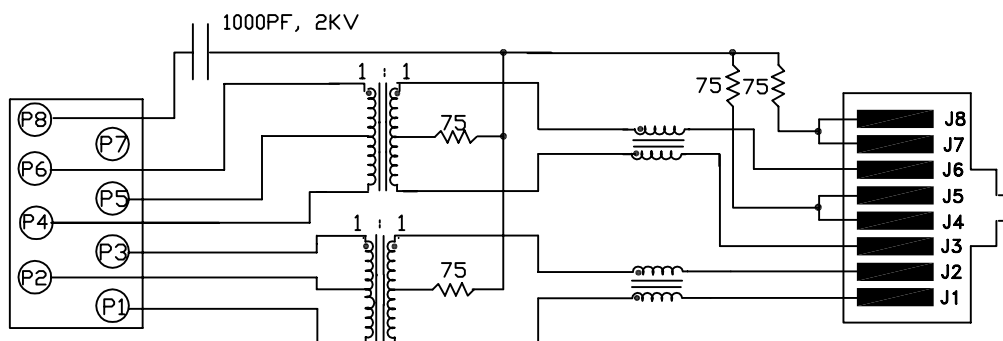
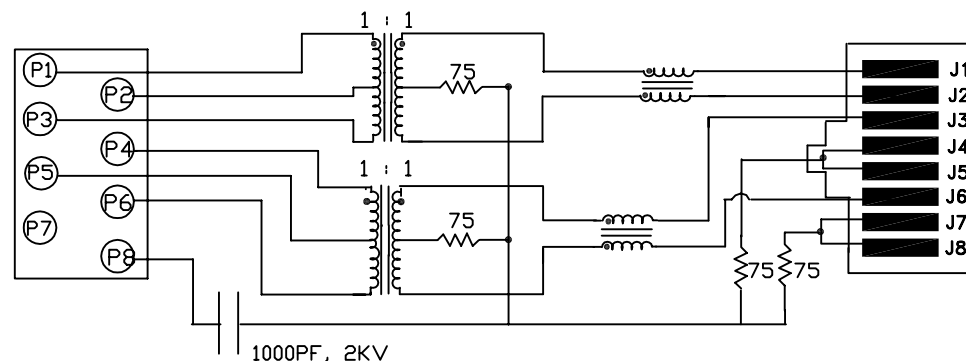


## LOWER ROW



## UPPER ROW



### ELECTRICAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE:

: 0°C TO +70°C

1.0 TURNS RATIO: (P1-P2-P3) : (J1-J2)  
(P4-P5-P6) : (J3-J6)

: 1CT : 1 ± 3%  
: 1CT : 1 ± 3%

2.0 INDUCTANCE: (P6-P4)  
(P3-P1)

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias  
: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT)  
P3-P1 (WITH J2 AND J1 SHORT)

: 0.3uH MAX. @ 1MHz  
: 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P6,P5,P4) TO (J6,J3)  
(P3,P2,P1) TO (J2,J1)

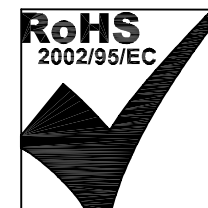
: 30pf TYP @ 1MHz  
: 30pf TYP @ 1MHz

5.0 DC RESISTANCE: (J6-J3)=(J1-J2)  
(P6-P4)  
(P3-P1)

: 1.2 ohms Max.  
: .55 ohms Max.  
: .55 ohms Max.

### NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.



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SHEET  
1 OF 4

DRAWING NO.  
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REV.  
13

RECEIVE

|                                                                                                 |                              |
|-------------------------------------------------------------------------------------------------|------------------------------|
| 6.0 RETURN LOSS: 1MHz TO 30MHz<br>60MHz TO 80MHz                                                | : 18dB MIN.<br>: 12dB MIN.   |
| 7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P2)<br>(J3, J6) TO (P3, P6)                          | : 1500 VAC<br>: 1500 VAC     |
| 8.0 INSERTION LOSS: RS=RL=100 ohms<br>100KHz TO 100MHz                                          | : 1.1 dB TYP                 |
| 9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS<br>OUTPUT VOLTAGE = 1 V peak<br>PULSE WIDTH= 112nS | : 3.0 nS MAX<br>: 3.0 nS MAX |
| 10.0 CROSS TALK: 1MHz TO 100MHz                                                                 | : 40 dB TYP                  |
| 11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz                                          | : 35dB TYP                   |

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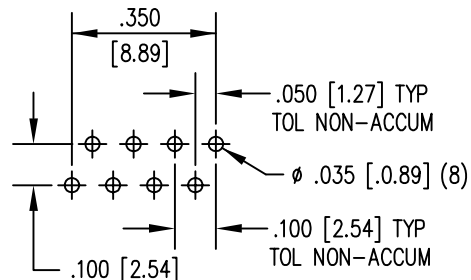
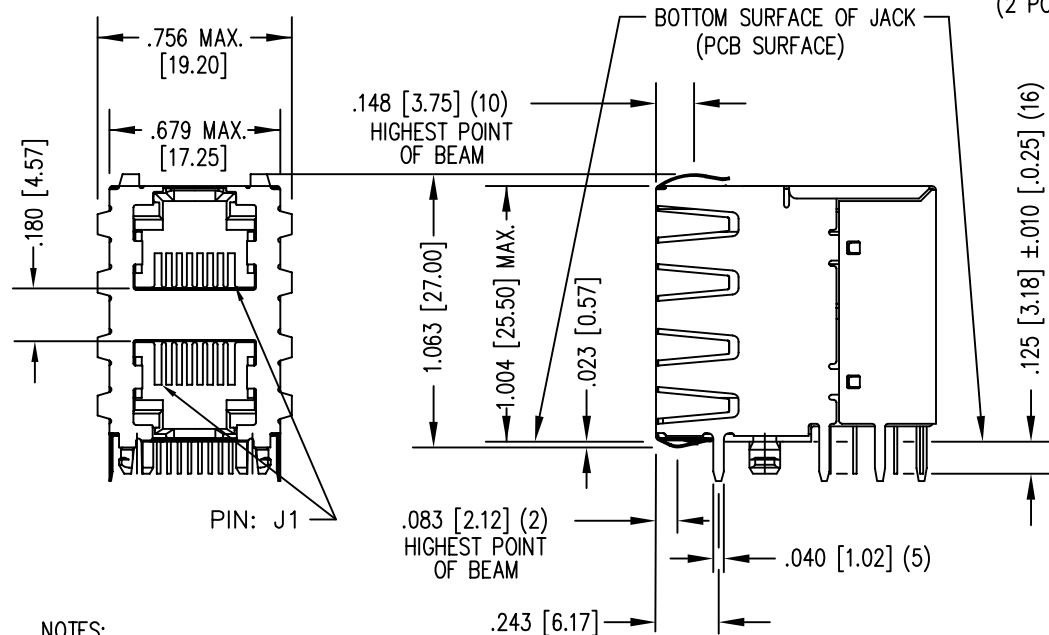
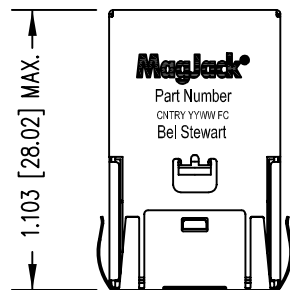
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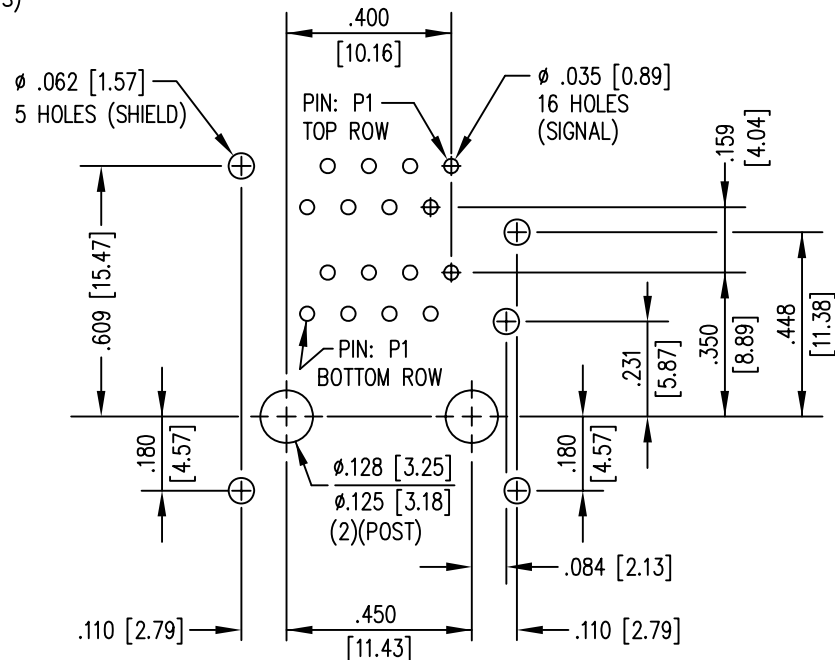
SHEET  
2 OF 4

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SI-30107-F

REV. 13



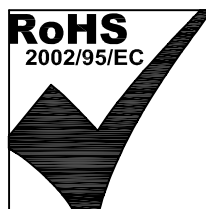
TYPICAL HOLE LAYOUT  
(2 POSITIONS)



P.C.B. RECOMMENDED HOLE LAYOUT  
SEEN FROM COMPONENT SIDE  
TOLERANCE ±.003 [0.08] UNLESS OTHERWISE SPECIFIED

NOTES:

- CONNECTOR MATERIALS:  
 HOUSING: THERMOPLASTIC UL94 V-0  
 CONTACT/SHIELD: COPPER ALLOY  
 SHIELD PLATING: NICKEL OR TIN  
 CONTACT PLATING: SELECTIVE GOLD,  
 50 MICRO-INCHES MIN. IN CONTACT AREA.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.  
 SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- REFLOW AND WAVE SOLDER COMPATIBLE - 260°C FOR  
 10 SECONDS MAX.



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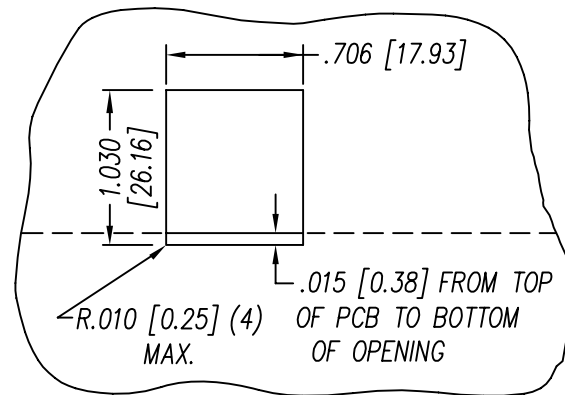
SHEET  
3 OF 4

DRAWING NO.

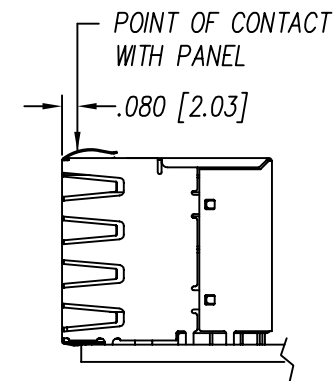
SI-30107-F

REV.

X



SUGGESTED PANEL OPENING  
(N.T.S.)



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE  $\pm .005$  [0.13]

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SHEET  
4 OF 4

DRAWING NO.  
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