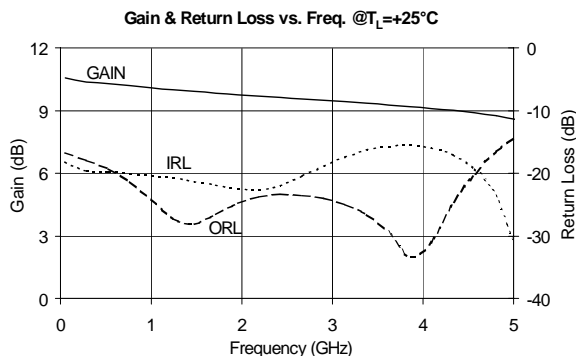




## Product Description

The SGA-2163 is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring 1 micron emitters provides high  $F_T$  and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only 2 DC-blocking capacitors, a bias resistor and an optional RF choke are required for operation.

The matte tin finish on Sirenza's lead-free package utilizes a post annealing process to mitigate tin whisker formation and is RoHS compliant per EU Directive 2002/95. This package is also manufactured with green molding compounds that contain no antimony trioxide nor halogenated fire retardants.



## SGA-2163

**SGA-2163Z**  RoHS Compliant & Green Package

**DC-5000 MHz, Cascadable  
SiGe HBT MMIC Amplifier**



## Product Features

- Now available in Lead Free, RoHS Compliant, & Green Packaging
- Broadband Operation: DC-5000 MHz
- Cascadable 50 Ohm
- Operates From Single Supply
- Low Thermal Resistance Package

## Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Symbol	Parameter	Units	Frequency	Min.	Typ.	Max.
G	Small Signal Gain	dB	850 MHz 1950 MHz 2400 MHz	9.5	10.5 9.8 9.6	11.5
$P_{1dB}$	Output Power at 1dB Compression	dBm	850 MHz 1950 MHz		7.1 6.2	
$OIP_3$	Output Third Order Intercept Point	dBm	850 MHz 1950 MHz		21.0 18.0	
Bandwidth	Determined by Return Loss (>10dB)	MHz			5000	
IRL	Input Return Loss	dB	1950 MHz		22.5	
ORL	Output Return Loss	dB	1950 MHz		24.8	
NF	Noise Figure	dB	1950 MHz		4.4	
$V_D$	Device Operating Voltage	V		1.9	2.2	2.5
$I_D$	Device Operating Current	mA		17	20	23
$R_{TH}$ , j-l	Thermal Resistance (junction to lead)	$^\circ\text{C}/\text{W}$			255	
<b>Test Conditions:</b> $V_S = 5\text{ V}$ $I_D = 20\text{ mA Typ.}$ $OIP_3$ Tone Spacing = 1 MHz, $P_{out}$ per tone = -10 dBm $R_{BIAS} = 140\text{ Ohms}$ $T_L = 25^\circ\text{C}$ $Z_S = Z_L = 50\text{ Ohms}$						

The information provided herein is believed to be reliable at press time. Sirenza Microdevices assumes no responsibility for inaccuracies or omissions. Sirenza Microdevices assumes no responsibility for the use of this information, and all such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Sirenza Microdevices does not authorize or warrant any Sirenza Microdevices product for use in life-support devices and/or systems. Copyright 2001 Sirenza Microdevices, Inc.. All worldwide rights reserved.

**Typical RF Performance at Key Operating Frequencies**

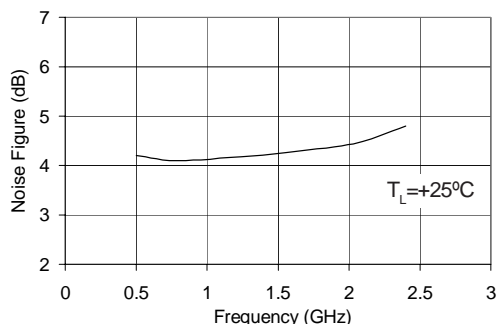
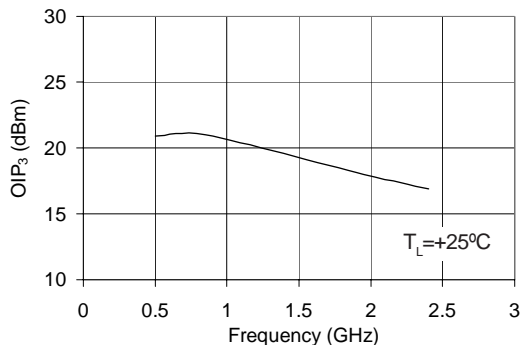
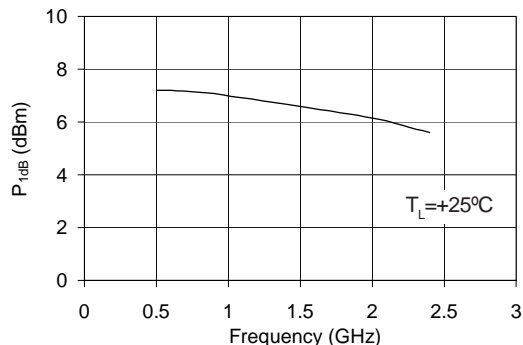
Symbol	Parameter	Unit	Frequency (MHz)					
			100	500	850	1950	2400	3500
G	Small Signal Gain	dB	10.7	10.6	10.5	9.8	9.6	9.3
OIP <sub>3</sub>	Output Third Order Intercept Point	dBm		20.9	21.0	18.0	16.9	
P <sub>1dB</sub>	Output Power at 1dB Compression	dBm		7.2	7.1	6.2	5.6	
IRL	Input Return Loss	dB	18.7	19.8	20.3	22.5	22.1	16.0
ORL	Output Return Loss	dB	17.2	19.1	22.3	24.8	23.4	27.6
S <sub>12</sub>	Reverse Isolation	dB	15.6	15.4	15.5	16.1	16.4	16.9
NF	Noise Figure	dB		4.2	4.1	4.4	4.8	

**Test Conditions:**  $V_S = 5\text{ V}$   $I_D = 20\text{ mA}$  Typ.  $OIP_3$  Tone Spacing = 1 MHz, Pout per tone = -10 dBm  
 $R_{BIAS} = 140\text{ Ohms}$   $T_L = 25^\circ\text{C}$   $Z_S = Z_L = 50\text{ Ohms}$

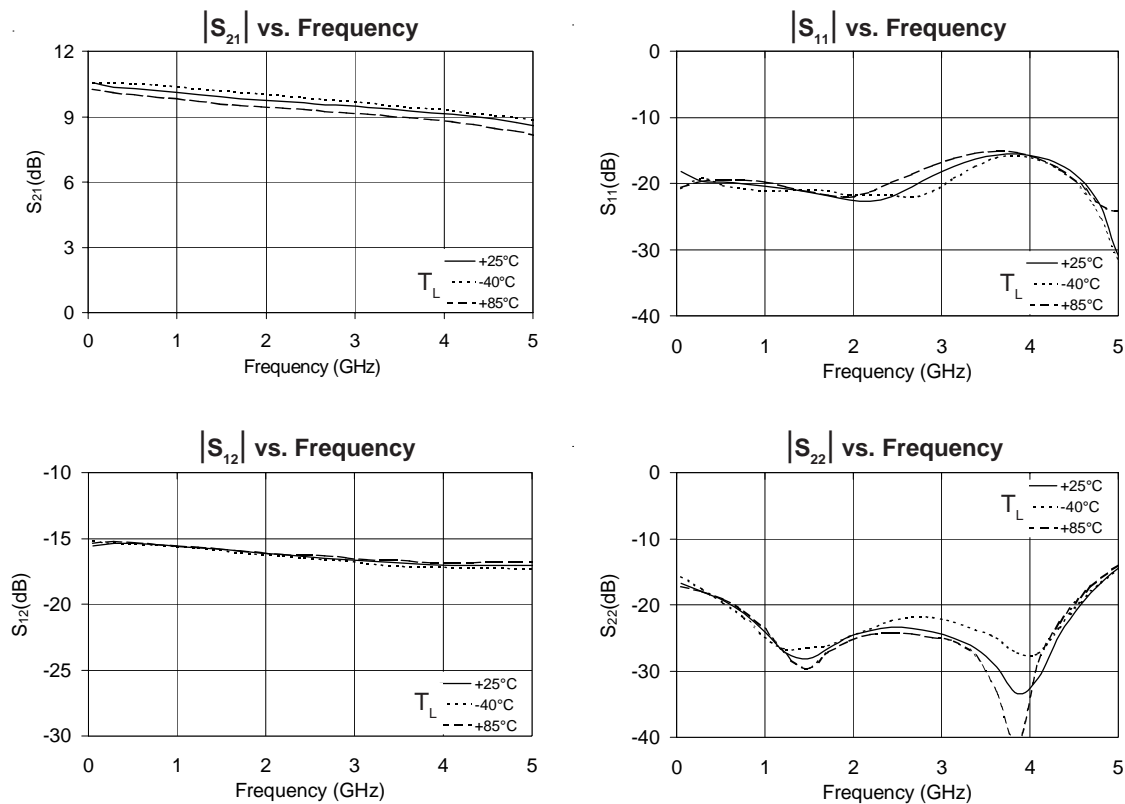
**Absolute Maximum Ratings**

Parameter	Absolute Limit
Max. Device Current ( $I_D$ )	40 mA
Max. Device Voltage ( $V_D$ )	4 V
Max. RF Input Power	+18 dBm
Max. Junction Temp. ( $T_J$ )	+150°C
Operating Temp. Range ( $T_L$ )	-40°C to +85°C
Max. Storage Temp.	+150°C

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.  
 Bias conditions should also satisfy the following expression:  
 $I_D V_D < (T_J - T_L) / R_{TH}$

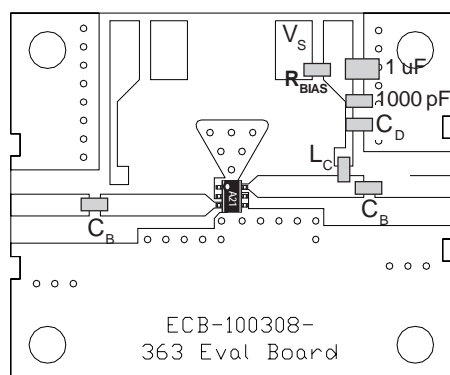
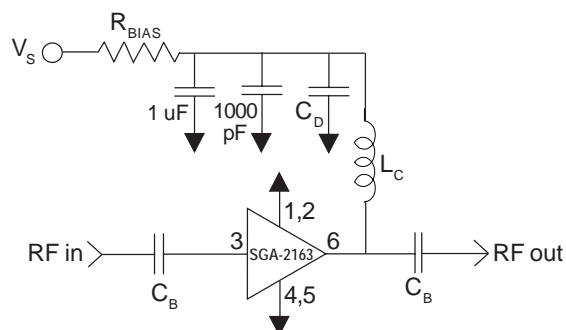
**Noise Figure vs. Frequency**  
 $V_D = 2.2\text{ V}$ ,  $I_D = 20\text{ mA}$ 

**OIP<sub>3</sub> vs. Frequency**  
 $V_D = 2.2\text{ V}$ ,  $I_D = 20\text{ mA}$ 

**P<sub>1dB</sub> vs. Frequency**  
 $V_D = 2.2\text{ V}$ ,  $I_D = 20\text{ mA}$ 


**Typical RF Performance Over Temperature ( Bias:  $V_D = 2.2$  V,  $I_D = 20$  mA (Typ.) )**



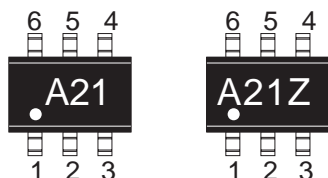
NOTE: Full S-parameter data available at [www.sirenza.com](http://www.sirenza.com)

## Basic Application Circuit



### Part Identification Marking

The part will be marked with an "A21" designator on the top surface of the package.



### Caution: ESD sensitive

Appropriate precautions in handling, packaging and testing devices must be observed.

### Application Circuit Element Values

Reference Designator	Frequency (Mhz)				
	500	850	1950	2400	3500
C <sub>B</sub>	220 pF	100 pF	68 pF	56 pF	39 pF
C <sub>D</sub>	100 pF	68 pF	22 pF	22 pF	15 pF
L <sub>C</sub>	68 nH	33 nH	22 nH	18 nH	15 nH

### Recommended Bias Resistor Values for I<sub>D</sub>=20mA

$$R_{BIAS} = (V_S - V_D) / I_D$$

Supply Voltage(V <sub>S</sub> )	5 V	6 V	8 V	10 V
R <sub>BIAS</sub>	140 Ω	200 Ω	300 Ω	390 Ω

Note: R<sub>BIAS</sub> provides DC bias stability over temperature.

### Mounting Instructions

1. Use a large ground pad area near device pins 1, 2, 4, and 5 with many plated through-holes as shown.
2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

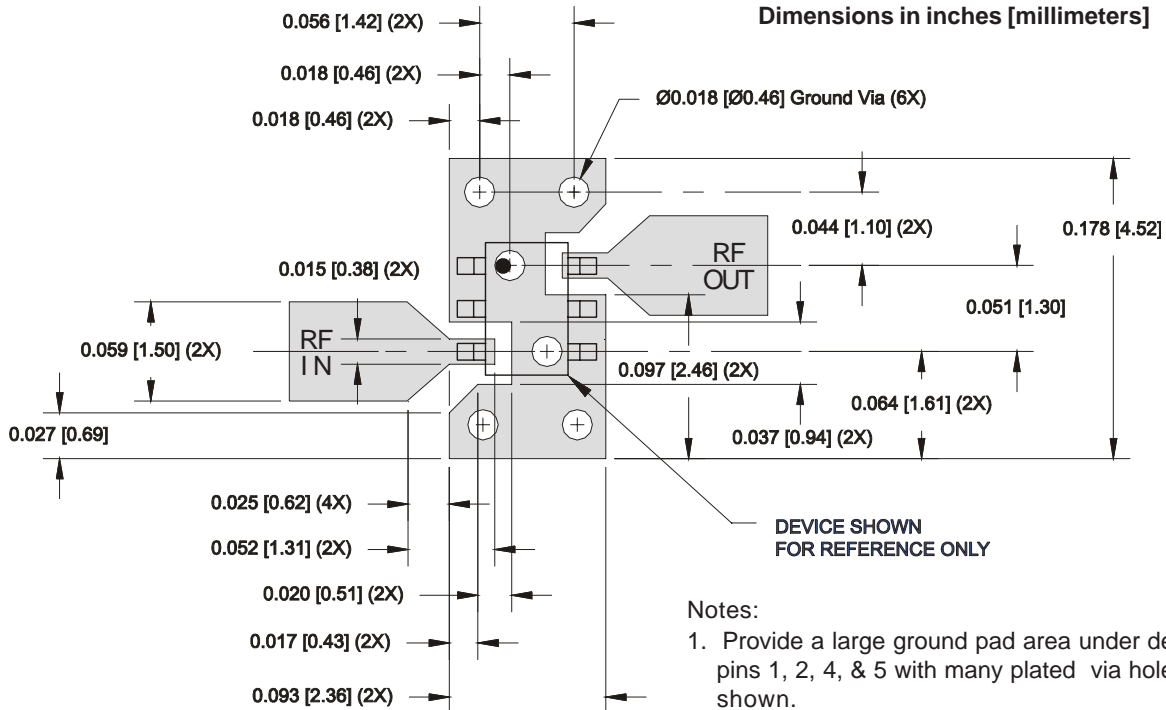
Pin #	Function	Description
3	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
1, 2, 4, 5	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.
6	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

### Part Number Ordering Information

Part Number	Reel Size	Devices/Reel
SGA-2163	7"	3000
SGA-2163Z	7"	3000

### SOT-363 PCB Pad Layout

Dimensions in inches [millimeters]



#### Notes:

1. Provide a large ground pad area under device pins 1, 2, 4, & 5 with many plated via holes as shown.
2. Dimensions given for 50 Ohm RF I/O lines are for 31 mil thick Getek. Scale accordingly for different board thicknesses and dielectric constants.
3. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick Getek with 1 ounce copper on both sides.

### SOT-363 Nominal Package Dimensions

Dimensions in inches [millimeters]

A link to the SOT-363 package outline drawing with full dimensions and tolerances may be found on the product web page at [www.sirenza.com](http://www.sirenza.com).

