





#### 0.2A SBR<sup>®</sup> SURFACE MOUNT SUPER BARRIER RECTIFIER

#### **Features**

- Ultra Low Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability



- Case: DFN1006-2
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: Cathode Dot
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.001 grams



Bottom View



Top View

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# **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	$V_{RRM}$		
Working Peak Reverse Voltage	$V_{RWM}$	100	V
DC Blocking Voltage	$V_{RM}$		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	70	V
Average Rectified Output Current (See Figure 1)	Io	250	mA
Non-Repetitive Peak Forward Surge Current 8.3ms	l=a	E	۸
Single Half Sine-Wave Superimposed on Rated Load	IFSM	5	А

### Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance			
Thermal Resistance, Junction to Ambient (Note 2) T <sub>A</sub> = 25°C	$R_{\theta JA}$	270	°C/W
Thermal Resistance, Junction to Ambient (Note 3) T <sub>A</sub> = 25°C	$R_{\theta JA}$	235	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

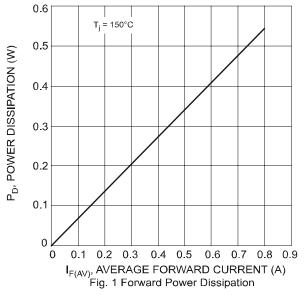
### **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

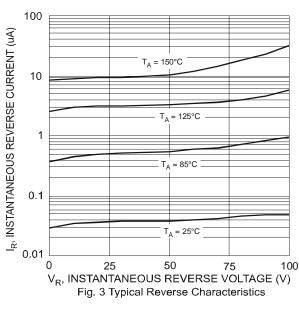
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 4)	V <sub>(BR)R</sub>	100	-	=	V	I <sub>R</sub> = 1mA
Forward Voltage Drop	V <sub>F</sub>	-	0.67 0.76 0.60	0.72 0.80 0.65	V	I <sub>F</sub> = 100mA, T <sub>J</sub> = 25°C I <sub>F</sub> = 200mA, T <sub>J</sub> = 25°C I <sub>F</sub> = 200mA, T <sub>J</sub> = 125°C
Leakage Current (Note 4)	I <sub>R</sub>	-	0.04 6	1.0 50	I IIA	$V_R = 75V, T_J = 25^{\circ}C$ $V_R = 75V, T_J = 85^{\circ}C$

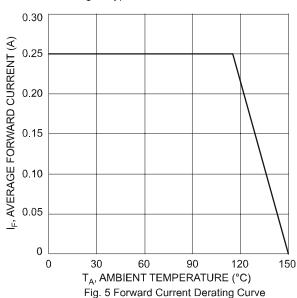
Notes

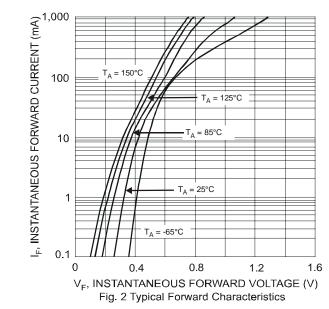
- 1. RoHS revision 13.2.2003. High temperature solder exemption applied, see *EU Directive Annex Note* 7.
- 2. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf
- 3. Polyimide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf
- 4. Short duration pulse test used to minimize self-heating effect.

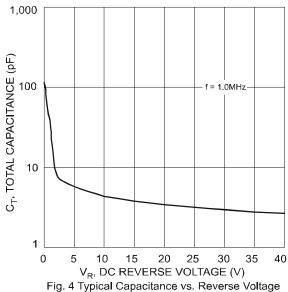


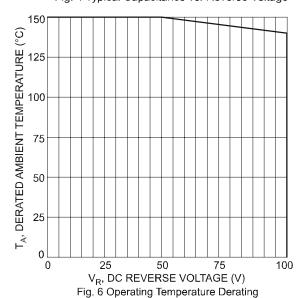












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#### Ordering Information (Note 5)

Part Number	Case	Packaging
SBR02U100LP-7	DFN1006-2	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

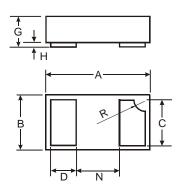
## **Marking Information**





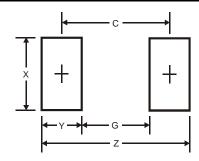
 $\underline{2}$ A,  $2\underline{A}$  = Product Type Marking Code Dot Denotes Cathode Side

### **Package Outline Dimensions**



DFN1006-2					
Dim	Min	Max	Тур		
Α	0.95	1.075	1.00		
<b>B</b> 0.55 0.		0.675	0.60		
<b>C</b> 0.45		0.55	0.50		
D	0.20	0.30	0.25		
G	0.47	0.53	0.50		
<b>H</b> 0		0.05	0.03		
N — — 0.4					
R	0.05	0.15	0.10		
All [	All Dimensions in mm				

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	1.1
G	0.3
X	0.7
Y	0.4
С	0.7

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