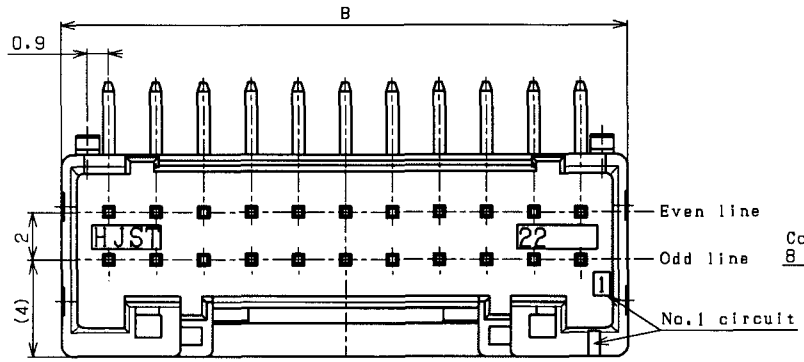
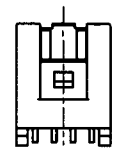
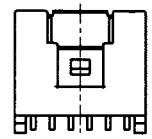
REV.	DESCRIPTION	DATE	DESIGNED
△			





Configuration for 8 to 10 circuits.

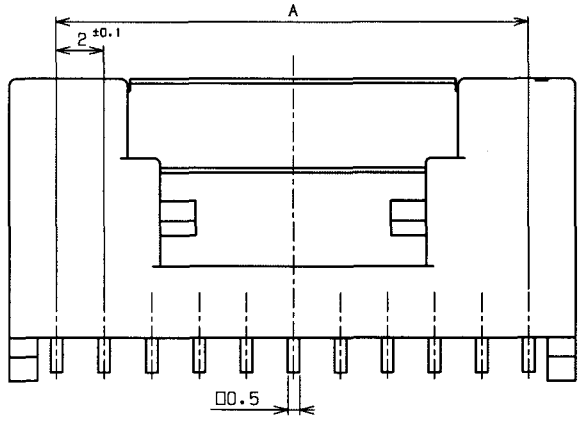


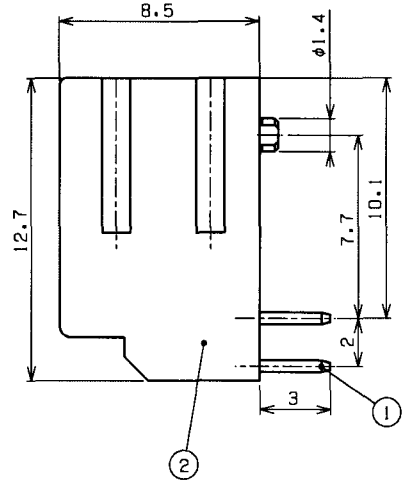
Configuration for 12 to 18 circuits.

NOTE

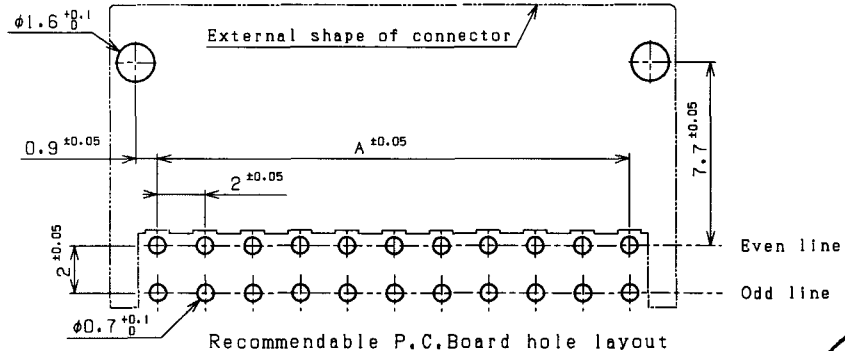
1. Unless otherwise specified,
   tolerances are $0<L\leq 5.0: \pm 0.3$
   $5.0<L \quad : \pm 0.4$
2. Part No.: S( )B-PUD( )S-1 (LF) (SN)
   Circuits / Color code

Color	Part No.
Natural (Ivory)	S( )B-PUDSS-1 (LF) (SN)
Black	S( )B-PUDKS-1 (LF) (SN)





Cir-cuits	Dimensions A	Dimensions B
8	6.0	10.0
10	8.0	12.0
12	10.0	14.0
14	12.0	16.0
16	14.0	18.0
18	16.0	20.0
20	18.0	22.0
22	20.0	24.0
24	22.0	26.0
26	24.0	28.0
28	26.0	30.0
30	28.0	32.0



Recommendable P.C.Board hole layout
(Connector mounted side)

No.	PART NAME	MATERIAL	SURFACE FINISH	REMARKS
2	WAFER	66NYLON(G.F.)		UL94V-0
1	POST	COPPER ALLOY	COPPER-UNDERPLATED TIN-PLATED	

SIZE	UNIT	SCALE	PROJECTION	DATE
A3	METRIC	5:1		MAR.14.2008

APPROVED	CHECKED	DESIGNED	DRAWN
S.K	E.N	M.I	

CUSTOMER

SERIES NAME	PUD CONNECTOR SIDE TYPE (LEAD-FREE PRODUCT)
PART No.	S( )B-PUD( )S-1 (LF) (SN)
DRAWING No.	KRD-42384-1 RO

J.S.T.MFG.CO., LTD.

JST CORP. JUL 28 2008 WEC

THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO J.S.T. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION.

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