4V Drive Nch MOS FET

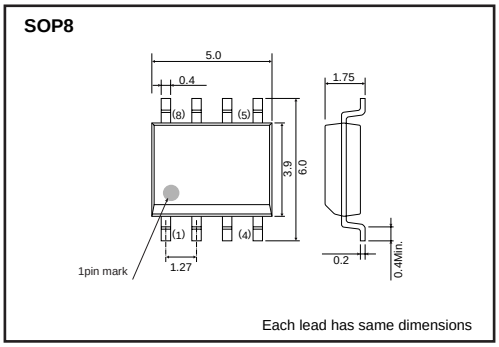
RSS130N03

●Structure
Silicon N-channel MOS FET

●Features
1) Low on-resistance.
2) Built-in G-S Protection Diode.
3) Small Surface Mount Package (SOP8).

●Application
Power switching, DC/DC converter.

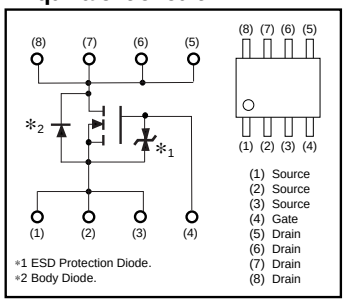
●External dimensions (Unit : mm)



●Packaging specifications

Type	Package	Taping
	Code	TB
	Basic ordering unit (pieces)	2500
RSS130N03		○

●Equivalent circuit



∗ A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use.Use a protection circuit when the fixed voltage are exceeded.

●Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	20	V
Drain Current	Continuous	I_D	±13	A
	Pulsed	I_{DP} *1	±52	A
Source Current (Body Diode)	Continuous	I_S	1.6	A
	Pulsed	I_{SP} *1	6.4	A
Total Power Dissipation		P_D *2	2	W
Channel Temperature		Tch	150	°C
Storage Temperature		Tstg	−55 to +150	°C

∗1 Pw≤10µs, Duty cycle≤1%
∗2 Mounted on a ceramic board.

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	62.5	°C / W

∗ Mounted on a ceramic board.

●**Electrical characteristics** (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Gate-Source Leakage	I_{GSS}	–	–	10	μA	V_{GS}=20V, V_{DS}=0V
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	30	–	–	V	I_D=1mA, V_{GS}=0V
Zero Gate Voltage Drain Current	I_{DSS}	–	–	1	μA	V_{DS}=30V, V_{GS}=0V
Gate Threshold Voltage	$V_{GS\ (th)}$	1.0	–	2.5	V	V_{DS}=10V, I_D=1mA
Static Drain-Source On-State Resistance	$R_{DS\ (on)}$*	–	5.9	8.3	mΩ	I_D=13A, V_{GS}=10V
		–	7.4	10.4		I_D=13A, V_{GS}=4.5V
		–	7.9	11.1		I_D=13A, V_{GS}=4V
Forward Transfer Admittance	$\|Y_{fs}\|$*	11	–	–	S	I_D=13A, V_{DS}=10V
Input Capacitance	C_{iss}	–	2000	–	pF	V_{DS}=10V
Output Capacitance	C_{oss}	–	605	–	pF	V_{GS}=0V
Reverse Transfer Capacitance	C_{rss}	–	320	–	pF	f=1MHz
Turn-On Delay Time	$t_{d(on)}$ *	–	13	–	ns	I_D=6.5A, V_{DD}≒ 15V
Rise Time	t_r *	–	30	–	ns	V_{GS}=10V
Turn-Off Delay Time	$t_{d(off)}$ *	–	88	–	ns	R_L=2.31Ω
Fall Time	t_f *	–	55	–	ns	R_G=10Ω
Total Gate Charge	Q_g *	–	25	35	nC	V_{DD} ≒ 15V
Gate-Source Charge	Q_{gs} *	–	4.7	–	nC	V_{GS}=5V
Gate-Drain Charge	Q_{gd} *	–	9.4	–	nC	I_D=13A

*Pulsed

●**Body diode characteristics** (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward Voltage	V_{SD} *	–	–	1.2	V	I_S=6.4A, V_{GS}=0V

*Pulsed

●**Electrical characteristic curves**

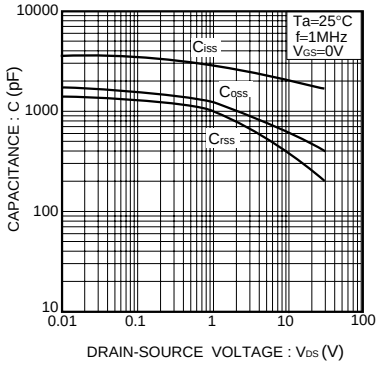


Fig.1 Typical Capacitance vs. Drain-Source Voltage

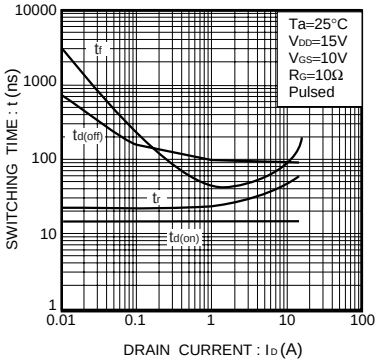


Fig.2 Switching Characteristics

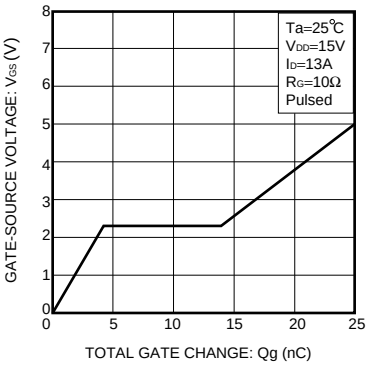


Fig.3 Dynamic Input Characteristics

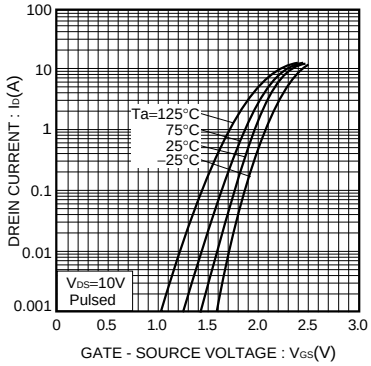


Fig.4 Typical Transfer Characteristics

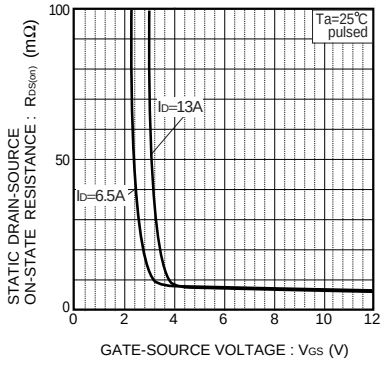


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

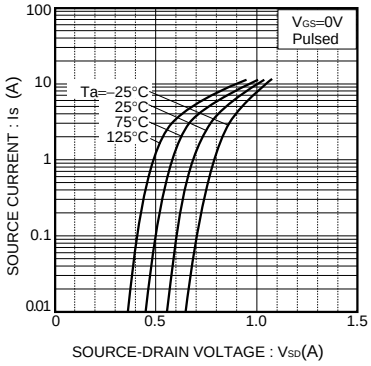


Fig.6 Source-Current vs. Source-Drain Voltage

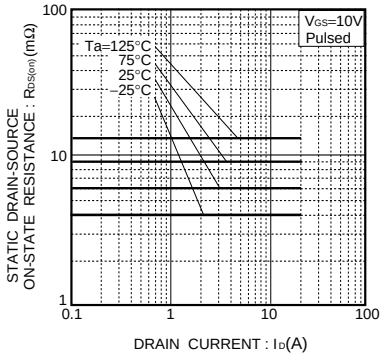


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (1)

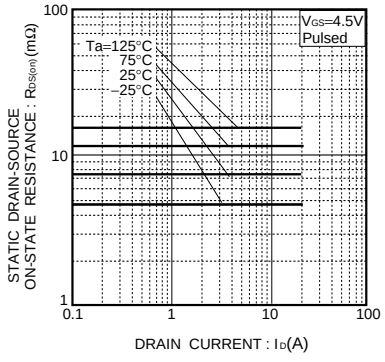


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (2)

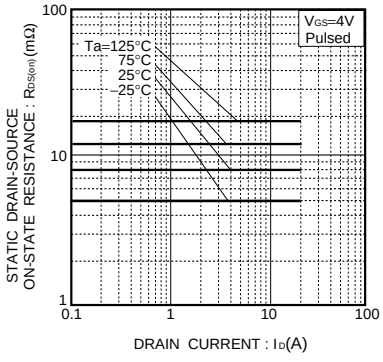


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (3)

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