4V Drive Pch MOS FET

RSQ025P03

Structure

Silicon P-channel MOS FET

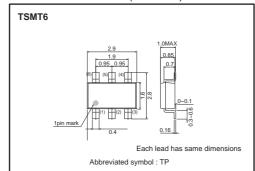
Features

- 1) Low On-resistance.(120m Ω at 4.5V)
- 2) High Power Package.(PD=1.25W)
- 3) High speed switching.
- 4) Low voltage drive. (4V)

Applications

DC-DC converter

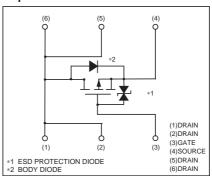
●External dimensions (Unit : mm)



Packaging specifications

Туре	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RSQ025P03	0	

●Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		Voss	-30	V	
Gate-source voltage		Vgss	±20	V	
Drain current	Continuous	ΙD	±2.5	A	
	Pulsed	IDP *1	±10	A	
Source current (Body diode)	Continuous	Is	-1	A	
	Pulsed	Isp *1	-4	A	
Total power dissipation		Po *2	1.25	W	
Channel temperature		Tch	150	°C	
Range of Storage temperature		Tstg	-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

●Thermal resistance

Parameter Symbol Limits Unit Channel to ambient Rth(ch-a)* 100 °C / W

^{*} Mounted on a ceramic board.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	-	_	±10	μΑ	Vgs=±20V, Vds=0V	
Drain-source breakdown voltage	V(BR)DSS	-30	_	_	V	I _D =-1mA, V _G S=0V	
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	V _{DS} =-30V, V _{GS} =0V	
Gate threshold voltage	VGS(th)	-1.0	_	-2.5	V	VDS=-10V, ID=-1mA	
Static drain-source on-state resistance		-	80	110	mΩ	In=-2.5A, Vgs=-10V	
	RDS(on)*	_	120	165	mΩ	ID=-2.5A, Vgs=-4.5V	
		_	145	200	mΩ	In=-1.25A, Vgs=-4.0V	
Foward transfer admittance	Y _{fs} *	1.2	_	_	S	VDS=-10V, ID=-1.25A	
Input capacitance	Ciss	_	320	_	pF	V _{DS} =-10V,V _{GS} =0V f=1MHz	
Output capacitance	Coss	_	85	_	pF		
Reverse transfer capacitance	Crss	_	60	_	pF		
Turn-on delay time	td(on) *	_	8	_	ns	ID=-1.25A VDD≔-15V VGS=-10V RI=12Ω	
Rise time	tr *	_	11	_	ns		
Turn-off delay time	td(off) *	_	33	_	ns		
Fall time	t _f *	-	7	-	ns	R _G =10Ω	
Total gate charge	Qg	-	4.4	_	nC	V . 45V	
Gate-source charge	Qgs	-	1.0	_	nC	VDD≔-15V VGS=-5V ID=-2.5A	
Gate-drain charge	Qgd	-	1.4	-	nC		

^{*}PULSED

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp	-	-	-1.2	V	Is=-0.9A, Vgs=0V

•Electrical characteristic curves

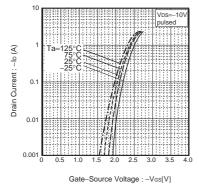
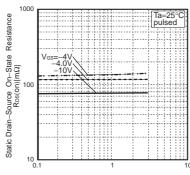


Fig.1 Typical Transfer Characteristics



Prig.2 Static Drain—Source On—State Resistance vs.Drain Current

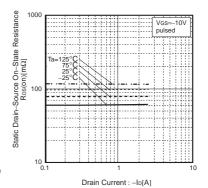


Fig.3 Static Drain–Source On–State Resistance vs.Drain Current

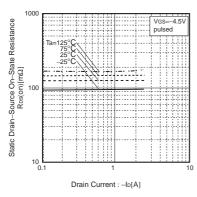


Fig.4 Static Drain–Source On–State Resistance vs.Drain–Current

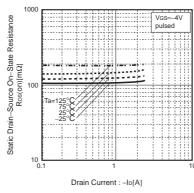


Fig.5 Static Drain–Source On–State Resistance vs.Drain–Current

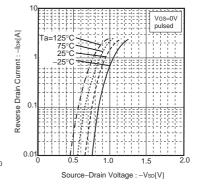


Fig.6 Reverse Drain Current Source-Drain Voltage

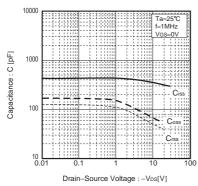


Fig.7 Typical Capactitance vs.Drain–Source Voltage

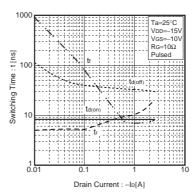
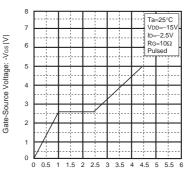


Fig.8 Switching Characteristics



Total Gate Charge : Qg[nC]
Fig.9 Dynamic Input Characteristics

Measurement circuits

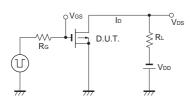


Fig.10 Switching Time Measurement Circuit

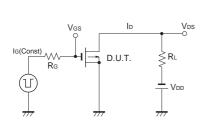


Fig.12 Gate Charge Measurement Circuit

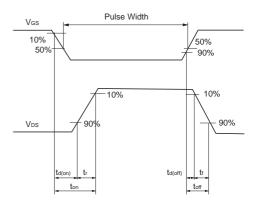


Fig.11 Switching Waveforms

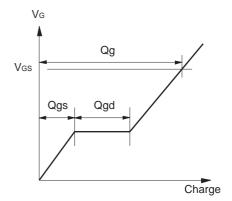


Fig.13 Gate Charge Waveforms

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