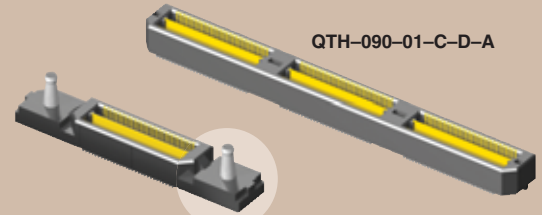


QTH-060-07-F-D-A



QTH-090-01-C-D-A

QTH-030-01-L-D-A-RT1

0,50mm HI-SPEED HEADER QTH SERIES

SPECIFICATIONS

For complete specifications and recommended PCB layouts see www.samtec.com?QTH

Insulator Material: Liquid Crystal Polymer



Terminal Material: Phosphor Bronze

Plating:

Au over 50μ" (1,27μm) Ni

Current Rating:

Contact: 1.0A @ 30°C

Temperature Rise

Ground Plane: 7.8A @ 30°C

Temperature Rise

Operating Temp Range:

-55°C to +125°C

Voltage Rating:

125 VAC (5mm Stack Height)

Max Cycles:

100

Unmating Force (-RT1 option):

-RT1 option increases unmating force up to 50%

RoHS Compliant:

Yes

Processing:

Max Processing Temp:

230°C for 60 seconds, or

260°C for 20 seconds 3x

Lead-Free Solderable:

Yes

SMT Lead Coplanarity:

(0,10mm) .004" max (030-060)

(0,15mm) .006" max (090-120)

Board Stacking:

For applications requiring

more than two connectors

per board or 4 banks or more,

contact ipg@samtec.com

APPLICATION SPECIFIC OPTION

- 14mm, 15mm, 22mm and 30mm stack height (Caution: Some automatic placement/inspection machines may have component height restrictions. Please consult machinery specifications.)
 - 30μ" (0,76μm) Gold (Specify -H plating for Data Rate cable mating applications.)
 - Edge Mount & Guide Posts
 - 150 positions per row
- Call Samtec.

***Note:** -C Plating passes 10 year MFG testing

Note: Some lengths, styles and options are non-standard, non-returnable.

Board Mates: QSH

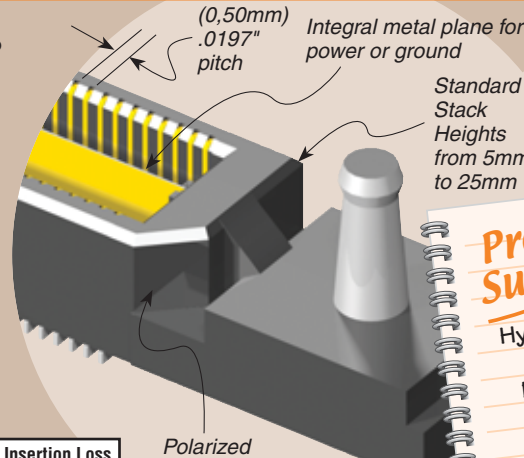


Cable Mates: HFHM2, HQCD, HQDP (See Application Specific note)



5mm Stack Height	Type	Rated @ -3dB Insertion Loss
Single-Ended Signaling	-D	9 GHz / 18 Gbps
Differential Pair Signaling	-D	8 GHz / 16 Gbps
Differential Pair Signaling	-DP	9.5 GHz / 19 Gbps

Performance data for other stack heights available at www.samtec.com?QTH



ALSO AVAILABLE

Board Spacing Standoffs. See SO Series.



Protocols Supported

Hypertransport

XAUI

PCI Express®

SATA

Infiniband

Download app notes at www.samtec.com/appnote
Contact SIG @ samtec.com for questions on protocols

QTH	PINS PER ROW NO. OF PAIRS	LEAD STYLE	PLATING OPTION	TYPE	A	OTHER OPTION																					
	<p>-030, -060, -090, -120 (60 total pins per bank = -D)</p> <p>-020, -040, -060, -080 (20 pairs per bank = -D-DP)</p>	Specify LEAD STYLE from chart	<p>-L = 10μ" (0,25μm) Gold on Signal Pins and Ground Plane, Matte Tin on tails</p> <p>-F = Gold Flash on Signal Pins and Ground Plane, Matte Tin on tails</p> <p>-C* = Electro-Polished Selective 50μ" (1,27μm) min Au over 150μ" (3,81μm) Ni on Signal Pins in contact area, 10μ" (0,25μm) min Au over 50μ" (1,27μm) Ni on Ground Plane in contact area, Matte Tin over 50μ" (1,27μm) min Ni on all solder tails</p>	<p>-D = Single-Ended</p> <p>-D-DP = Differential Pair (-01 only)</p>	<p>-K = (7,00mm) .275" DIA Polyimide film Pick & Place Pad</p> <p>-TR = Tape & Reel (-090 positions maximum)</p> <p>-RT1 = Retention Option (-01 Lead Style only & -090 positions maximum)</p>																						
<p>Dimensions:</p> <p>-D = (No. of Pins per Row/30) x (20,00) .7875</p> <p>-DP = (No. of Pairs per Row/20) x (20,00) .7875</p> <p>(20,00) .7875</p> <p>(7,11) .280</p> <p>(0,50) .0197</p> <p>(0,20) .008</p> <p>(0,76) .030</p> <p>(3,30) .130</p> <p>(1,57) .062</p> <p>(0,89) .035 DIA</p> <p>(0,89) .035 DIA</p> <p>(1,57) .062 DIA</p> <p>-01 & -02</p> <p>-03 thru -07</p> <p>-01</p> <p>-RT1</p>																											
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Due to technical progress, all designs, specifications and components are subject to change without notice.

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