

HIGH CMR, 10 Mbps OPEN COLLECTOR OUTPUT TYPE
8-PIN SSOP (SO-8)
HIGH-SPEED PHOTOCOUPLER

-NEPOC Series-

DESCRIPTION

The PS9817A-1 and PS9817A-2 are active-low type high-speed photocouplers that use a GaAlAs light-emitting diode on the input side and a photodetector IC that includes a photodiode and a signal processor on the same chip on the output side.

The PS9817A-1 and PS9817A-2 are designed specifically for high common mode transient immunity (CMR) and low pulse width distortion. The PS9817A-2 is suitable for high density applications.

FEATURES

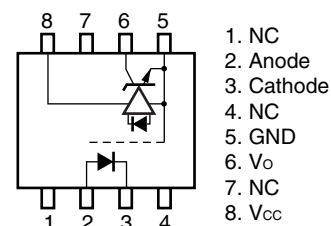
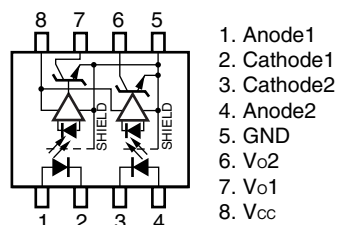
- Pulse width distortion ($|t_{PHL}-t_{PLH}| = 35 \text{ ns MAX.}$)
- High common mode transient immunity ($CM_H, CM_L = \pm 15 \text{ kV}/\mu\text{s MIN.}$)
- 40% reduction of mounting area (5-pin SOP \times 2)
- High-speed (10 Mbps)
- High isolation voltage ($BV = 2\,500 \text{ Vr.m.s.}$)
- Open collector output
- Ordering number of tape product : PS9817A-1-F3: 1 500 pcs/reel
: PS9817A-2-F3: 1 500 pcs/reel
- Pb-Free product
- Safety standards
 - UL approved: File No. E72422
 - DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40008347 (Option)

<R>

APPLICATIONS

- Measurement equipment
- PDP
- FA Network

PIN CONNECTION
(Top View)

PS9817A-1**PS9817A-2****TRUTH TABLE**

LED	Output
ON	L
OFF	H

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Technical drawing of a 12-pin connector. The top view shows a rectangular body with a width of 5.21 ± 0.25 and a height of 3.27 ± 0.2 . It features 12 pins arranged in two rows of 6. The pins are numbered 1 through 12. The side view shows a trapezoidal profile with a top width of 6.0 ± 0.2 and a bottom width of 3.95 ± 0.1 . The height of the side view is 0.15 ± 0.05 . The cross-sectional view shows a trapezoidal profile with a top width of 0.4 ± 0.10 and a bottom width of 0.25 ± 0.05 . The height of the cross-section is 0.1 ± 0.1 . The drawing includes a detail view of the pin base with a diameter of 0.25 ± 0.05 and a length of 1.27 . A note indicates a tolerance of 0.10 S for the side view.

PS9817A-1

9817A-1
NL731

← Assembly Lot

(N)

No. 1 pin Mark
Initial of NEC
(Engraved mark)

N L 7 31

Rank Code

In-house Code
(L: Pb-Free)

Year Assembled
(Last 1 Digit)

PS9817A-2

9817A-2
NL731

← Assembly Lot

(N)

No. 1 pin Mark
Initial of NEC
(Engraved mark)

N L 7 31

Rank Code

In-house Code
(L: Pb-Free)

Year Assembled
(Last 1 Digit)

ORDERING INFORMATION

Part Number	Order Number	Solder plating specification	Packing Style	Safety Standards Approval	Application Part Number ^{*1}
PS9817A-1	PS9817A-1-A	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products (UL approved)	PS9817A-1
PS9817A-1-F3	PS9817A-1-F3-A		Embossed Tape 1 500 pcs/reel		
PS9817A-2	PS9817A-2-A		20 pcs (Tape 20 pcs cut)		PS9817A-2
PS9817A-2-F3	PS9817A-2-F3-A		Embossed Tape 1 500 pcs/reel		
PS9817A-1-V	PS9817A-1-V-A		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	PS9817A-1
PS9817A-1-V-F3	PS9817A-1-V-F3-A		Embossed Tape 1 500 pcs/reel	(VDE0884 Part2)	
PS9817A-2-V	PS9817A-2-V-A		20 pcs (Tape 20 pcs cut)	approved	PS9817A-2
PS9817A-2-V-F3	PS9817A-2-V-F3-A		Embossed Tape 1 500 pcs/reel	(Option)	

*1 For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings		Unit
			PS9817A-1	PS9817A-2	
Diode	Forward Current	I _F	20 ^{*1}	15 ^{*2}	mA/ch
	Reverse Voltage	V _R	5		V/ch
Detector	Supply Voltage	V _{CC}	7		V
	Output Voltage	V _O	7		V/ch
	Output Current	I _O	25		mA/ch
	Power Dissipation ^{*3}	P _C	40		mW/ch
Isolation Voltage ^{*4}		BV	2 500		V _{r.m.s.}
Operating Ambient Temperature		T _A	-40 to +85		°C
Storage Temperature		T _{stg}	-55 to +125		°C

*1 Reduced to 0.3 mA/°C at T_A = 60°C or more.

*2 Reduced to 0.1 mA/°C at T_A = 60°C or more.

*3 Applies to output pin V_O (collector pin). Reduced to 1.5 mW/°C at T_A = 65°C or more.

*4 AC voltage for 1 minute at T_A = 25°C, RH = 60% between input and output.

Pins 1-4 shorted together, 5-8 shorted together.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage	V _{FL}	0		0.8	V
High Level Input Current	I _{FH}	6.3	10	12.5	mA
Supply Voltage	V _{CC}	4.5		5.5	V
Pull-up Resistance	R _L	330		4 k	Ω
TLL (R _L = 1.0 kΩ, loads)	N			5	

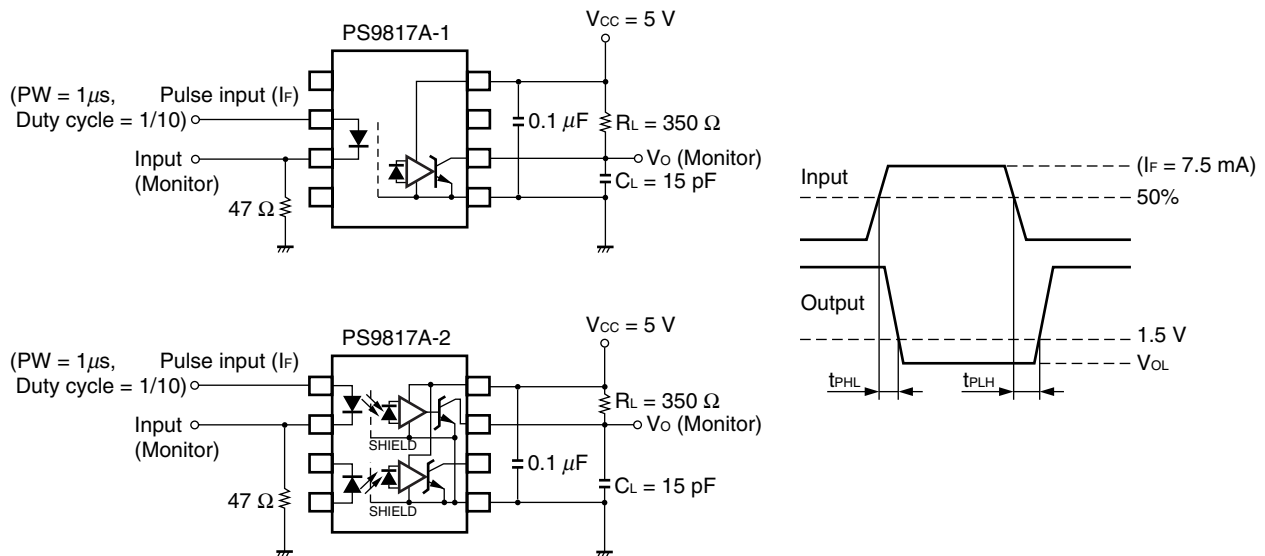
ELECTRICAL CHARACTERISTICS ($T_A = -40$ to $+85^\circ\text{C}$, unless otherwise specified)

Parameter		Symbol	Conditions	MIN.	TYP. *1	MAX.	Unit
Diode	Forward Voltage	V_F	$I_F = 10\text{ mA}$, $T_A = 25^\circ\text{C}$	1.4	1.65	1.8	V
	Reverse Current	I_R	$V_R = 3.0\text{ V}$, $T_A = 25^\circ\text{C}$			10	μA
	Terminal Capacitance	C_i	$V_F = 0\text{ V}$, $f = 1\text{ MHz}$, $T_A = 25^\circ\text{C}$		30		pF
Detector	High Level Output Current	I_{OH}	$V_{CC} = V_O = 5.5\text{ V}$, $V_F = 0.8\text{ V}$		1	100	μA
	Low Level Output Voltage *2	V_{OL}	$V_{CC} = 5.5\text{ V}$, $I_F = 5.0\text{ mA}$, $I_{OL} = 13\text{ mA}$		0.2	0.6	V
	High Level Supply Current (PS9817A-1)	I_{CCH}	$V_{CC} = 5.5\text{ V}$, $I_F = 0\text{ mA}$, $V_O = \text{open}$		4	7	mA
	High Level Supply Current (PS9817A-2)				8	14	
	Low Level Supply Current (PS9817A-1)	I_{CCL}	$V_{CC} = 5.5\text{ V}$, $I_F = 10\text{ mA}$, $V_O = \text{open}$		6	10	
	Low Level Supply Current (PS9817A-2)				12	20	
Coupled	Threshold Input Current ($H \rightarrow L$)	I_{FHL}	$V_{CC} = 5.0\text{ V}$, $V_O = 0.8\text{ V}$, $R_L = 350\ \Omega$		2	5	mA
	Isolation Resistance	R_{i-O}	$V_{i-O} = 1\text{ kV}_{DC}$, $R_H = 40$ to 60% , $T_A = 25^\circ\text{C}$	10^{11}			Ω
	Insulation Resistance (Input-Input), (PS9817A-2)	R_{i-I}	$V_{i-I} = 1\text{ kV}_{DC}$, $R_H = 40$ to 60% , $T_A = 25^\circ\text{C}$	10^{10}			Ω
	Isolation Capacitance	C_{i-O}	$V = 0\text{ V}$, $f = 1\text{ MHz}$, $T_A = 25^\circ\text{C}$		0.6		pF
	Insulation Capacitance (Input-Input), (PS9817A-2)	C_{i-I}	$V = 0\text{ V}$, $f = 1\text{ MHz}$, $T_A = 25^\circ\text{C}$		0.3		pF
	Propagation Delay Time ($H \rightarrow L$) *3	t_{PHL}	$V_{CC} = 5.0\text{ V}$, $R_L = 350\ \Omega$, $I_F = 7.5\text{ mA}$	$T_A = 25^\circ\text{C}$	40	75	ns
						100	
	Propagation Delay Time ($L \rightarrow H$) *3	t_{PLH}		$T_A = 25^\circ\text{C}$	45	75	
						100	
	Rise Time	t_r			20		
	Fall Time	t_f			5		
	Pulse Width Distortion (PWD) *3	$ t_{PHL} - t_{PLH} $			5	35	
	Propagation Delay Skew	t_{PSK}				40	
	Common Mode Transient Immunity at High Level Output *4	CM_H	$V_{CC} = 5.0\text{ V}$, $R_L = 350\ \Omega$, $T_A = 25^\circ\text{C}$, $I_F = 0\text{ mA}$, $V_O > 2\text{ V}$, $V_{CM} = 1\text{ kV}$	15	20		kV/ μs
	Common Mode Transient Immunity at Low Level Output *4	CM_L	$V_{CC} = 5.0\text{ V}$, $R_L = 350\ \Omega$, $T_A = 25^\circ\text{C}$, $I_F = 7.5\text{ mA}$, $V_O < 0.8\text{ V}$, $V_{CM} = 1\text{ kV}$	15	20		

*1 Typical values at $T_A = 25^\circ\text{C}$

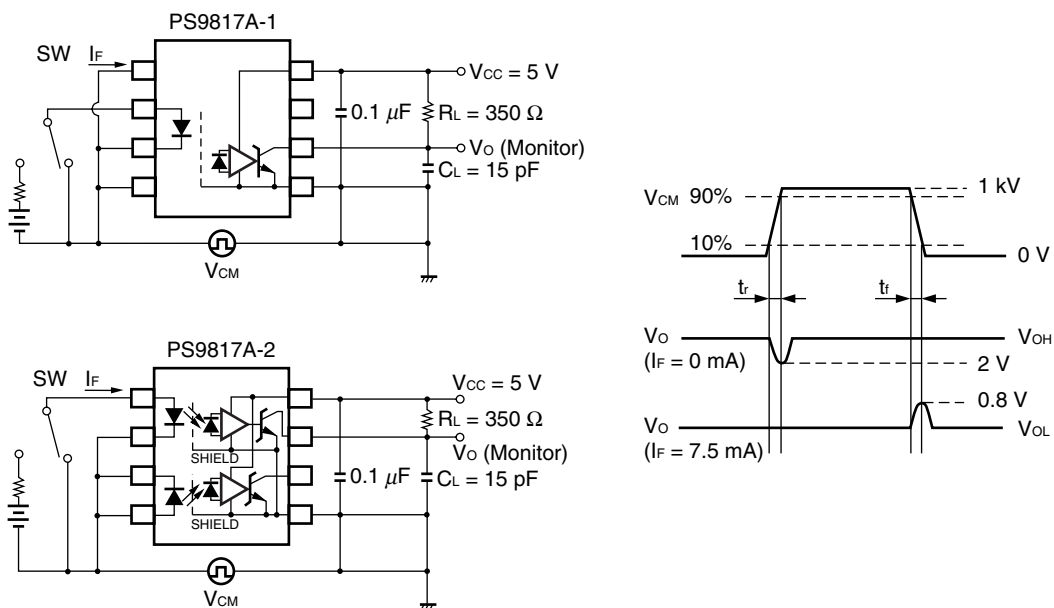
*2 Because V_{OL} of 2 V or more may be output when LED current input and when output supply of $V_{CC} = 2.6\text{ V}$ or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.

***3 Test circuit for propagation delay time**



Remark C_L includes probe and stray wiring capacitance.

***4 Test circuit for common mode transient immunity**

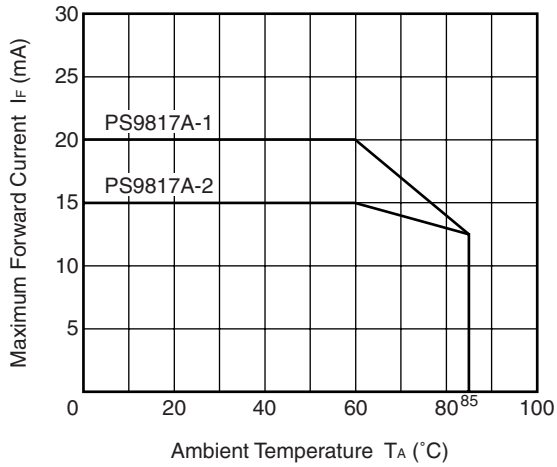


USAGE CAUTIONS

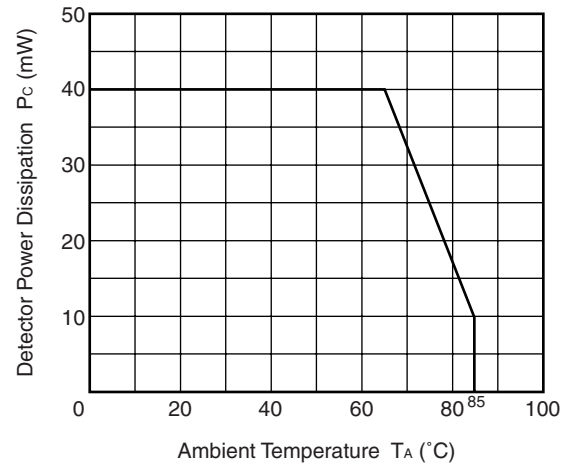
1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
2. By-pass capacitor of $0.1\mu F$ is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm .
3. Avoid storage at a high temperature and high humidity.

<R> **TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise specified)**

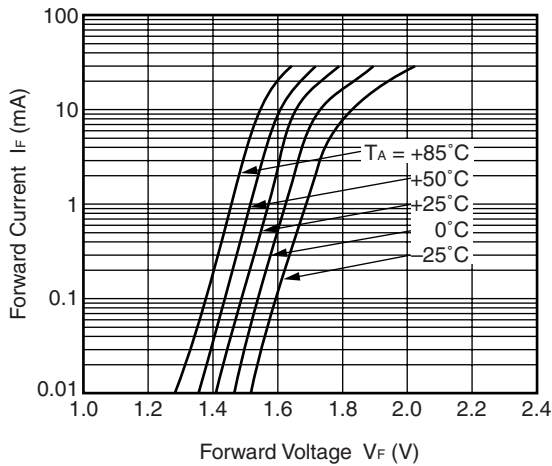
MAXIMUM FORWARD CURRENT vs. AMBIENT TEMPERATURE



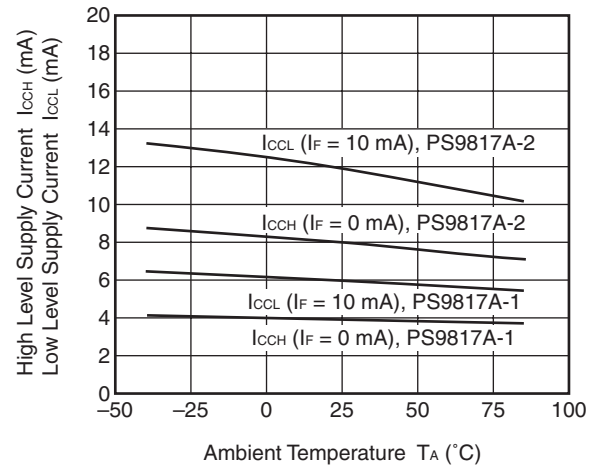
DETECTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



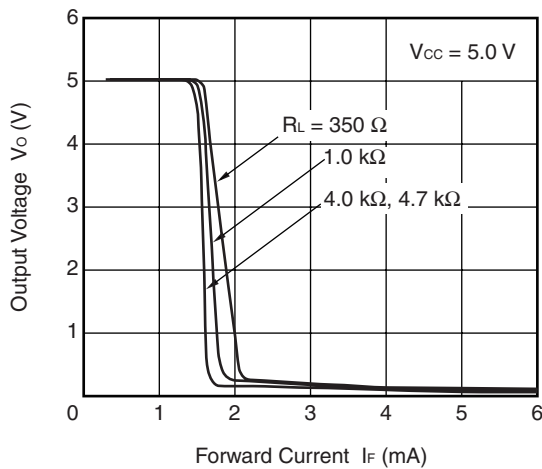
FORWARD CURRENT vs. FORWARD VOLTAGE



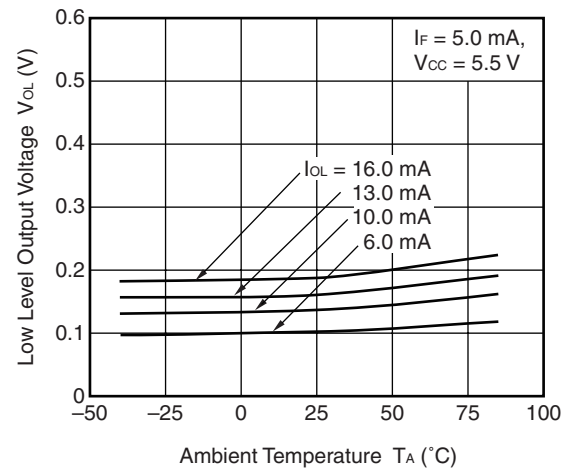
SUPPLY CURRENT vs. AMBIENT TEMPERATURE



OUTPUT VOLTAGE vs. FORWARD CURRENT

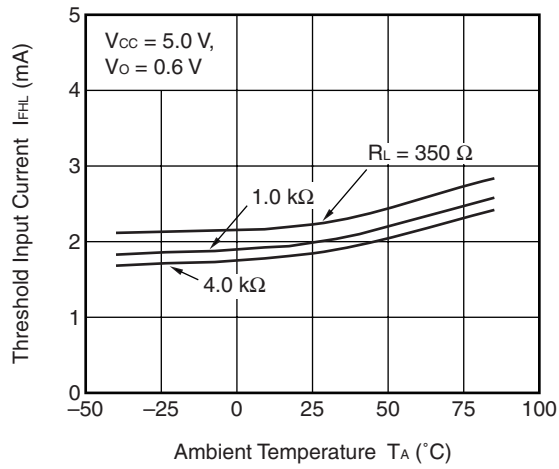


LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE

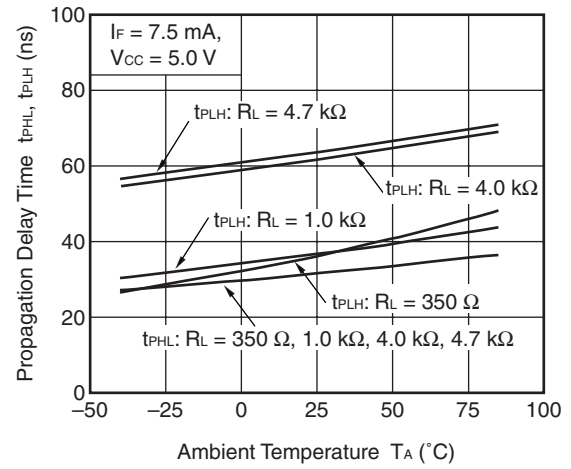


Remark The graphs indicate nominal characteristics.

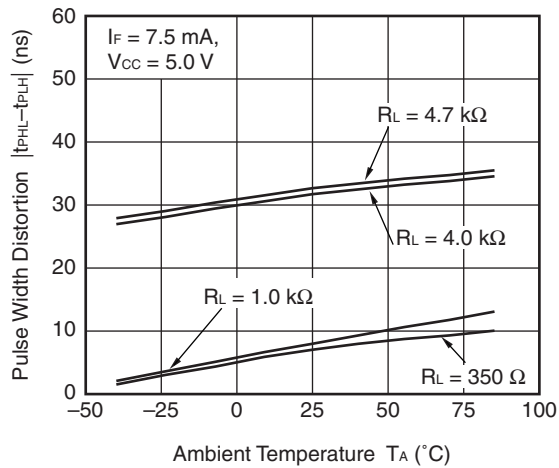
THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE



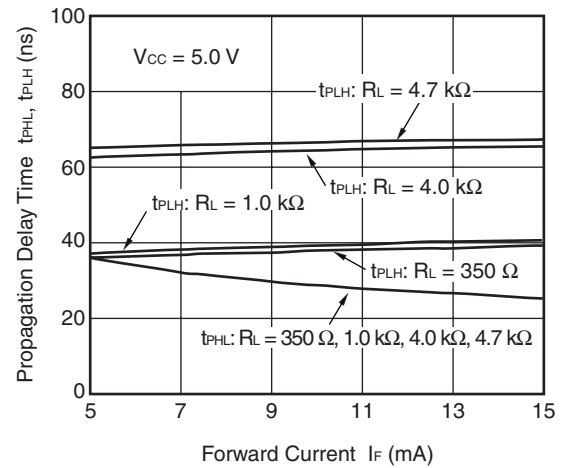
PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE



PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE



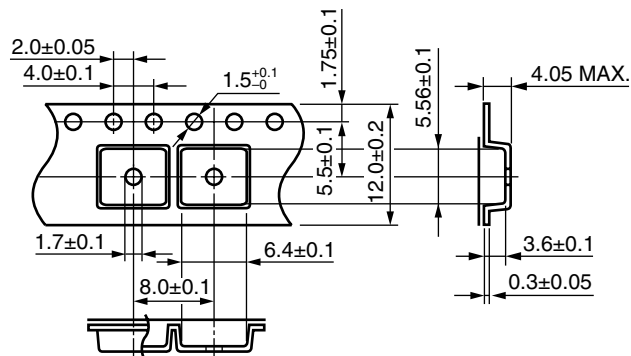
PROPAGATION DELAY TIME vs. FORWARD CURRENT



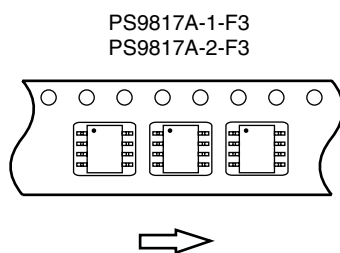
Remark The graphs indicate nominal characteristics.

TAPING SPECIFICATIONS (UNIT: mm)

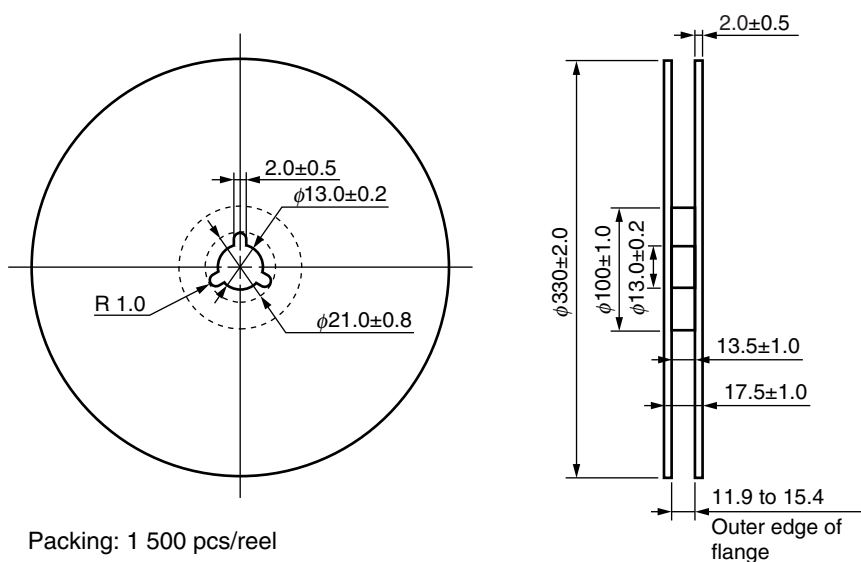
Outline and Dimensions (Tape)



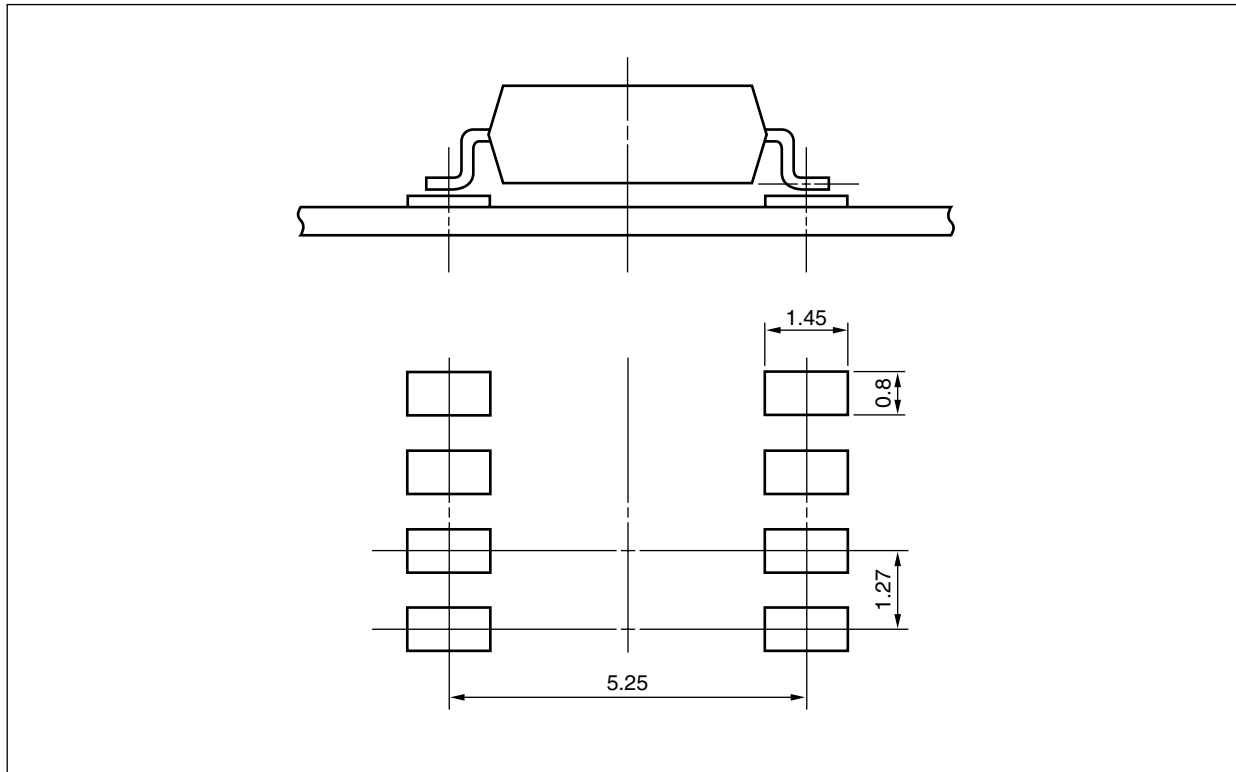
Taping Direction



Outline and Dimensions (Reel)



RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



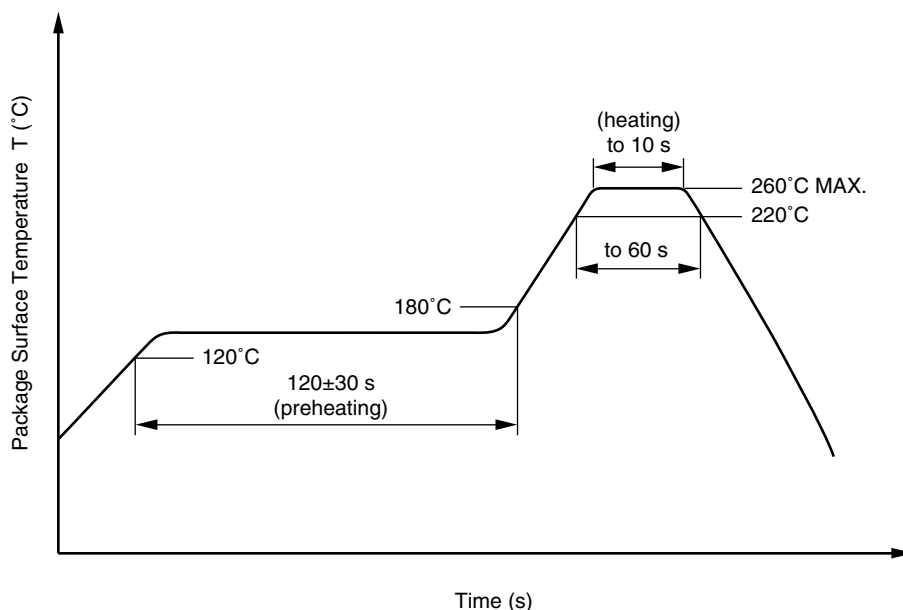
NOTES ON HANDLING

1. Recommended soldering conditions

(1) Infrared reflow soldering

- Peak reflow temperature 260°C or below (package surface temperature)
- Time of peak reflow temperature 10 seconds or less
- Time of temperature higher than 220°C 60 seconds or less
- Time to preheat temperature from 120 to 180°C 120±30 s
- Number of reflows Three
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



(2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

- Peak Temperature (lead part temperature) 350°C or below
- Time (each pins) 3 seconds or less
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over 100°C

(4) Cautions

- Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

USAGE CAUTIONS

1. Protect against static electricity when handling.
2. Avoid storage at a high temperature and high humidity.

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M8E 02.11-1

<div>Caution</div>	GaAs Products	<p>This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.</p> <ul style="list-style-type: none"> • Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below. <ol style="list-style-type: none"> 1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials. 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal. <ul style="list-style-type: none"> • Do not burn, destroy, cut, crush, or chemically dissolve the product. • Do not lick the product or in any way allow it to enter the mouth.
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