

FEATURES

- **HIGH ISOLATION VOLTAGE**
BV: 3.75 k V_{r.m.s.} MIN
- **SOP (SMALL OUT-LINE PACKAGE)**
- **ISOLATED CHANNELS PER EACH PACKAGE**
- **HIGH COLLECTOR TO EMITTER VOLTAGE**
V_{CEO}: 120 V MIN
- **HIGH SPEED SWITCHING**
t_r, t_f = 10 μs TYP
- **LOW COLLECTOR DARK CURRENT**
I_{CEO}: 5 nA TYP @ T_A = 25 °C, V_{CE} = 40 V
- **TAPE AND REEL AVAILABLE**

DESCRIPTION

PS2703-1, -2, and -4 series are optically coupled isolators containing a GaAs light emitting diode and a NPN silicon phototransistor. Each is mounted in a plastic SOP (Small Out-line Package) for high density applications. This package has a shield effect to cut off ambient light.

APPLICATIONS

Interface circuit for various instrumentations and control equipment.

- **AC LINE/DIGITAL LOGIC**
- **DIGITAL LOGIC INTERFACE**
- **TWISTED PAIR LINE RECEIVER**
- **TELEPHONE/TELEGRAPH LINE RECEIVER**
- **HIGH FREQUENCY POWER SUPPLY FEEDBACK CONTROL**
- **RELAY CONTACT MONITOR**
- **POWER SUPPLY MONITOR**

ELECTRICAL CHARACTERISTICS (T_A = 25°C)

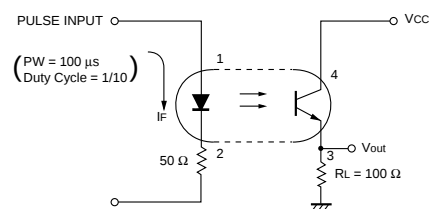
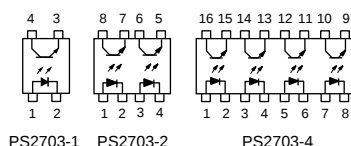
PART NUMBER			PS2703-1, -2, -4		
SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
Diode	V _F	Forward Voltage, I _F = 5 mA		1.1	1.4
	I _R	Reverse Current, V _R = 5 V			5
	C	Junction Capacitance, V = 0, f = 1.0 MHz		30	
Transistor	I _{CEO}	Collector to Emitter Dark Current, V _{CE} = 120 V, I _F = 0			100
Coupled	CTR	Current Transfer Ratio ¹ , I _F = 5 mA, V _{CE} = 5 V	50	150	400
		I _F = 1 mA, V _{CE} = 5 V	10	80	
	V _{CE (sat)}	Collector Saturation Voltage, I _F = 10 mA, I _C = 2 mA			0.3
	R ₁₋₂	Isolation Resistance, V _{in-out} = 1.0 k V _{DC}	10 ¹¹		
	C ₁₋₂	Isolation Capacitance, V = 0, f = 1.0 MHz		0.4	
	t _r	Rise Time ² , V _{CC} = 5 V, I _C = 2 mA, R _L = 1 k Ω		10	
	t _f	Fall Time ² , V _{CC} = 5 V, I _C = 2 mA, R _L = 1 k Ω		10	

Notes:

1. CTR rank (PS2703-1 only)

2. Test Circuit for Switching

CTR Rank	CTR (%)	Conditions
K	200 to 400	I _F = 5 mA, V _{CE} = 5 V
	80 to	I _F = 1 mA, V _{CE} = 5 V
L	100 to 300	I _F = 5 mA, V _{CE} = 5 V
	25 to	I _F = 1 mA, V _{CE} = 5 V
M	50 to 150	I _F = 5 mA, V _{CE} = 5 V
	10 to	I _F = 1 mA, V _{CE} = 5 V

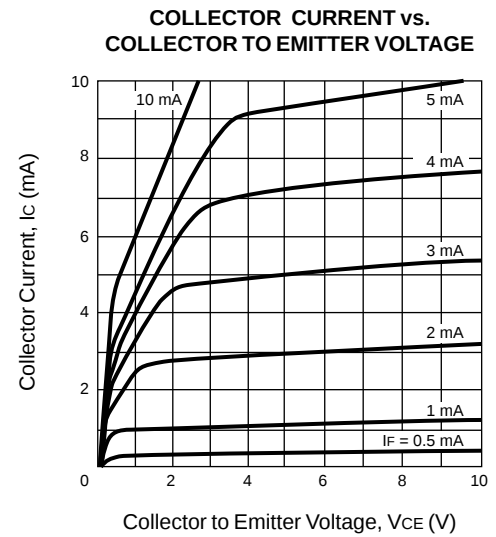
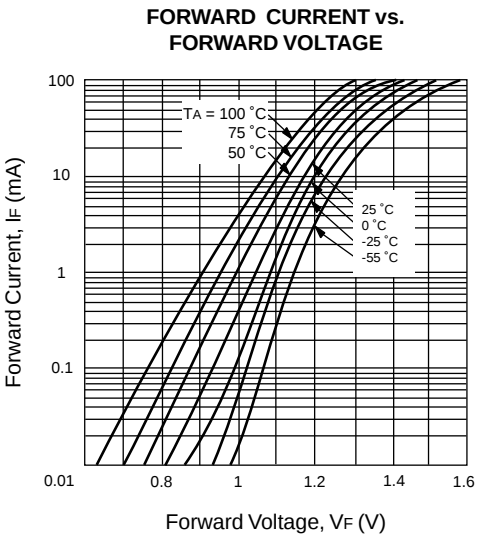
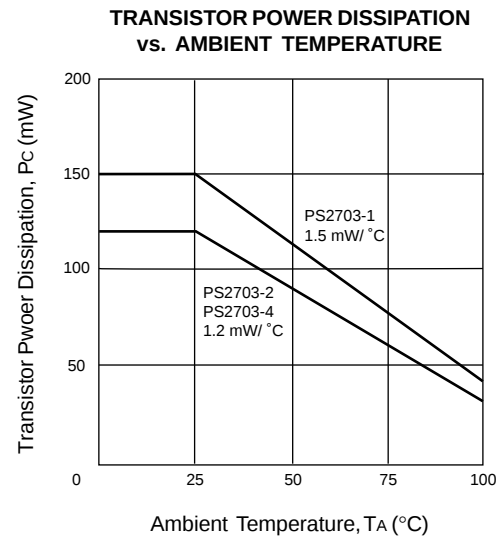
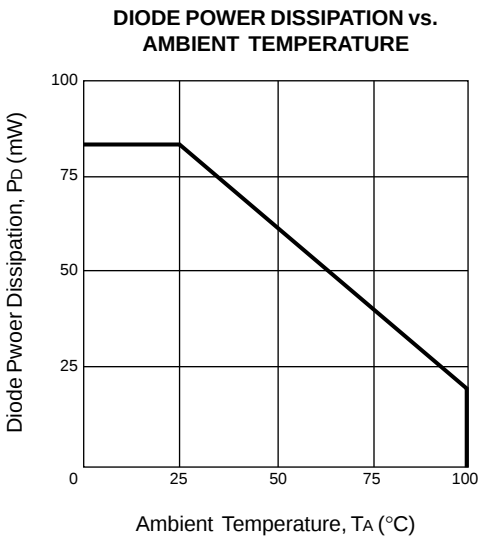


ABSOLUTE MAXIMUM RATINGS¹ (T_A = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS	
			PS2703-1	PS2703-2 PS2703-4
Diode				
I _F	Forward Current (DC)	mA	50	50
V _R	Reverse Voltage	V	6	6
P _D	Power Dissipation	mW/Ch	80	80
I _F (PEAK)	Peak Forward Current (P _W = 100 μs, Duty Cycle 1%)	A	1	1
Transistor				
V _{CEO}	Collector to Emitter Voltage (I _C = I _m A, I _B = 0)	V	120	120
V _{ECO}	Emitter to Collector Voltage (I _E = 100μA, I _B = 0)	V	6	6
I _C	Collector Current	mA/Ch	30	30
P _D	Power Dissipation	mW/Ch	150	120
Coupled				
BV	Isolation Voltage ²	V _{r.m.s.}	3750	3750
T _{STG}	Storage Temperature	°C	-55 to +150	-55 to +150
T _{OP}	Operating Temperature	°C	-55 to +100	-55 to +100

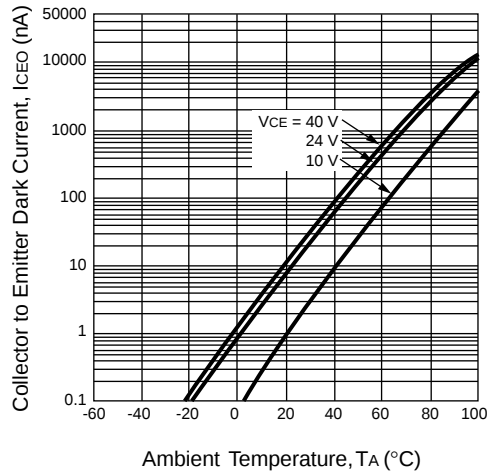
- Notes:
1. Operation in excess of any one of these parameters may result in permanent damage.
 2. AC voltage for 1 minute at T_A = 25 °C, RH = 60 % between input and output.

TYPICAL PERFORMANCE CURVES (T_A = 25 °C)

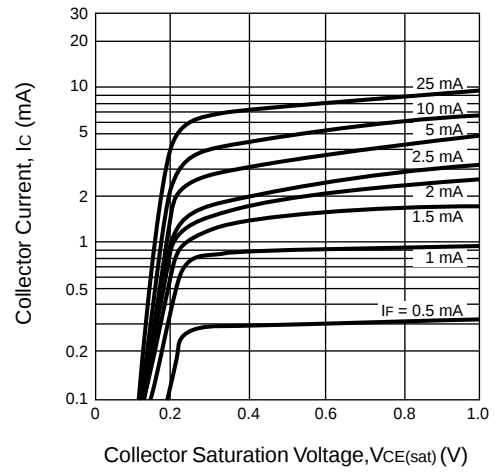


TYPICAL PERFORMANCE CURVES ($T_A = 25^\circ$)

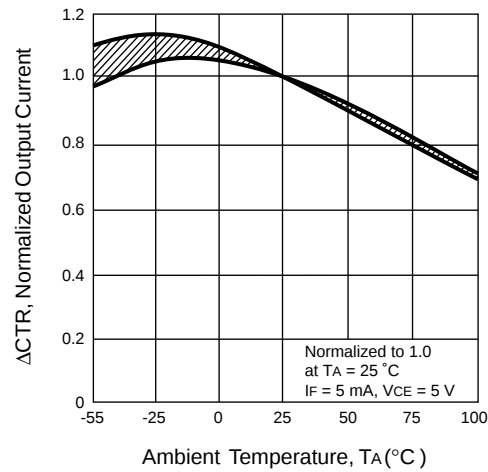
COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE



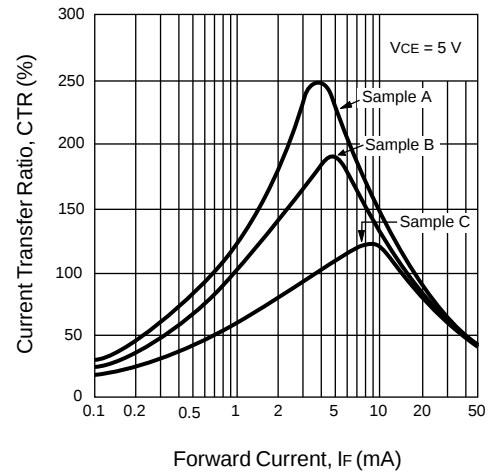
COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE



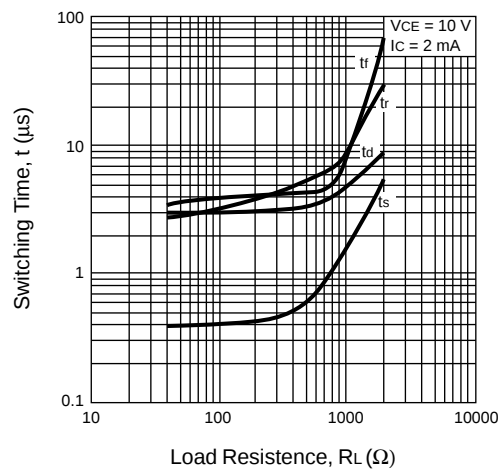
NORMALIZED OUTPUT CURRENT vs. AMBIENT TEMPERATURE



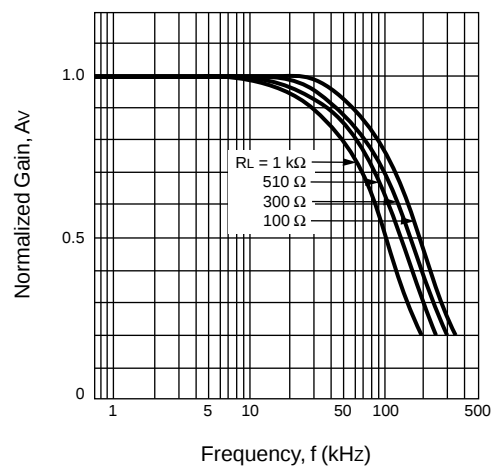
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



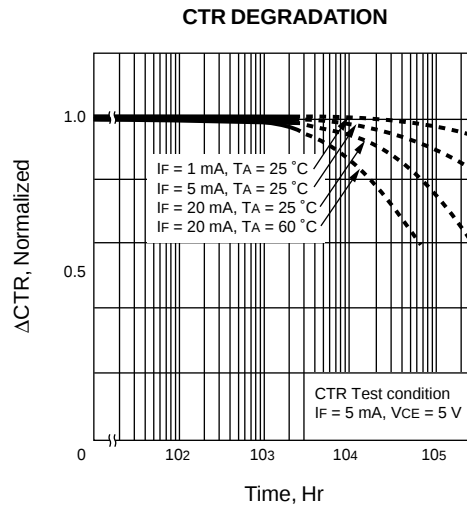
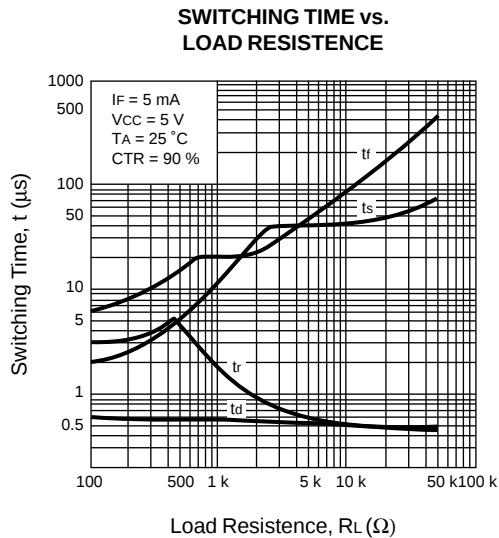
SWITCHING TIME vs. LOAD RESISTANCE



FREQUENCY RESPONSE

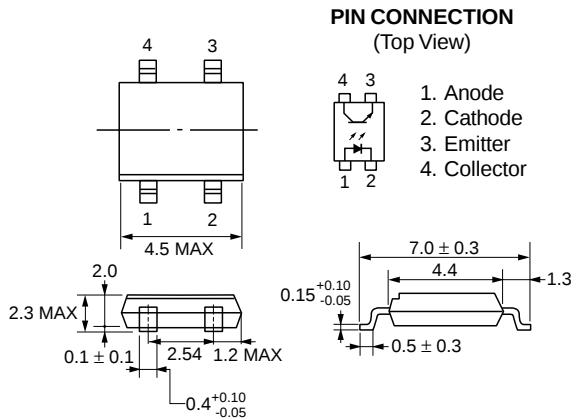


TYPICAL PERFORMANCE CURVES (T_A = 25 °)

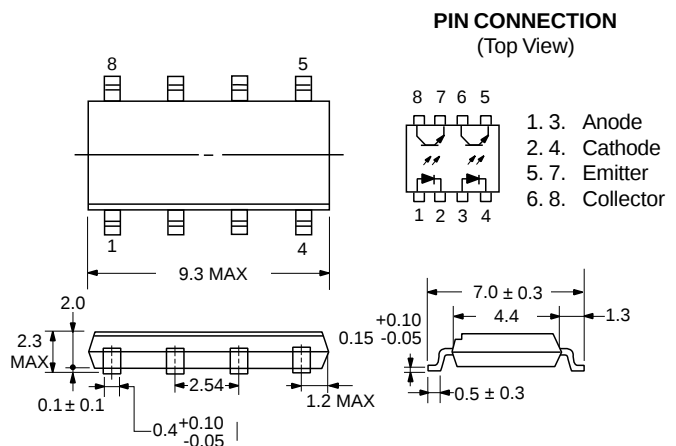


OUTLINE DIMENSIONS (Units in mm)

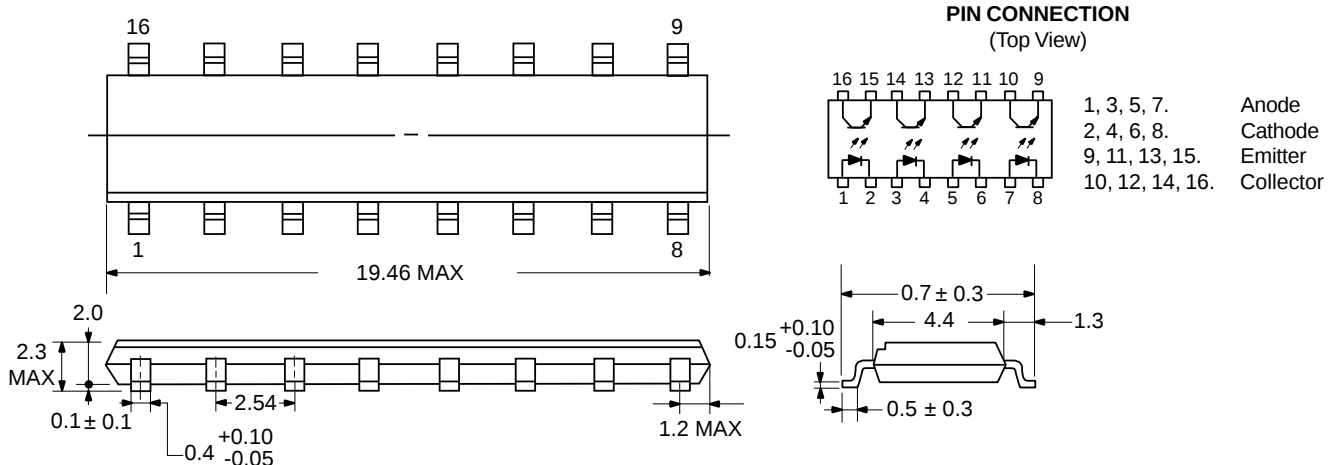
PS2703-1



PS2703-2



PS2703-4



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