



Parameter	Rating	Units
AC Operating Voltage	120	V _{rms}
Load Current	1	A _{rms}
On-State Voltage Drop (I _L =1A _{rms})	1.2	V _{rms}

Features

- Load Current up to 1A_{rms} (3A_{rms} with heat sink)
- Blocking Voltages up to 400V_P
- 5mA Sensitivity
- Zero-Crossing Detection
- DC Control, AC Output
- Optically Isolated
- TTL and CMOS Compatible
- Low EMI and RFI Generation
- High Noise Immunity
- VDE compatible
- Machine Insertable, Wave Solderable

Applications

- Programmable Control
- Process Control
- Power Control Panels
- Remote Switching
- Gas Pump Electronics
- Contactors
- Large Relays
- Solenoids
- Motors
- Heaters

Description

The PS1201 is an AC Solid State Switch using optical coupling with dual power SCR outputs to produce an alternative to optocoupler and Triac circuits. The PS1201 switches are robust enough to provide a blocking voltage of up to 400V_P and max surge current rating of 20A. In addition, tightly controlled zero-cross circuitry ensures switching of AC loads without the generation of transients. The input and output circuits are optically coupled to provide 3750V_{rms} of isolation and noise immunity between control and load circuits. As a result the PS1201 is well suited for industrial environments where electromagnetic interference would disrupt the operation of electromechanical relays.

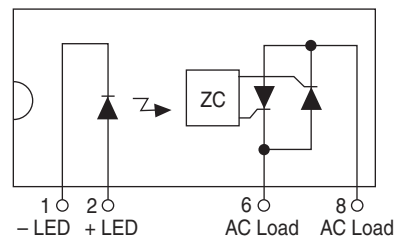
Approvals

- UL Recognized Component: UL 508, File # E69938
- CSA Certified Component: Certificate # LR43639-8

Ordering Information

Part #	Description
PS1201	4-Lead, 8-Pin, SIP Package (25/Tube)

Pin Configuration



Absolute Maximum Ratings (@ 25°C)

Parameter	Min	Max	Units
Blocking Voltage	-	400	V_P
Reverse Input Voltage	-	5	V
Input Control Current	-	100	mA
Peak (10ms)	-	1	A
Input Power Dissipation ¹	-	150	mW
Total Package Dissipation ²	-	1600	mW
Isolation Voltage, Input to Output	3750	-	V_{rms}
Operational Temperature	-40	+85	°C
Storage Temperature	-40	+125	°C

¹ Derate Linearly 1.33 mW / °C

² Derate Linearly 16.6 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

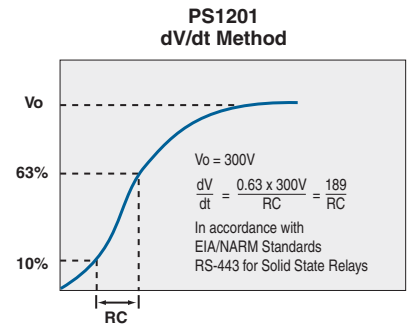
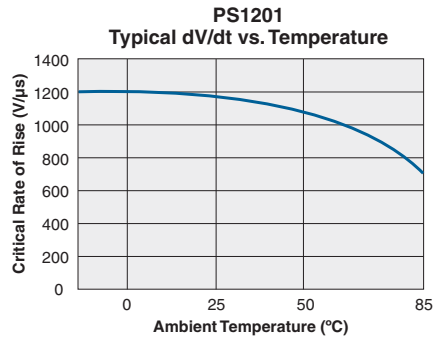
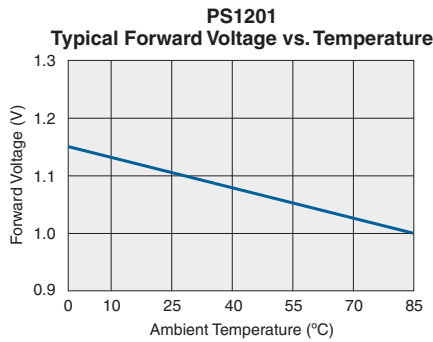
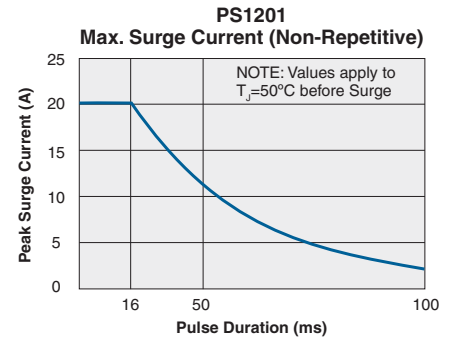
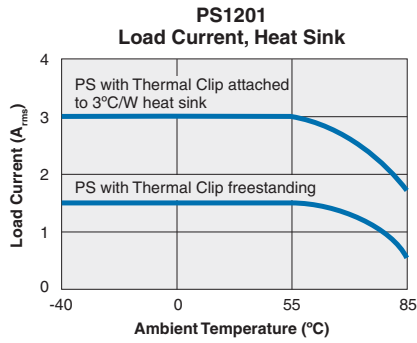
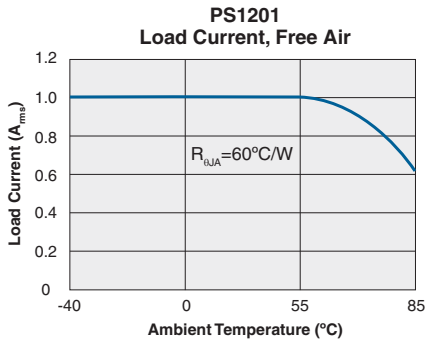
Electrical Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics @ 25°C						
AC Operating Voltage	$I_F=5mA$	V_{OP}	20	-	120	V_{rms}
Load Current (Continuous)	$V_L=120-240VAC$	I_L	0.005	-	1	A_{rms}
Maximum Surge Current	$t \leq 16ms$	I_{PEAK}	-	-	20	A
Off-State Leakage Current	$V_L=400V_{DC}$	I_{LEAK}	-	-	1	mA
On-State Voltage Drop	$I_L=1A_{rms}$	-	-	-	1.2	V_{rms}
Critical Rate of Rise	-	dV/dt	1000	1200	-	V/ μs
Switching Speeds						
Turn-On	$I_F=5mA$	T_{ON}	-	-	0.5	Cycles
Turn-Off		T_{OFF}	-	-	0.5	
Zero-Cross Turn-On Voltage	1 st Half-Cycle	-	-	2	5	V
Sub Half-Cycle		-	-	-	1	V
Operating Frequency ¹	-	-	20	-	500	Hz
Load Power Factor for Guaranteed Turn-On ²	-	PF	0.25	-	-	-
Capacitance Input-To-Output	-	C_{IO}	-	3	-	pF
Input Characteristics @ 25°C						
Input Control Current						
For Normal Environment	-	I_F	-	-	5	mA
For High Noise Environment	-		-	-	10	
Input Voltage Drop	$I_F=5mA$	V_F	0.9	1.2	1.4	V
Input Dropout Voltage	-	-	0.8	-	-	V
Reverse Input Current	$V_R=5V$	I_R	-	-	10	μA

¹ Zero Cross 1st half-cycle @ < 100Hz.

² Snubber circuits may be required at low power factors.

PERFORMANCE DATA*



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

MANUFACTURING INFORMATION

Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

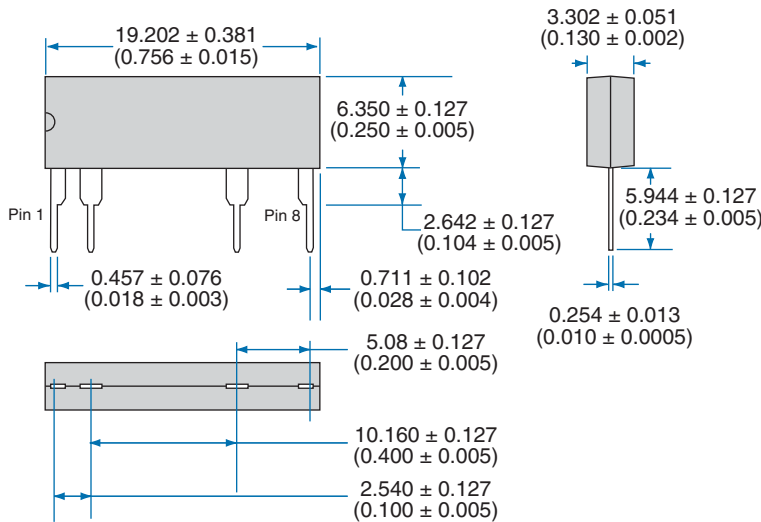
Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.

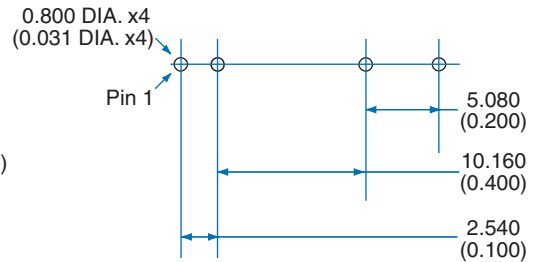


MECHANICAL DIMENSIONS

4-Lead, 8-Pin, SIP Package



Recommended PCB Hole Pattern



Dimensions
mm
(inches)

For additional information please visit our website at: www.clare.com

Clare, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in Clare's Standard Terms and Conditions of Sale, Clare, Inc. assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of Clare's product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. Clare, Inc. reserves the right to discontinue or make changes to its products at any time without notice.

Specification: DS-PS1201-R10
©Copyright 2008, Clare, Inc.
All rights reserved. Printed in USA.
4/24/08